

## N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a, e</sup>	Q <sub>g</sub> (Typ.)
30	0.0073 at V <sub>GS</sub> = 10 V	45	53 nC
	0.0102 at V <sub>GS</sub> = 4.5 V	40	

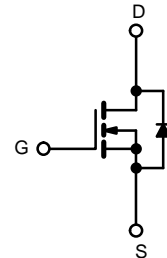
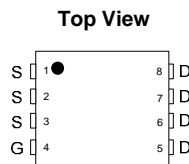
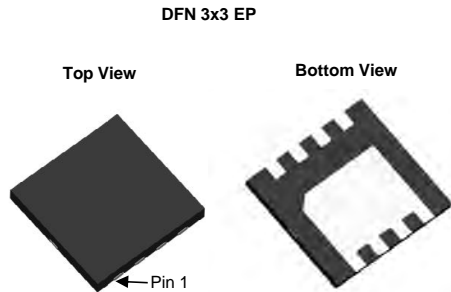
### FEATURES

- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested



### APPLICATIONS

- Notebook PC Core
- VRM/POL



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	30	V	
Gate-Source Voltage	V <sub>GS</sub>	± 20		
Continuous Drain Current (T <sub>J</sub> = 175 °C)	I <sub>D</sub>	T <sub>C</sub> = 25 °C	45 <sup>a, e</sup>	A
		T <sub>C</sub> = 70 °C	37 <sup>e</sup>	
		T <sub>A</sub> = 25 °C	21 <sup>b, c</sup>	
		T <sub>A</sub> = 70 °C	17.8 <sup>b, c</sup>	
Pulsed Drain Current	I <sub>DM</sub>	135		
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	26	mJ
Single Pulse Avalanche Energy		E <sub>AS</sub>	53.2	
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	45 <sup>a, e</sup>	A
		T <sub>A</sub> = 25 °C	21 <sup>b, c</sup>	
Maximum Power Dissipation	P <sub>D</sub>	T <sub>C</sub> = 25 °C	31.2	W
		T <sub>C</sub> = 70 °C	20	
		T <sub>A</sub> = 25 °C	3.55 <sup>b, c</sup>	
		T <sub>A</sub> = 70 °C	2.13 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	31	44	°C/W
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	3	4	

Notes:

- a. Based on T<sub>C</sub> = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. Maximum under steady state conditions is 90 °C/W.
- e. Calculated based on maximum junction temperature. Package limitation current is 80 A.

**SPECIFICATIONS** ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted)

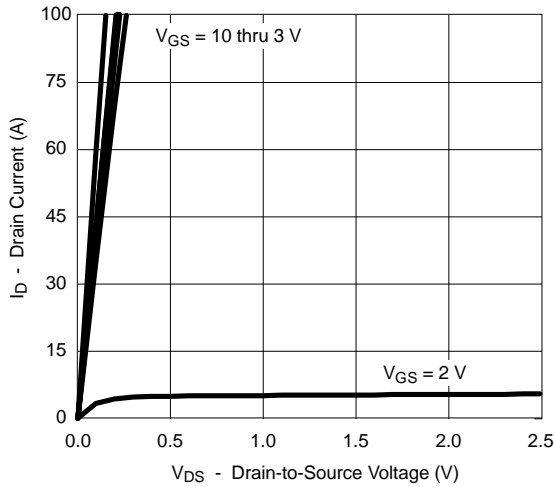
Parameter	Symbol	Test Conditions	Min .	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	30			V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$		35		mV/ $^\circ\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 5.5		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	0.5		1.5	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			10	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	45			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 12\text{ A}$		0.0073	0.0081	$\Omega$
		$V_{GS} = 4.5\text{ V}, I_D = 9\text{ A}$		0.0102	0.012	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 15\text{ V}, I_D = 12\text{ A}$		100		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 12.5\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		1520		pF
Output Capacitance	$C_{oss}$			1003		
Reverse Transfer Capacitance	$C_{rss}$			941		
Total Gate Charge	$Q_g$	$V_{DS} = 15\text{ V}, V_{GS} = 10\text{ V}, I_D = 12\text{ A}$		71		nC
		$V_{DS} = 15\text{ V}, V_{GS} = 4.5\text{ V}, I_D = 9\text{ A}$		61.5		
Gate-Source Charge	$Q_{gs}$			34		
Gate-Drain Charge	$Q_{gd}$		29			
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		1.4	2.1	$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15\text{ V}, R_L = 0.555\text{ }\Omega$ $I_D \cong 7\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		18	27	ns
Rise Time	$t_r$			11	17	
Turn-Off Delay Time	$t_{d(off)}$			70	105	
Fall Time	$t_f$			10	15	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15\text{ V}, R_L = 0.625\text{ }\Omega$ $I_D \cong 4\text{ A}, V_{GEN} = 4.5\text{ V}, R_g = 1\text{ }\Omega$		55	83	
Rise Time	$t_r$			180	270	
Turn-Off Delay Time	$t_{d(off)}$			55	83	
Fall Time	$t_f$			12	18	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$			45	A
Pulse Diode Forward Current <sup>a</sup>	$I_{SM}$				135	
Body Diode Voltage	$V_{SD}$	$I_S = 12\text{ A}$		0.8	1.2	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 10\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		52	78	ns
Body Diode Reverse Recovery Charge	$Q_{rr}$			70.2	105	nC
Reverse Recovery Fall Time	$t_a$			27		ns
Reverse Recovery Rise Time	$t_b$			25		

**Notes:**

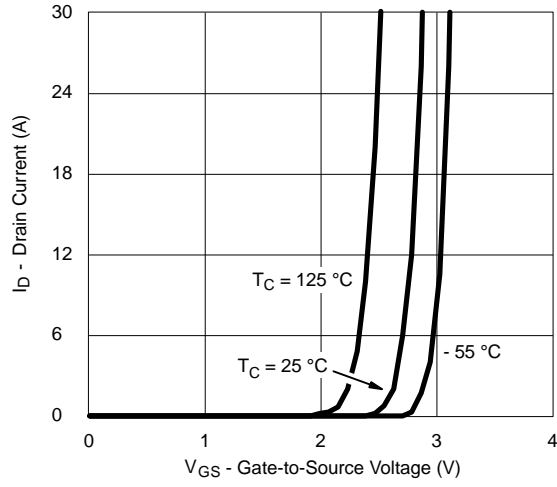
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

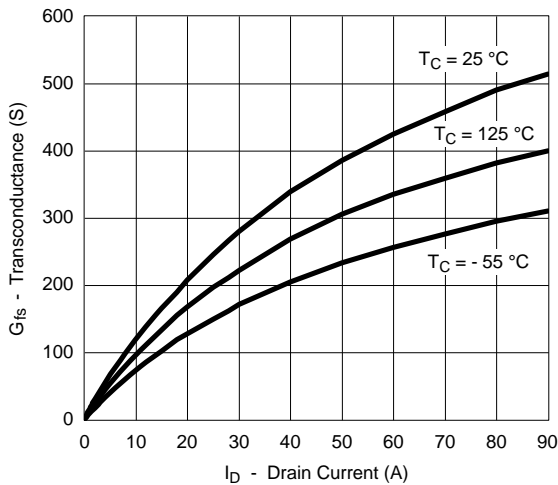
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



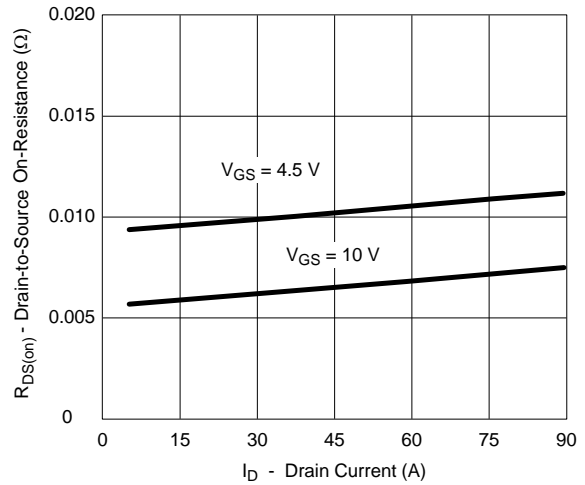
**Output Characteristics**



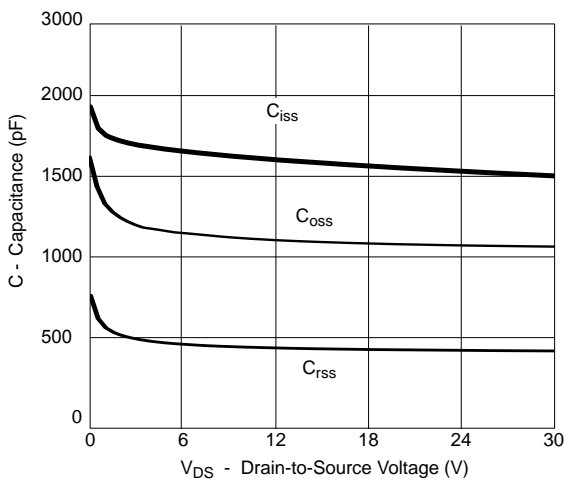
**Transfer Characteristics**



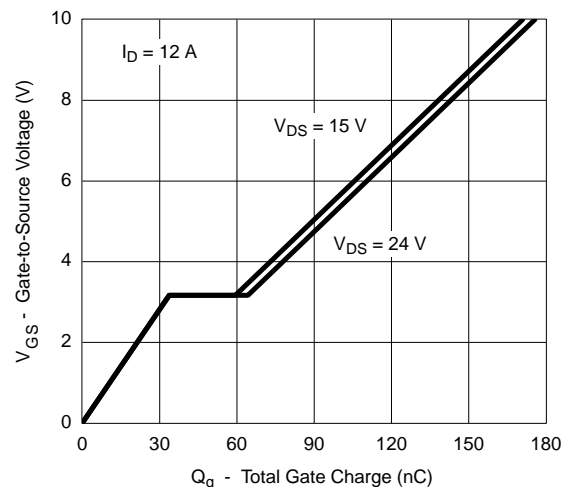
**Transconductance**



**$R_{DS(on)}$  vs. Drain Current**

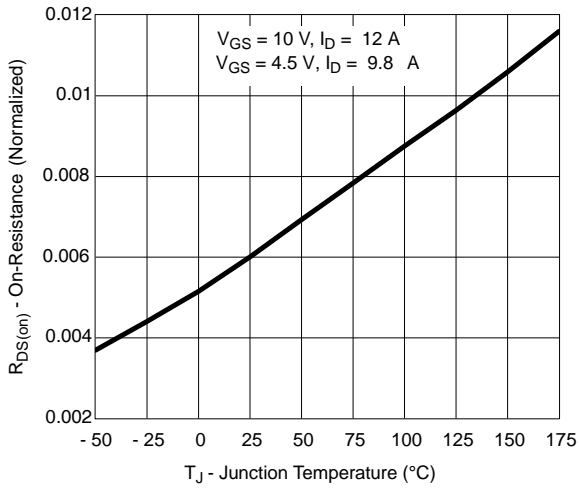


**Capacitance**

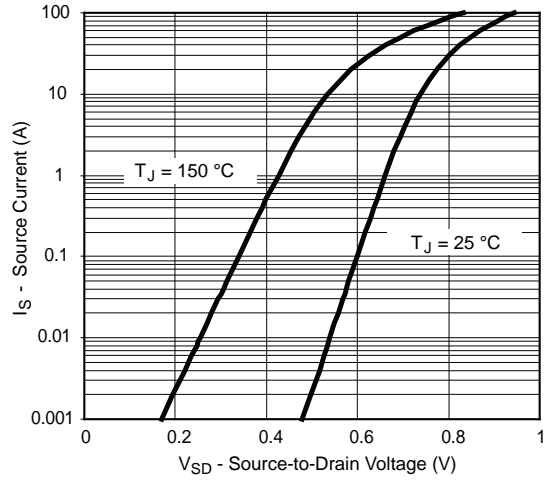


**Gate Charge**

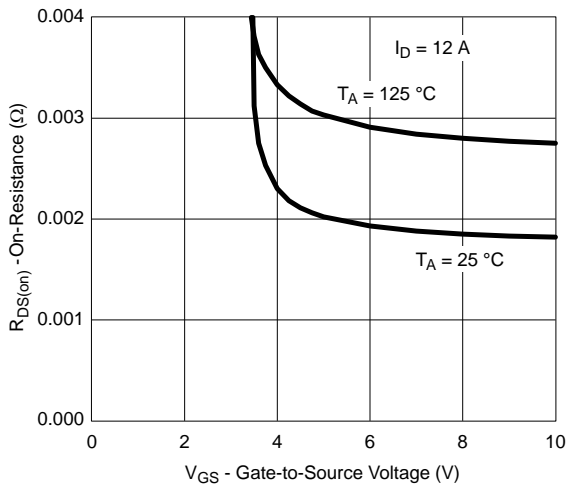
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



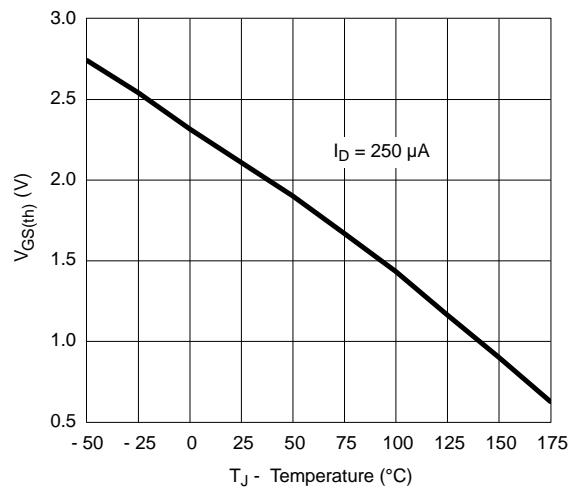
**On-Resistance vs. Junction Temperature**



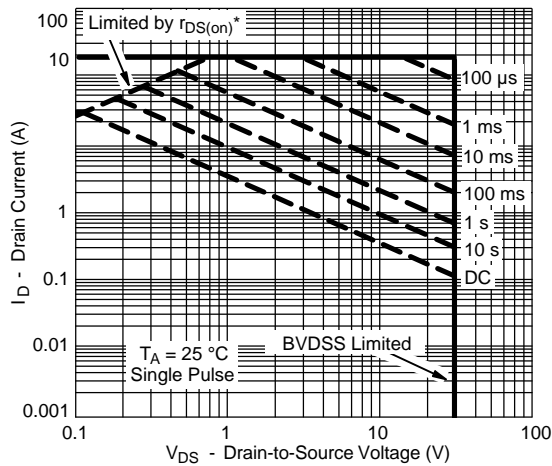
**Forward Diode Voltage vs. Temperature**



**R<sub>DS(on)</sub> vs. V<sub>GS</sub> vs. Temperature**

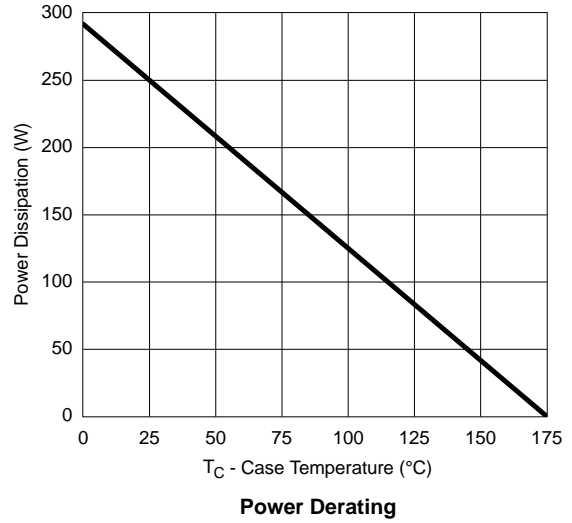
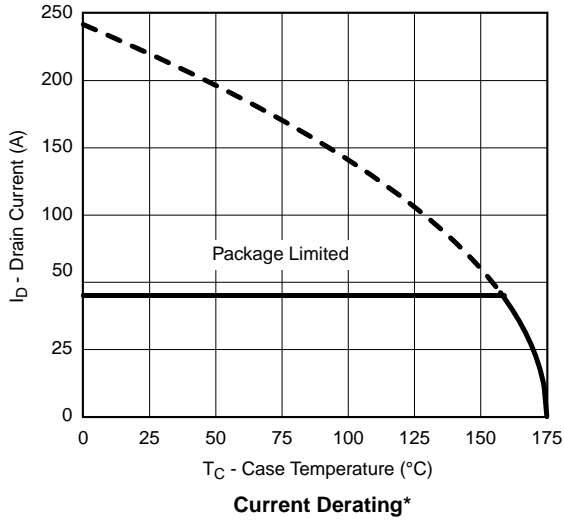


**Threshold Voltage**

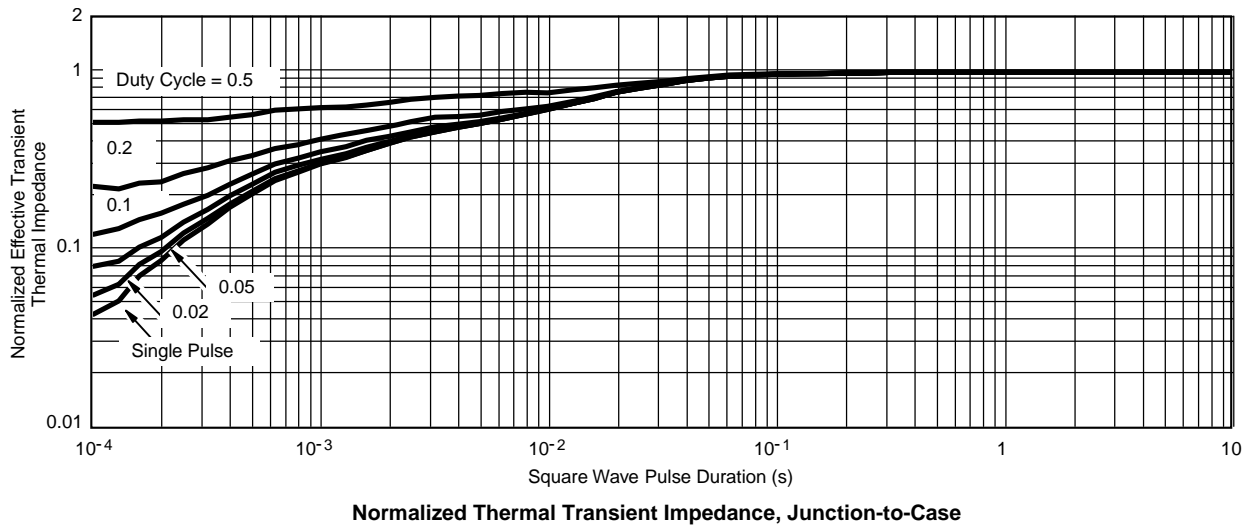


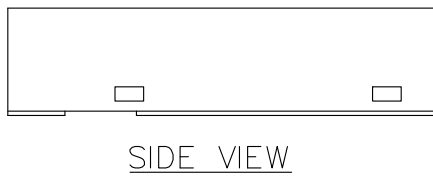
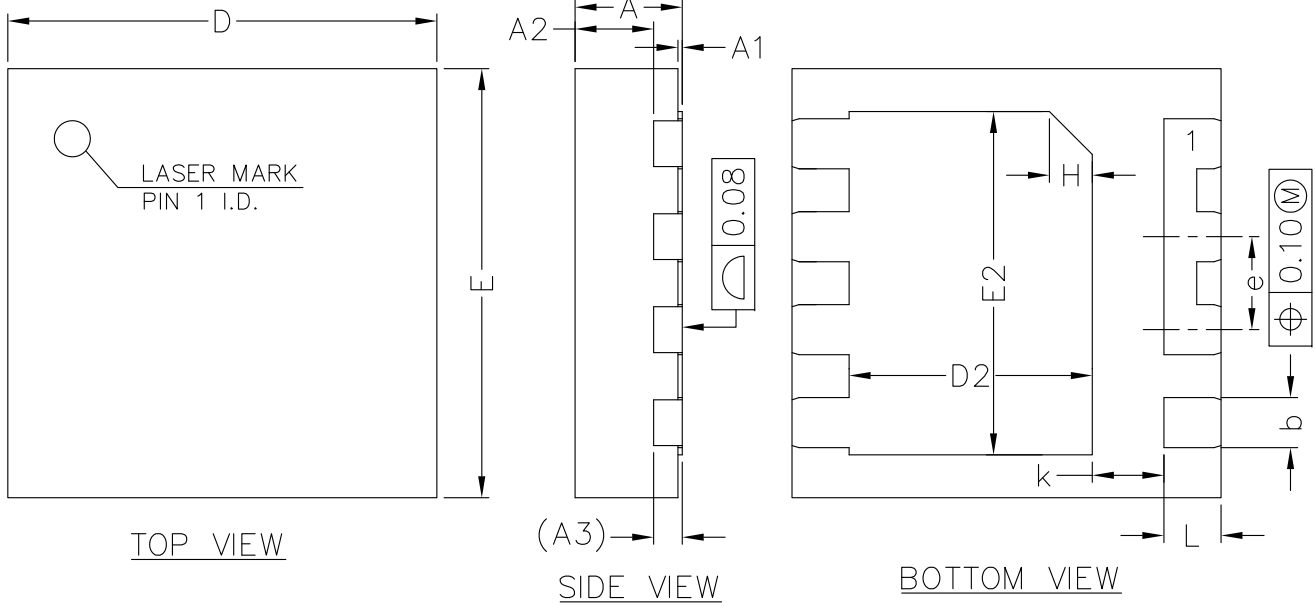
\*V<sub>GS</sub> > minimum V<sub>GS</sub> at which r<sub>DS(on)</sub> is specified  
**Safe Operating Area, Junction-to-Ambient**

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 175$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A2	0.50	0.55	0.60
A3	0.20REF		
b	0.30	0.35	0.40
D	2.90	3.00	3.10
E	2.90	3.00	3.10
D2	1.60	1.70	1.80
E2	2.30	2.40	2.50
e	0.55	0.65	0.75
K	0.40	0.50	0.60
L	0.35	0.40	0.45

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