

## N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY			
$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A) <sup>a, e</sup>	$Q_g$ (Typ.)
60	0.033 at $V_{GS} = 10$ V	25	53 nC
	0.039 at $V_{GS} = 4.5$ V	20	

### FEATURES

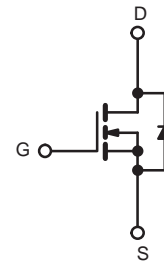
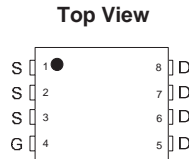
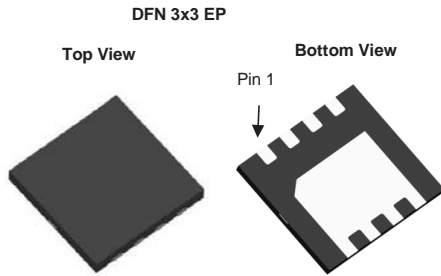
- TrenchFET<sup>®</sup> Power MOSFET
- 100 %  $R_g$  and UIS Tested



RoHS  
COMPLIANT

### APPLICATIONS

- Notebook PC Core
- VRM/POL



N-Channel MOSFET

### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 175$ °C)	$I_D$	$T_C = 25$ °C	A
		$T_C = 70$ °C	
		$T_A = 25$ °C	
		$T_A = 70$ °C	
Pulsed Drain Current	$I_{DM}$	100	
Avalanche Current Pulse	$I_{AS}$	20	mJ
Single Pulse Avalanche Energy			
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25$ °C	A
		$T_A = 25$ °C	
Maximum Power Dissipation	$P_D$	$T_C = 25$ °C	W
		$T_C = 70$ °C	
		$T_A = 25$ °C	
		$T_A = 70$ °C	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 175	°C

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b, d</sup>	$R_{thJA}$	38	50	°C/W
Maximum Junction-to-Case	$R_{thJC}$	3.5	5	

Notes:

- Based on  $T_C = 25$  °C.
- Surface mounted on 1" x 1" FR4 board.
- $t = 10$  s.
- Maximum under steady state conditions is 90 °C/W.
- Calculated based on maximum junction temperature. Package limitation current is 80 A.

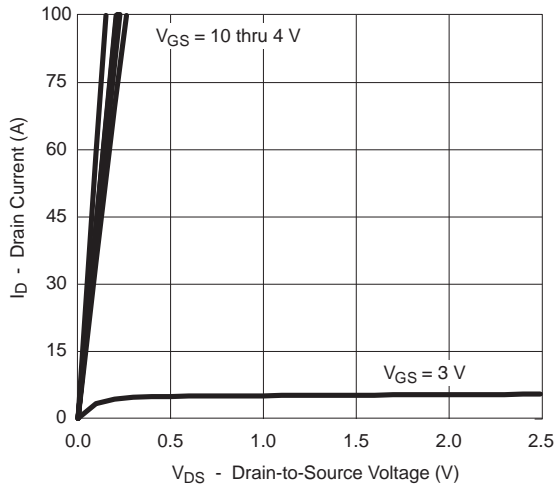
<b>SPECIFICATIONS</b> ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min .	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	60			V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$		35		mV/ $^\circ\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 5.5		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1.0		3.0	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 48\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 48\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			10	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	100			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 8\text{ A}$		0.033	0.037	$\Omega$
		$V_{GS} = 4.5\text{ V}, I_D = 8\text{ A}$		0.039	0.045	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 48\text{ V}, I_D = 8\text{ A}$		100		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		1355		pF
Output Capacitance	$C_{oss}$			898		
Reverse Transfer Capacitance	$C_{rss}$			536		
Total Gate Charge	$Q_g$	$V_{DS} = 25\text{ V}, V_{GS} = 10\text{ V}, I_D = 8\text{ A}$		53		nC
		$V_{DS} = 25\text{ V}, V_{GS} = 4.5\text{ V}, I_D = 8\text{ A}$		45		
Gate-Source Charge	$Q_{gs}$			30		
Gate-Drain Charge	$Q_{gd}$		19			
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		1.4	2.5	$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 48\text{ V}, R_L = 0.555\text{ }\Omega$ $I_D \cong 7\text{ A}, V_{GS} = 10\text{ V}, R_g = 1\text{ }\Omega$		17		ns
Rise Time	$t_r$			11		
Turn-Off Delay Time	$t_{d(off)}$			71		
Fall Time	$t_f$			10		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 48\text{ V}, R_L = 0.625\text{ }\Omega$ $I_D \cong 4\text{ A}, V_{GS} = 4.5\text{ V}, R_g = 1\text{ }\Omega$		50		
Rise Time	$t_r$			150		
Turn-Off Delay Time	$t_{d(off)}$			50		
Fall Time	$t_f$			10		
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$			25	A
Pulse Diode Forward Current <sup>a</sup>	$I_{SM}$				100	
Body Diode Voltage	$V_{SD}$	$I_S = 12\text{ A}$		0.7	1.2	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 10\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		52		ns
Body Diode Reverse Recovery Charge	$Q_{rr}$			65		nC
Reverse Recovery Fall Time	$t_a$			25		ns
Reverse Recovery Rise Time	$t_b$			23		

Notes:

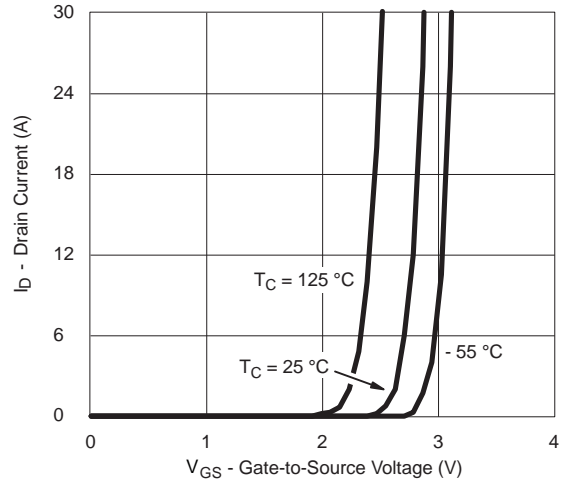
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

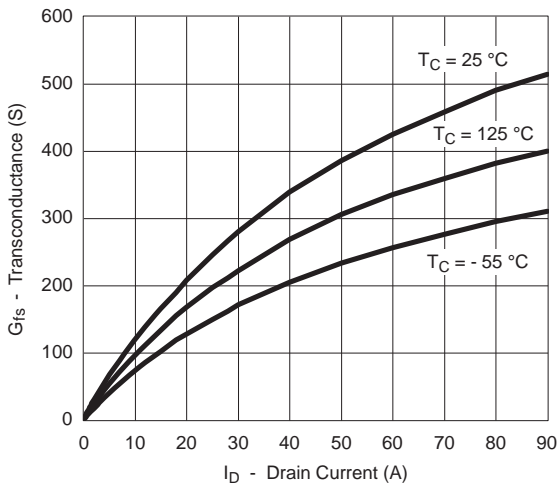
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



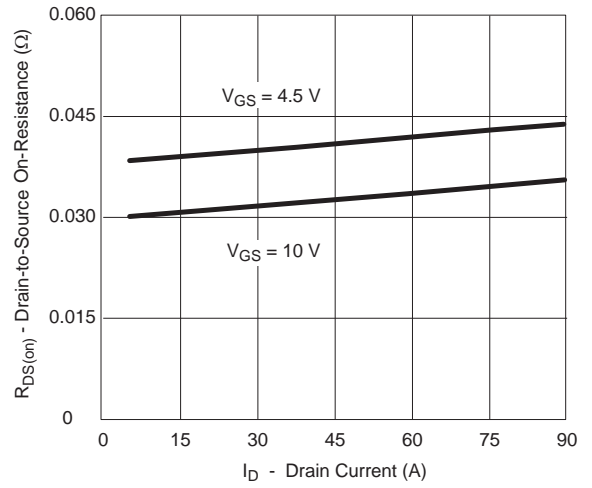
**Output Characteristics**



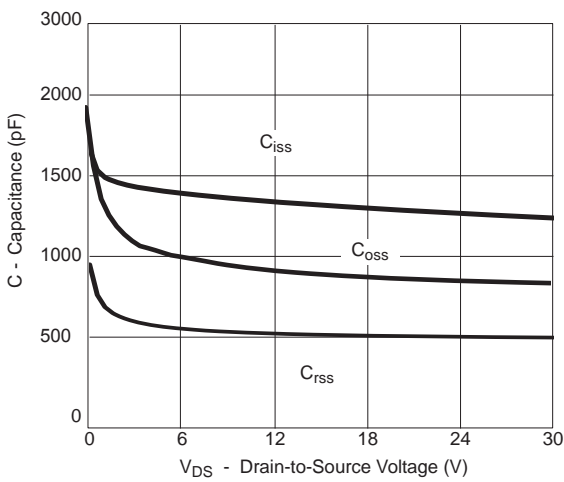
**Transfer Characteristics**



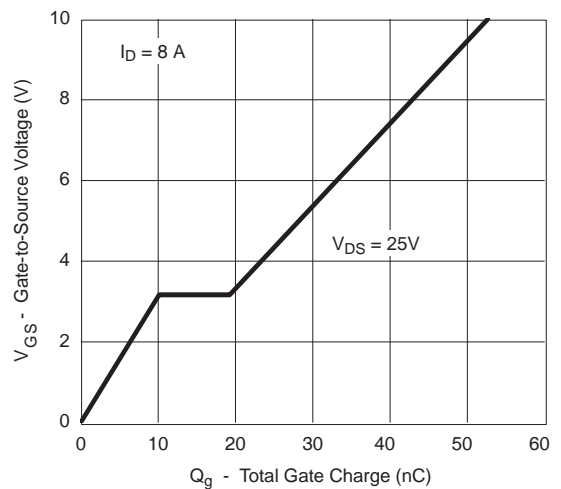
**Transconductance**



**$R_{DS(on)}$  vs. Drain Current**

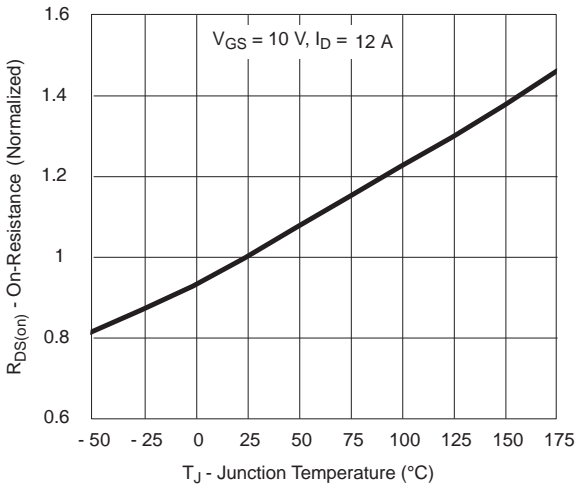


**Capacitance**

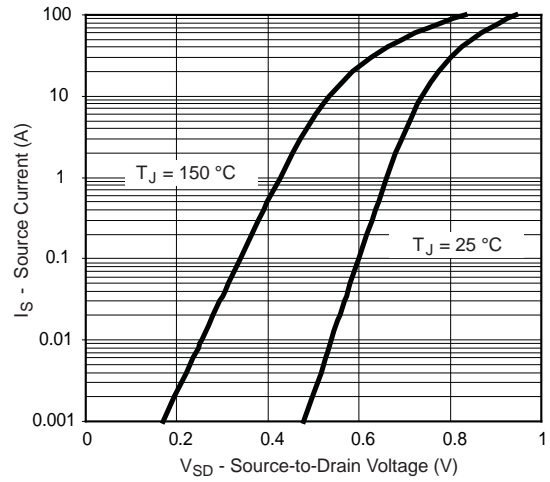


**Gate Charge**

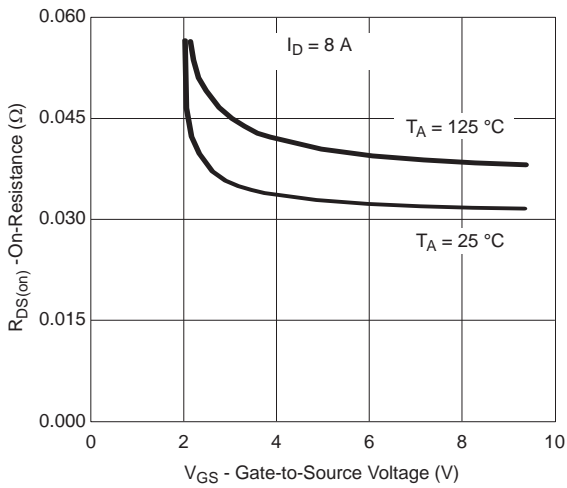
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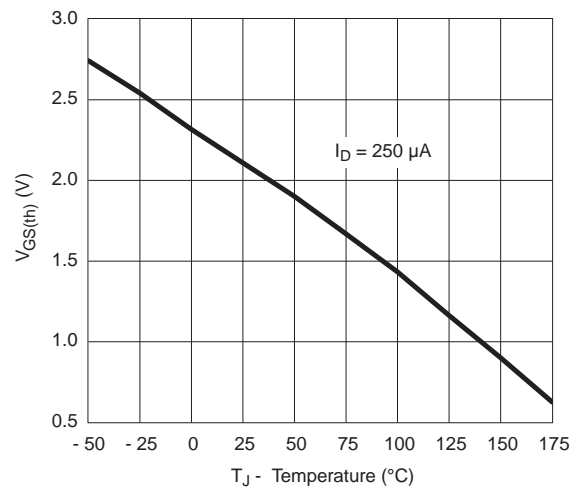
**On-Resistance vs. Junction Temperature**



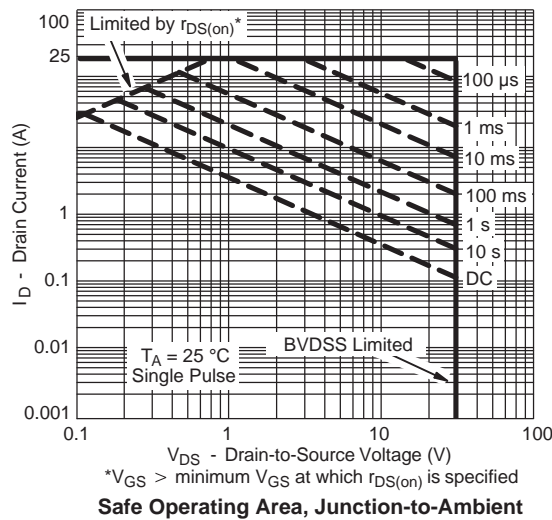
**Forward Diode Voltage vs. Temperature**



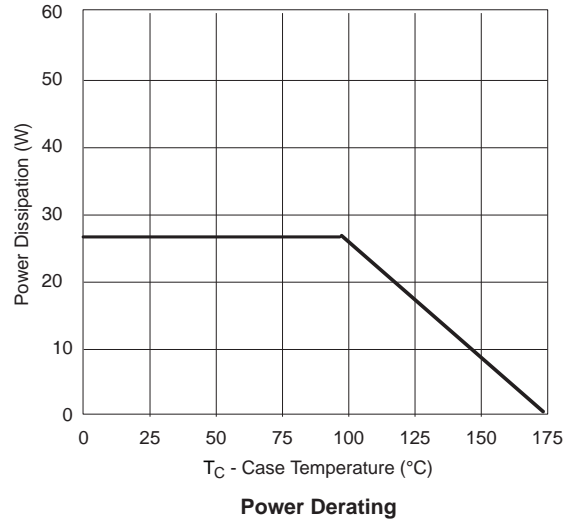
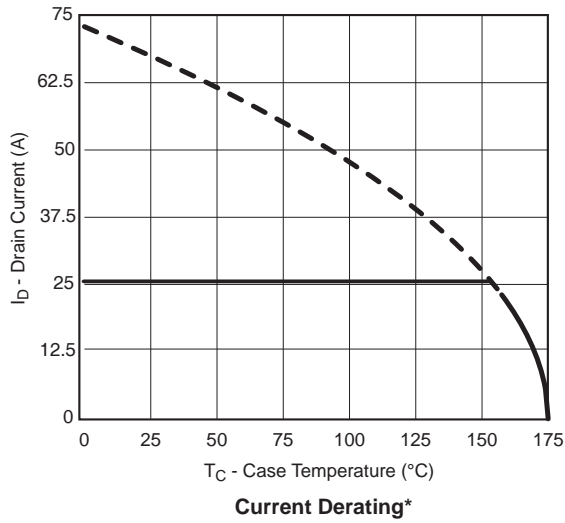
**R<sub>DS(on)</sub> vs. V<sub>GS</sub> vs. Temperature**



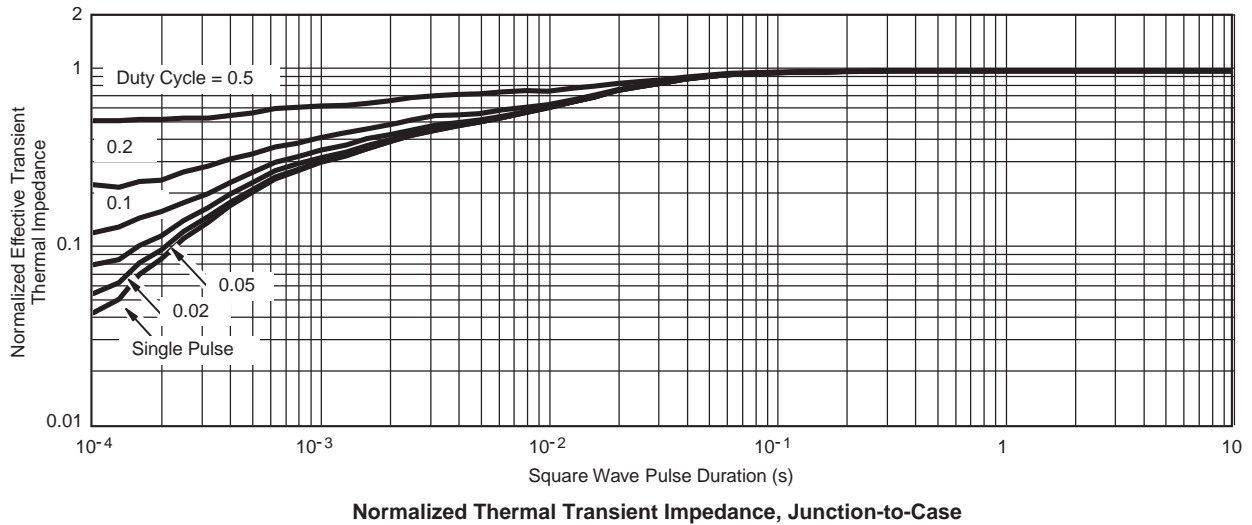
**Threshold Voltage**

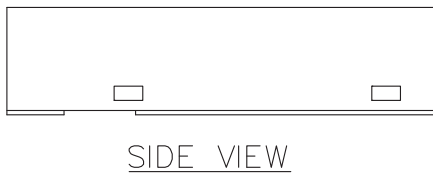
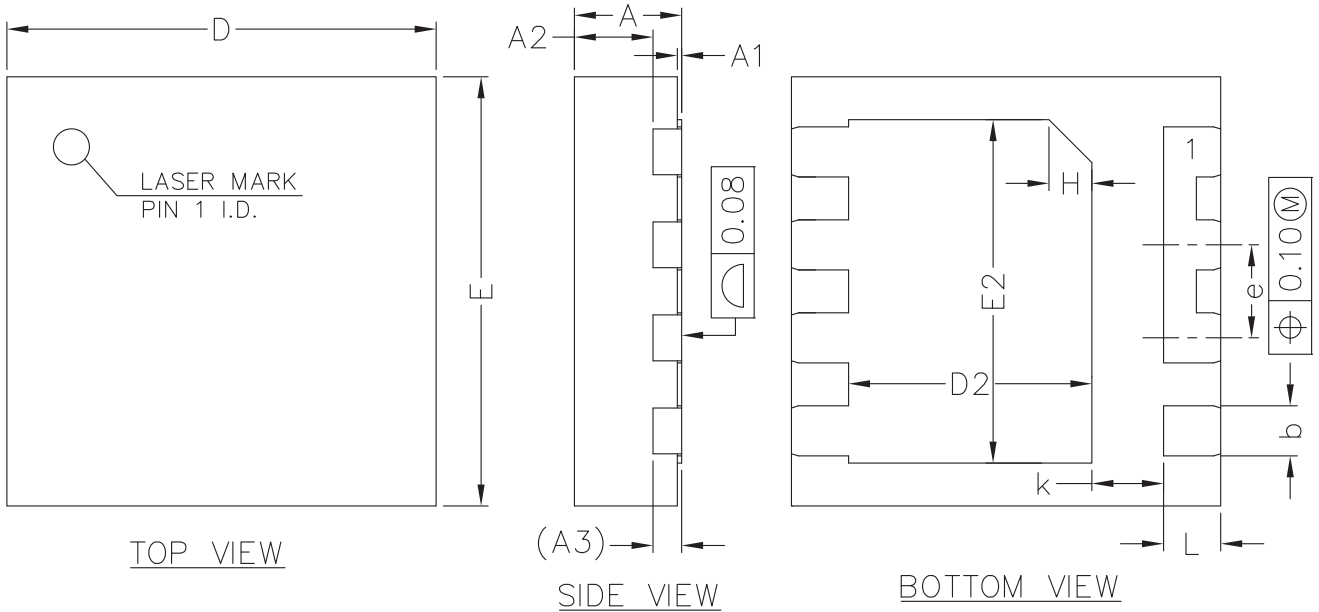


**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 175\text{ °C}$ , using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A2	0.50	0.55	0.60
A3	0.20REF		
b	0.30	0.35	0.40
D	2.90	3.00	3.10
E	2.90	3.00	3.10
D2	1.60	1.70	1.80
E2	2.30	2.40	2.50
e	0.55	0.65	0.75
K	0.40	0.50	0.60
L	0.35	0.40	0.45

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