

## N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω) Max.	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)
20	0.026 at V <sub>GS</sub> = 4.5 V	2.9	3.5
	0.033 at V <sub>GS</sub> = 2.5 V	2.6	

### FEATURES

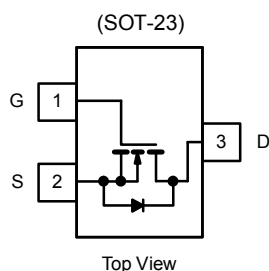
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

### APPLICATIONS

- Load Switching for Portable Devices
- DC/DC Converter



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)				
Parameter	Symbol	5 s	Steady State	Unit
Drain-Source Voltage	V <sub>DS</sub>	20		V
Gate-Source Voltage	V <sub>GS</sub>	± 8		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	2.9	2.6	A
	T <sub>A</sub> = 70 °C	2.3	2.1	
Pulsed Drain Current (t = 300 μs) <sup>b</sup>	I <sub>DM</sub>	10		
Continuous Source Current (Diode Conduction) <sup>a</sup>	I <sub>S</sub>	0.72	0.6	W
Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	0.86	0.71	
	T <sub>A</sub> = 70 °C	0.55	0.46	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 5 s	120	145	°C/W	
	Steady State	140	175		
Maximum Junction-to-Foot	Steady State	62	78		

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. Pulse width limited by maximum junction temperature.

**SPECIFICATIONS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)

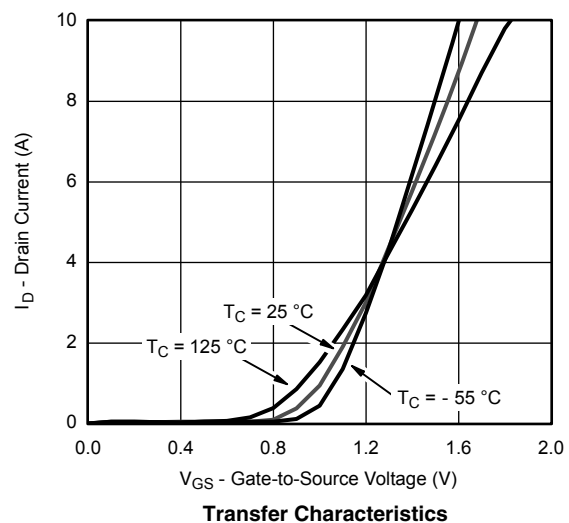
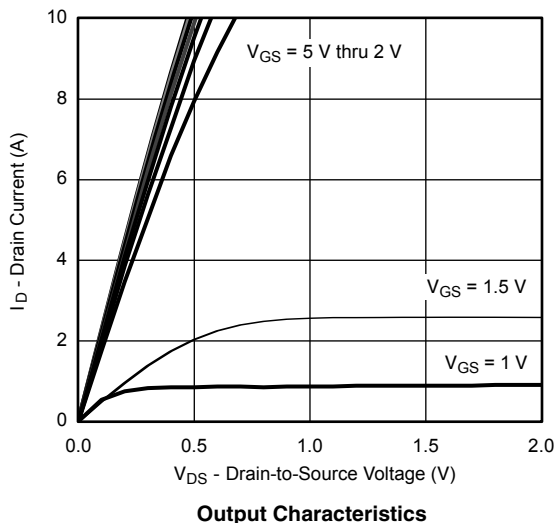
Parameter	Symbol	Test Conditions	Limits			Unit
			Min.	Typ.	Max.	
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	20			V
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	0.40		0.85	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 8\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 20\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 20\text{ V}, V_{GS} = 0\text{ V}, T_J = 70\text{ }^\circ\text{C}$			75	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 10\text{ V}, V_{GS} = 4.5\text{ V}$	6			A
Drain-Source On-Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 4.5\text{ V}, I_D = 3.6\text{ A}$		0.026	0.029	$\Omega$
		$V_{GS} = 2.5\text{ V}, I_D = 3.1\text{ A}$		0.033	0.037	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 5\text{ V}, I_D = 3.6\text{ A}$		13		S
Diode Forward Voltage	$V_{SD}$	$I_S = 0.95\text{ A}, V_{GS} = 0\text{ V}$		0.7	1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = 10\text{ V}, V_{GS} = 4.5\text{ V}, I_D = 3.6\text{ A}$		3.5	5.5	nC
Gate-Source Charge	$Q_{gs}$			0.6		
Gate-Drain Charge	$Q_{gd}$			0.45		
Gate Resistance	$R_g$	$f = 1\text{ MHz}$	2	4	8	$\Omega$
<b>Switching</b>						
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 10\text{ V}, R_L = 2.78\text{ }\Omega$ $I_D \cong 3.6\text{ A}, V_{GEN} = 4.5\text{ V}, R_g = 1\text{ }\Omega$		8	15	ns
Rise Time	$t_r$			7	15	
Turn-Off Delay Time	$t_{d(off)}$			30	45	
Fall Time	$t_f$			7	15	
Source-Drain Reverse Recovery Time	$t_{rr}$	$I_F = 3.6\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		8.5	15	nC
Body Diode Reverse Recovery Charge	$Q_{rr}$			2	4	

Notes:

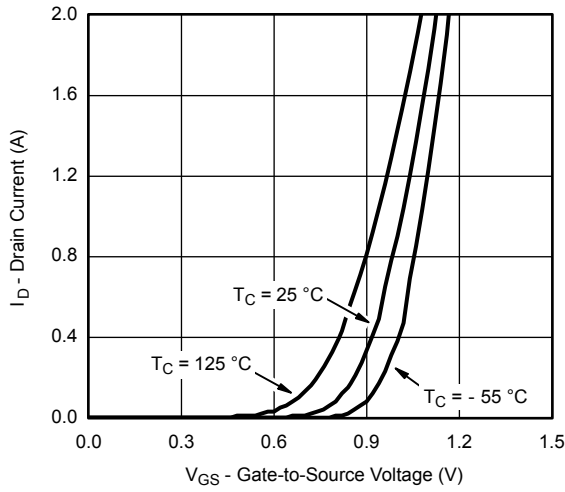
- a. Pulse test: Pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

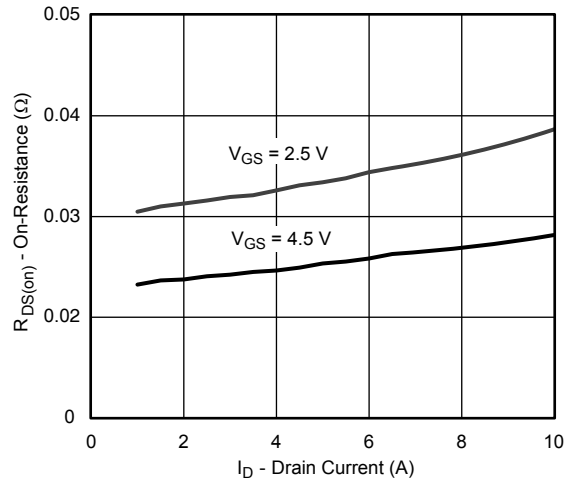
**TYPICAL CHARACTERISTICS** ( $25\text{ }^\circ\text{C}$ , unless otherwise noted)



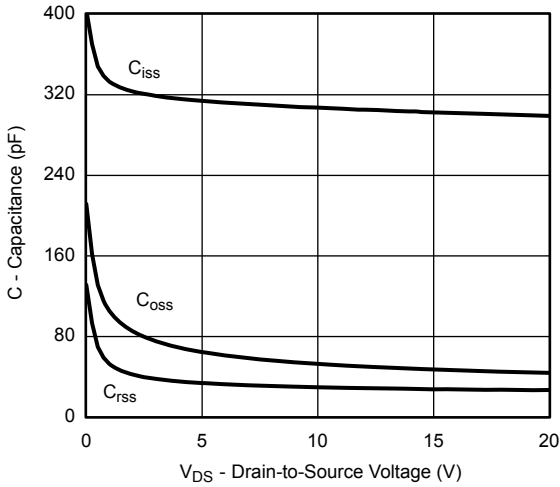
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



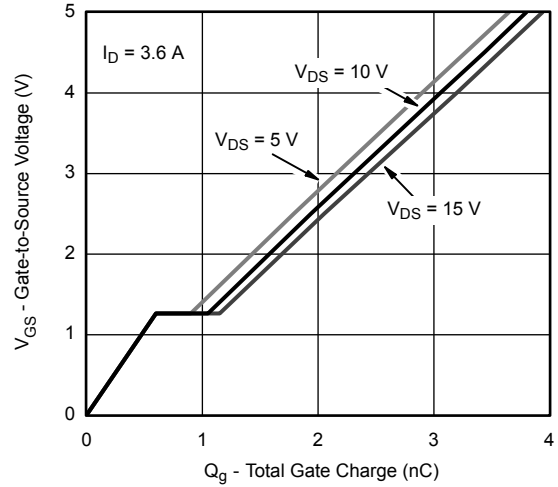
**Transfer Characteristics**



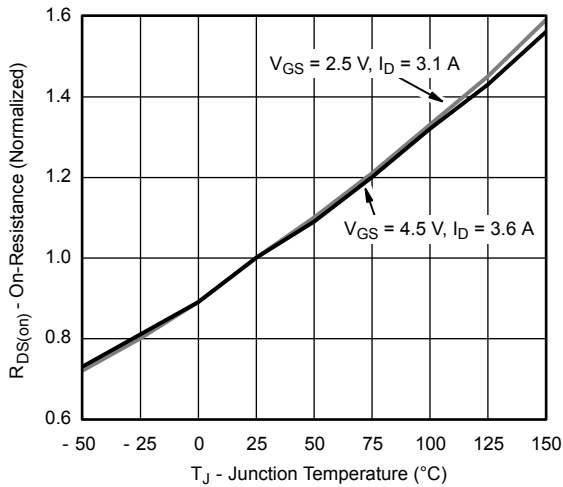
**On-Resistance vs. Drain Current**



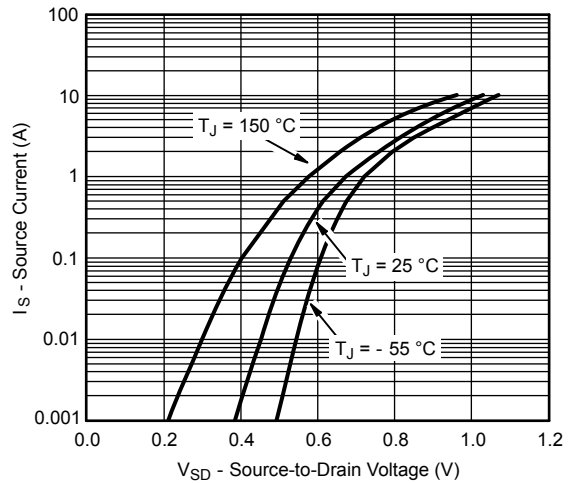
**Capacitance**



**Gate Charge**

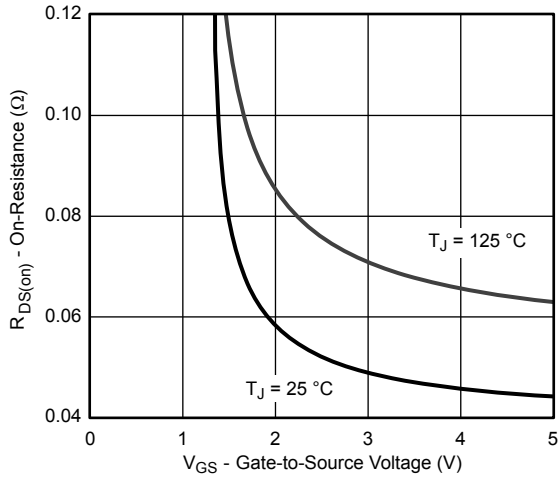


**On-Resistance vs. Junction Temperature**

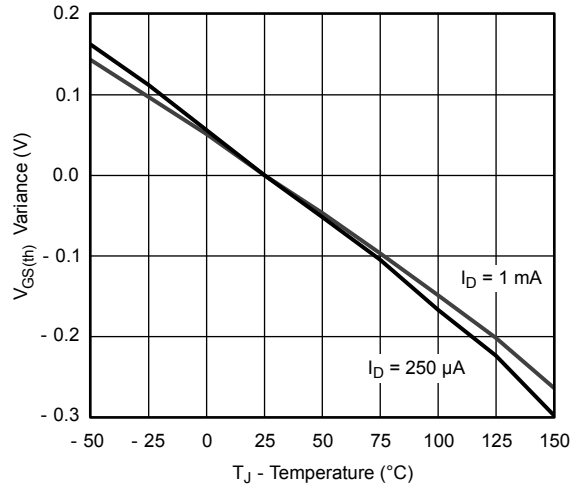


**Source-Drain Diode Forward Voltage**

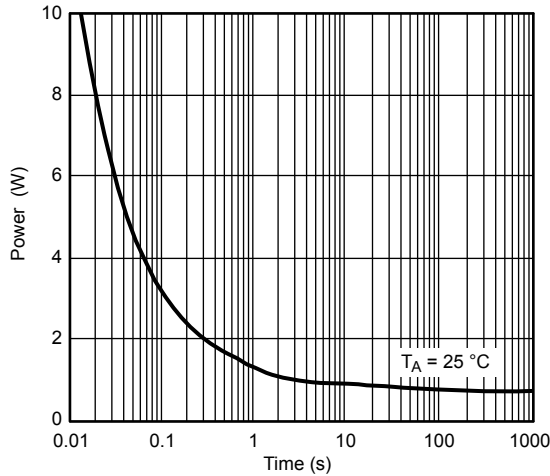
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



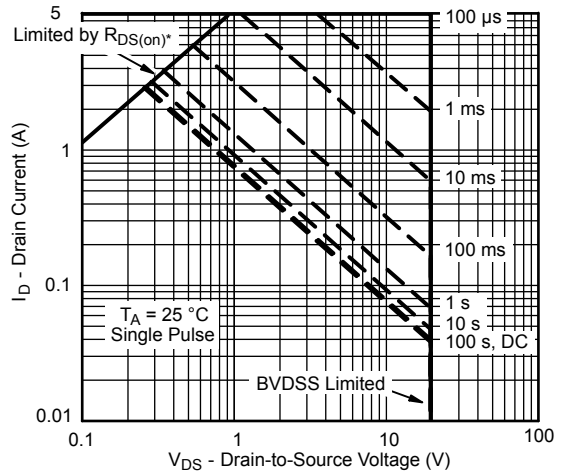
**On-Resistance vs. Gate-to-Source Voltage**



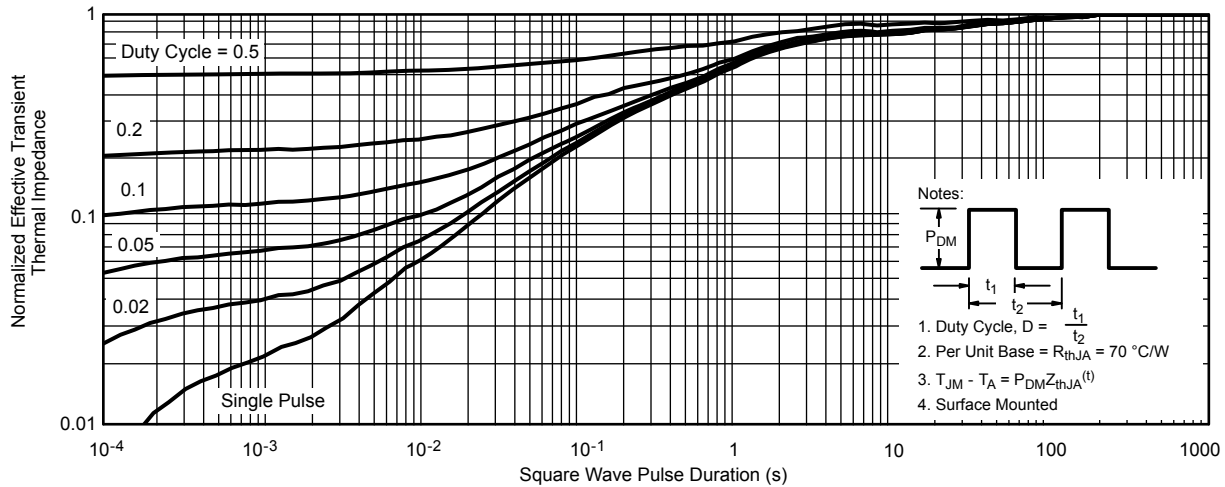
**Threshold Voltage**



**Single Pulse Power**

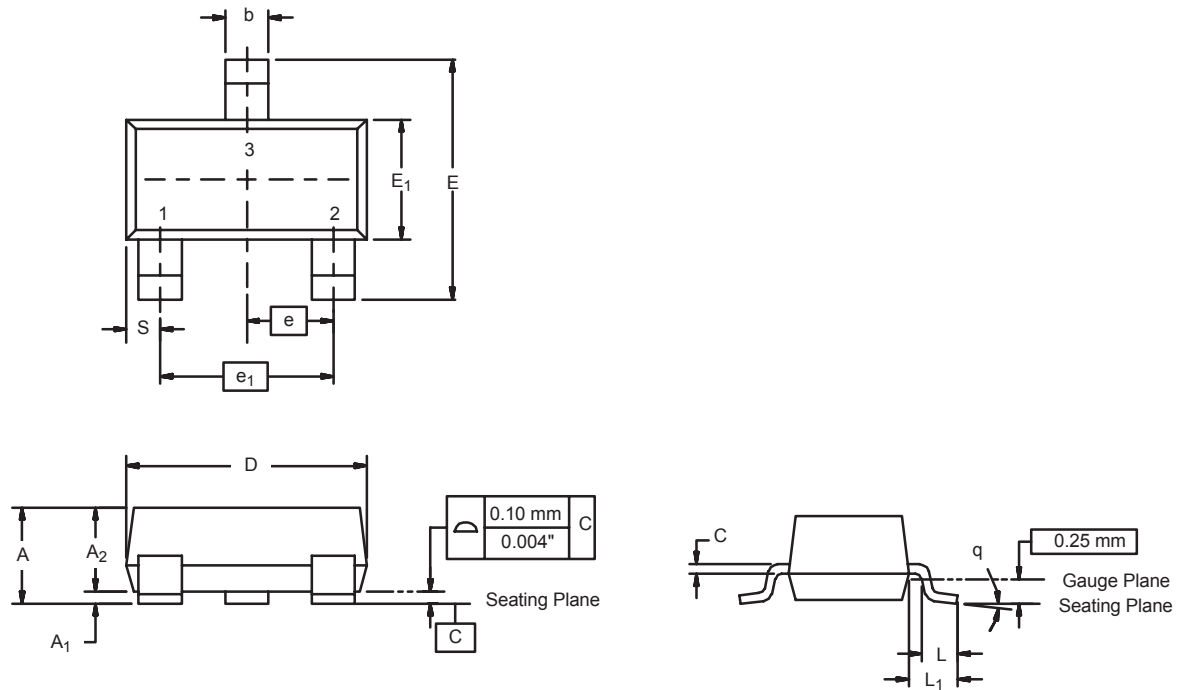


\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified  
**Safe Operating Area, Junction-to-Ambient**



**Normalized Thermal Transient Impedance, Junction-to-Ambient**

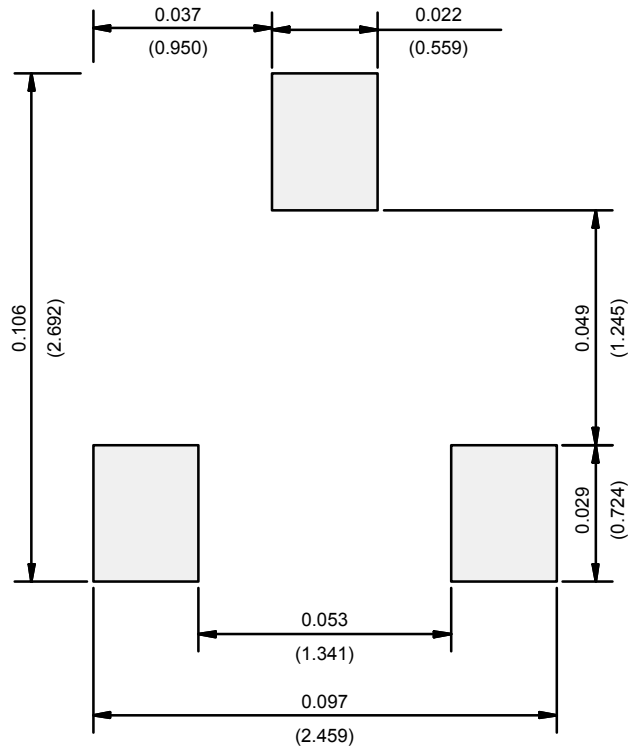
**SOT-23 : 3-LEAD**



Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	0.89	1.12	0.035	0.044
A <sub>1</sub>	0.01	0.10	0.0004	0.004
A <sub>2</sub>	0.88	1.02	0.0346	0.040
b	0.35	0.50	0.014	0.020
c	0.085	0.18	0.003	0.007
D	2.80	3.04	0.110	0.120
E	2.10	2.64	0.083	0.104
E <sub>1</sub>	1.20	1.40	0.047	0.055
e	0.95 BSC		0.0374 Ref	
e <sub>1</sub>	1.90 BSC		0.0748 Ref	
L	0.40	0.60	0.016	0.024
L <sub>1</sub>	0.64 Ref		0.025 Ref	
S	0.50 Ref		0.020 Ref	
q	3°	8°	3°	8°

ECN: S-03946-Rev. K, 09-Jul-01  
DWG: 5479

**RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads  
Dimensions in Inches/(mm)

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