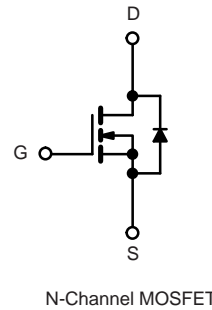
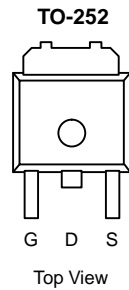


N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY		
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a
60	0.0038 at V _{GS} = 10 V	80
	0.0057 at V _{GS} = 4.5 V	75

FEATURES

- 175 °C Junction Temperature
- TrenchFET[®] Power MOSFET
- Material categorization:



ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)				
Parameter		Symbol	Limit	Unit
Gate-Source Voltage		V _{GS}	± 20	V
Continuous Drain Current (T _J = 175 °C) ^b	T _C = 25 °C	I _D	80	A
	T _C = 100 °C		70 ^a	
Pulsed Drain Current		I _{DM}	240	
Continuous Source Current (Diode Conduction)		I _S	80 ^a	
Avalanche Current		I _{AS}	85	
Single Avalanche Energy (Duty Cycle ≤ 1 %)	L = 0.1 mH	E _{AS}	255	mJ
Maximum Power Dissipation	T _C = 25 °C	P _D	186	W
	T _A = 25 °C		5 ^b , 13.3 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	t ≤ 10 sec	R _{thJA}	11	15	°C/W
	Steady State		30	45	
Maximum Junction-to-Case		R _{thJC}	0.75	1.0	

Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t ≤ 10 s.

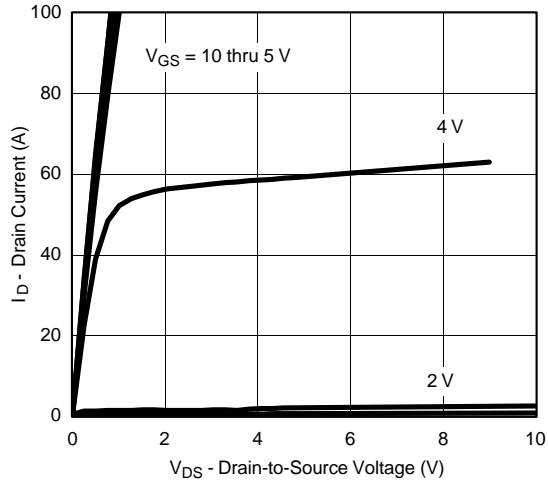
SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	60			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1	-	3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 48\text{ V}, V_{GS} = 0\text{ V}$			1	μA
		$V_{DS} = 48\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$			50	
		$V_{DS} = 48\text{ V}, V_{GS} = 0\text{ V}, T_J = 175\text{ }^\circ\text{C}$			250	
On-State Drain Current ^b	$I_{D(on)}$	$V_{DS} = 5\text{ V}, V_{GS} = 10\text{ V}$	80			A
Drain-Source On-State Resistance ^b	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 10\text{ A}$		0.0038	0.0045	Ω
		$V_{GS} = 10\text{ V}, I_D = 10\text{ A}, T_J = 125\text{ }^\circ\text{C}$		0.0045	0.005	
		$V_{GS} = 10\text{ V}, I_D = 10\text{ A}, T_J = 175\text{ }^\circ\text{C}$		0.0049	0.0055	
		$V_{GS} = 4.5\text{ V}, I_D = 5\text{ A}$		0.0057	0.0069	
Forward Transconductance ^b	g_{fs}	$V_{DS} = 15\text{ V}, I_D = 10\text{ A}$		80		S
Dynamic						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = 25\text{ V}, f = 1\text{ MHz}$		6650		μF
Output Capacitance	C_{oss}			370		
Reverse Transfer Capacitance	C_{rss}			45		
Total Gate Charge ^c	Q_g	$V_{DS} = 48\text{ V}, V_{GS} = 10\text{ V}, I_D = 50\text{ A}$		55	70	nC
Gate-Source Charge ^c	Q_{gs}			12		
Gate-Drain Charge ^c	Q_{gd}			16		
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = 48\text{ V}, R_L = 0.6\text{ }\Omega$ $I_D \cong 50\text{ A}, V_{GEN} = 10\text{ V}, R_g = 2.5\text{ }\Omega$		18		ns
Rise Time ^c	t_r			20		
Turn-Off Delay Time ^c	$t_{d(off)}$			26		
Fall Time ^c	t_f			9		
Source-Drain Diode Ratings and Characteristics ($T_C = 25\text{ }^\circ\text{C}$)						
Pulsed Current	I_{SM}				240	A
Diode Forward Voltage	V_{SD}	$I_F = 20\text{ A}, V_{GS} = 0\text{ V}$		1		V
Reverse Recovery Time	t_{rr}	$I_F = 20\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		35		ns

Notes:

- For design aid only; not subject to production testing.
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

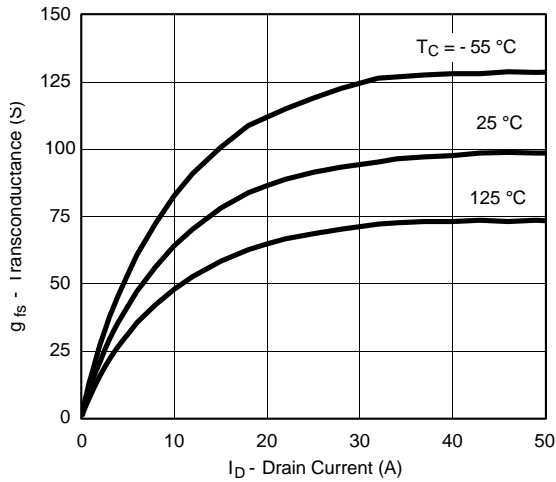
TYPICAL CHARACTERISTICS (25 °C unless noted)



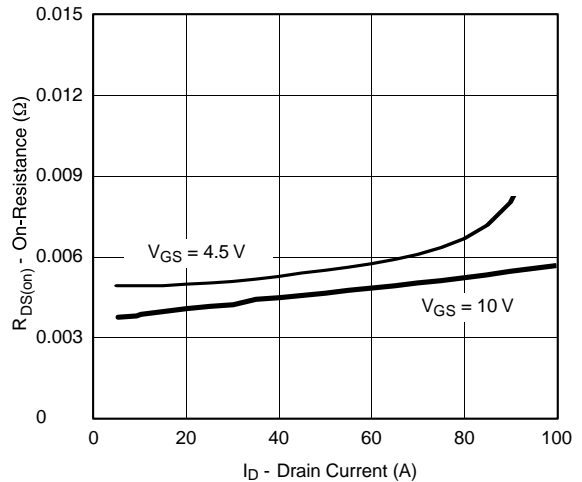
Output Characteristics



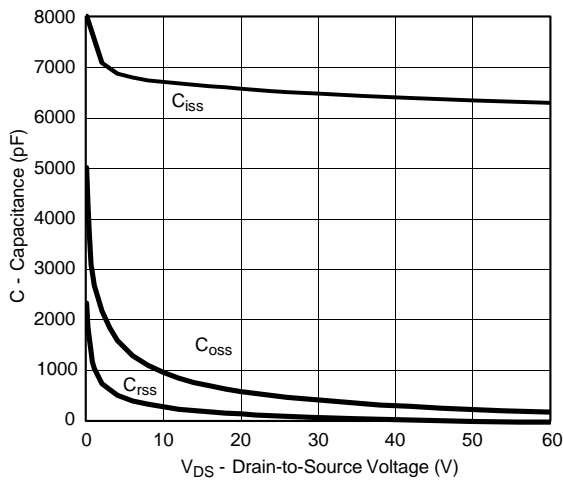
Transfer Characteristics



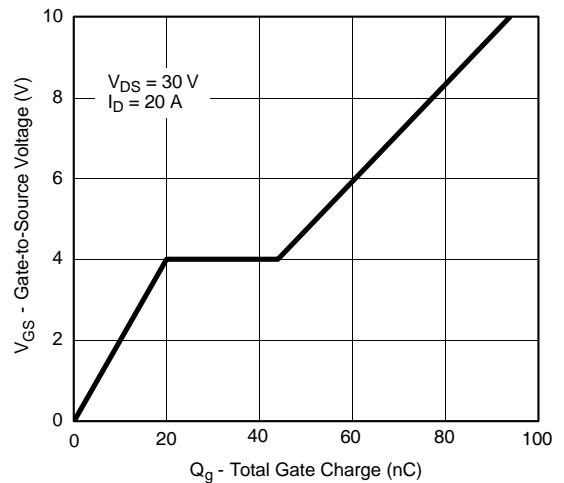
Transconductance



On-Resistance vs. Drain Current

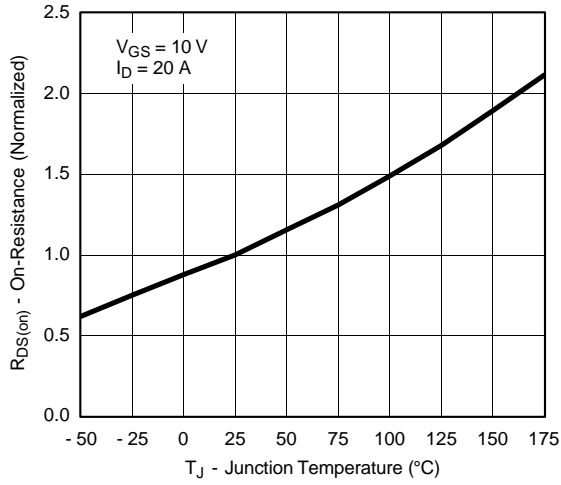


Capacitance

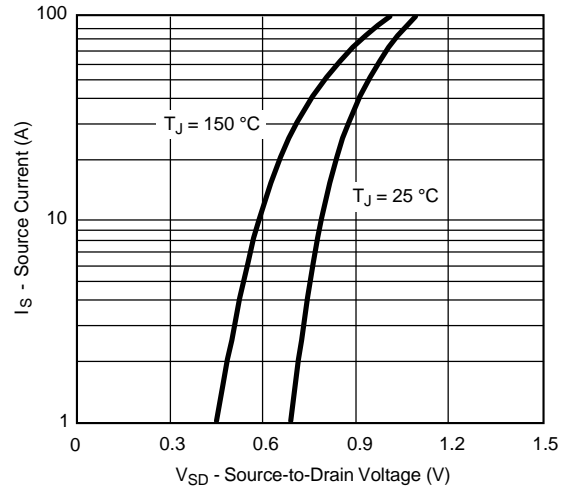


Gate Charge

TYPICAL CHARACTERISTICS (25 °C unless noted)



On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

TO-252AA CASE OUTLINE



DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.38	0.086	0.094
A1	-	0.127	-	0.005
b	0.64	0.88	0.025	0.035
b2	0.76	1.14	0.030	0.045
b3	4.95	5.46	0.195	0.215
C	0.46	0.61	0.018	0.024
C2	0.46	0.89	0.018	0.035
D	5.97	6.22	0.235	0.245
D1	5.21	-	0.205	-
E	6.35	6.73	0.250	0.265
E1	4.32	-	0.170	-
H	9.40	10.41	0.370	0.410
e	2.28 BSC		0.090 BSC	
e1	4.56 BSC		0.180 BSC	
L	1.40	1.78	0.055	0.070
L3	0.89	1.27	0.035	0.050
L4	-	1.02	-	0.040
L5	1.14	1.52	0.045	0.060
ECN: X12-0247-Rev. M, 24-Dec-12 DWG: 5347				

Note

- Dimension L3 is for reference only.

RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads
Dimensions in Inches/(mm)

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