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N-Channel 600-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)(Typ.)	I _D (A) ^a	Q _g (Typ.)		
600	25 at V _{GS} = 10 V	0.12	3.6 nC		

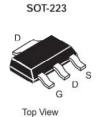
FEATURES

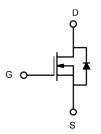
- Depletion mode MOSFET
- 100 % R_g Tested
- Low Gate Charge



APPLICATIONS

Load Switch





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS TA = 25	°C, unless oth	erwise noted			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	600	V	
Gate-Source Voltage		V _{GS}	± 30	V	
Continuous Drain Current (T _{.I} = 150 °C)	T _A = 25 °C	I_	0.12 ^{b, c}	Α	
Continuous Drain Guiterit (1) = 150 C)	T _A = 70 °C	l I _D	0.10 ^{b, c}		
Pulsed Drain Current	T _A = 25 °C	I _{DM}	0.48		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	0.12 ^{b, c}		
Avalanche Current		I _{AS}	0.1		
Single-Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	0.1	mJ	
	T _C = 25 °C	P _D	2.5	W	
Maximum Power Dissipation	T _C = 70 °C		1.9		
Maximum Fower Dissipation	T _A = 25 °C		1.86 ^{b, c}		
	T _A = 70 °C		0.7 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	≤ 5 s	R _{thJA}	90	115	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{th.IF}	60	75	1 6/44	

- a. Based on T_C = 25 °C.
 b. Surface Mounted on 1" x 1" FR4 board.
- d. Maximum under Steady State conditions is 120 °C/W.



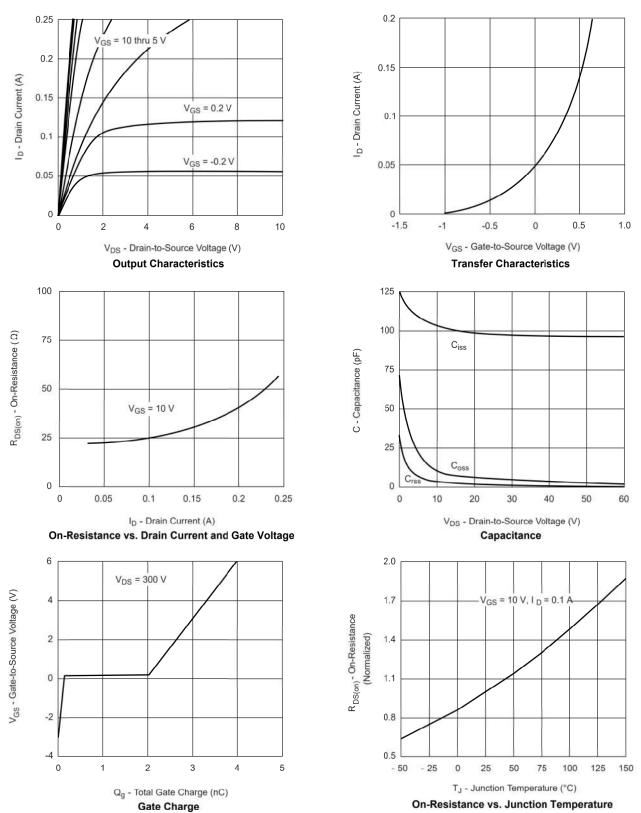


Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				,			
Drain-Source Breakdown Voltage	V _{DS}	$V_{DS} = 0 \text{ V, I}_{D} = 250 \mu\text{A}$	600			V	
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J			0.6		mV/°(
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 1.5] """/ \	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 100 \mu A$	-2.5		-1	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = 20 \text{ V}$			100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 10 V, V _{GS} = 0 V	20			mA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	0.1			Α	
Drain-Source On-State Resistance ^a	Rear)	$V_{GS} = 0 \text{ V}, I_D = 0.01 \text{ A}$		30	60	60 45	
	R _{DS(on)}	V _{GS} = 10 V, I _D = 0.1 A		25	45		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 5V, I _D = 0.1 A		0.15		S	
Dynamic ^b							
Input Capacitance	C _{iss}			96			
Output Capacitance	C _{oss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		7.5		pF	
Reverse Transfer Capacitance	C _{rss}			3.3			
Total Gate Charge	Qg			3.6			
Gate-Source Charge	Q_{gs}	$V_{DS} = 300 \text{ V}, V_{GS} = -3 \text{ to } 5 \text{ V},$ $ID = 0.1 \text{ A}$		0.2		nC	
Gate-Drain Charge	Q _{gd}	ID = 0.1 A		2			
Gate Resistance	R _g	f = 1 MHz		4.5		Ω	
Turn-On Delay Time	t _{d(on)}			6			
Rise Time	t _r	$V_{DD} = 300 \text{ V}, I_{D} = 0.1 \text{ A},$		20		ns	
Turn-Off Delay Time	t _{d(off)}	V_{GEN} = -3 to 5 V, R_G = 15 Ω		10			
Fall Time	t _f			25			
Drain-Source Body Diode Characteristic	s			•			
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			0.12	А	
Pulse Diode Forward Current ^a	I _{SM}				0.48		
Body Diode Voltage	V _{SD}	I _S = 0.2 A			1.5	V	
Body Diode Reverse Recovery Time	t _{rr}			195		ns	
Body Diode Reverse Recovery Charge	Q _{rr}	L = 0.2 A dl/dt = 100 A/::2 T = 25 °C		610		nC	
Reverse Recovery Fall Time	t _a	$I_F = 0.2 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 °\text{C}$		8		.	
Reverse Recovery Rise Time	t _b			9		ns ns	

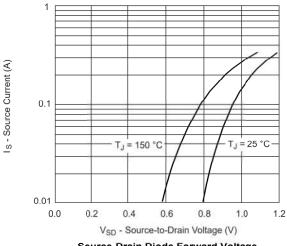
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

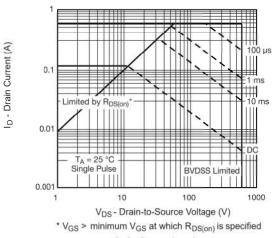
Notes:
a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

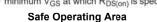


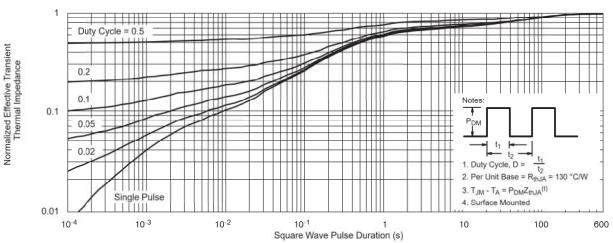
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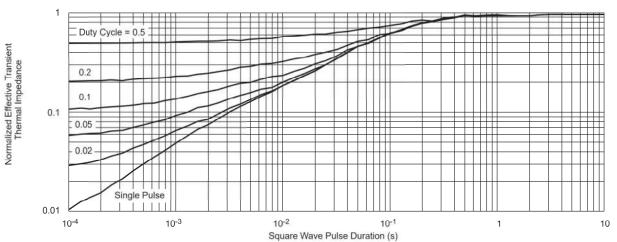


Source-Drain Diode Forward Voltage





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot





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