

P-Channel 20-V (D-S) MOSFET

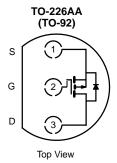
PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
- 20	0.045 at V _{GS} = - 4.5 V	- 4.2		
	0.072 at V _{GS} = - 2.5 V	- 3.6		

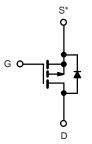
FEATURES

- DT-Trench Power MOSFET
- 100 % R_g Tested
- 100 % UIS Tested



RoHS COMPLIANT





P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unles	ss otherwise r	noted			
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 20		V	
Gate-Source Voltage		V _{GS}	± 12			
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	- I _D	- 4.2	- 4.0		
	T _A = 70 °C		- 3.9	- 3.2		
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	- 20		А	
Continuous Source Current (Diode Conduction) ^a		I _S	- 1.35 - 0.95			
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	1.5	1.05	W	
	T _A = 70 °C		1.0	0.67		
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Marrian Institut to Ambient	t ≤ 10 s	- R _{thJA}	65	83	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		100	120		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	43	52		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.



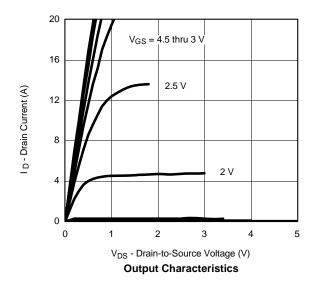
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$			- 1.5	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 12 V, V _{GS} = 0 V	- 1		- 1		
		V _{DS} = - 12 V, V _{GS} = 0 V, T _J = 70 °C			- 25	μΑ	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 4.5 V	- 20			Α	
Drain-Source On-State Resistance ^a	Ъ	$V_{GS} = -4.5 \text{ V}, I_D = -4.2 \text{ A}$		0.032	0.045	Ω	
	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -3.6 \text{ A}$		0.053	0.072		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 4.2 A		14		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 1.35 A, V _{GS} = 0 V		- 0.77	- 1.1	٧	
Dynamic ^b			· ·	"			
Total Gate Charge	Q_g			10	15		
Gate-Source Charge	Q _{gs}	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -4.2 \text{ A}$		1.8		nC	
Gate-Drain Charge	Q _{gd}			3			
Gate Resistance	R_g	f = 1 MHz		7.7		Ω	
Turn-On Delay Time	t _{d(on)}			45	70		
Rise Time	t _r	V_{DD} = - 6 V, R_L = 6 Ω		60	90		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 1 A, V_{GEN} = - 4.5 V, R_g = 6 Ω		70	110	ns	
Fall Time	t _f			35	55		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.35 A, di/dt = 100 A/μs		65			

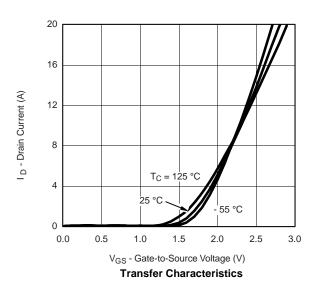
Notes:

- a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

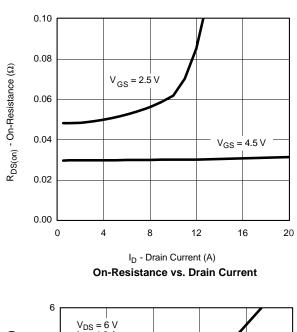
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

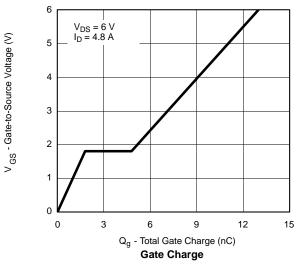


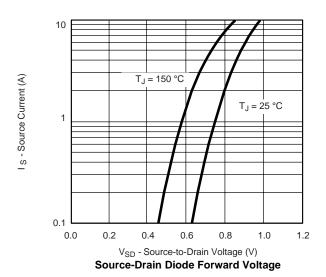


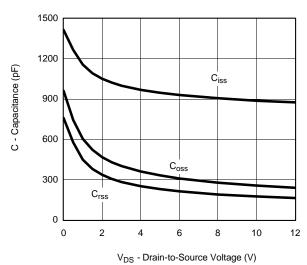


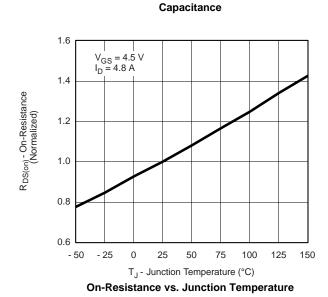
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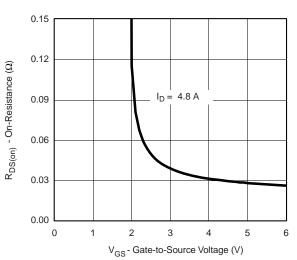






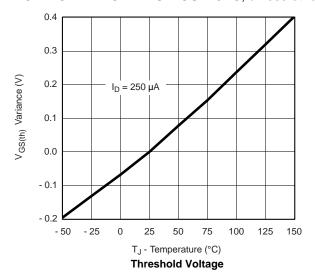


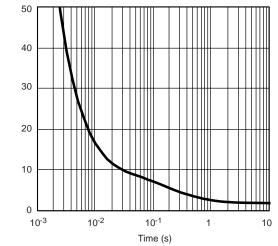




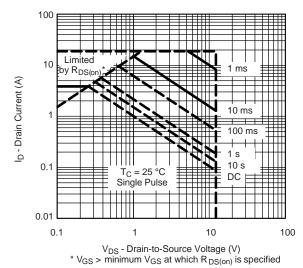


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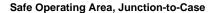


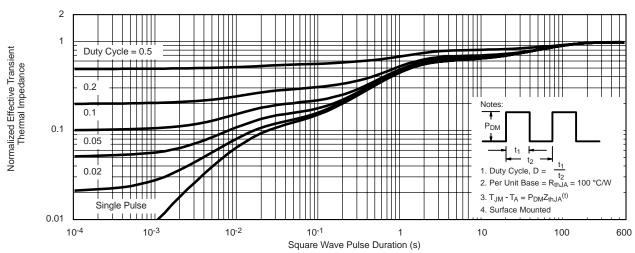


Single Pulse Power, Junction-to-Ambient



Power (W)

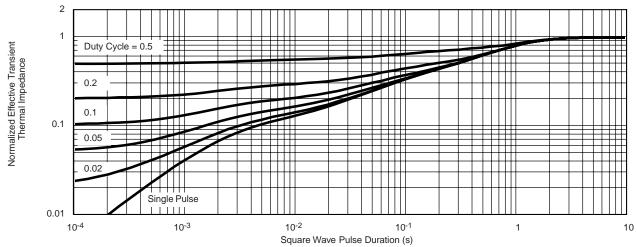




Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot





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