

## P-Channel 40 V (D-S) MOSFET

### PRODUCT SUMMARY

V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (mΩ) (Typ.)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
- 40	2.9 at V <sub>GS</sub> = - 10 V	- 110	42 nC
	4.1 at V <sub>GS</sub> = - 4.5 V	- 90	

### FEATURES

- DT-Trench Power MOSFET
- 100 % Rg and UIS Tested

### APPLICATIONS

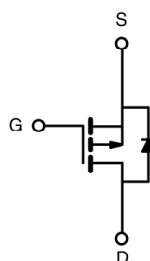
- Load Switch



TO-263



Top View



P-Channel MOSFET

### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25 °C, unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V <sub>DS</sub>	-40	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	
Continuous Drain Current <sup>d</sup> (T <sub>J</sub> = 175 °C)	T <sub>C</sub> = 25 °C	I <sub>D</sub>	-110	A
	T <sub>C</sub> = 125 °C		-97	
Pulsed Drain Current		I <sub>DM</sub>	- 440	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	-100	
Single Pulse Avalanche Energy <sup>a</sup>		E <sub>AS</sub>	80	mJ
Power Dissipation	T <sub>C</sub> = 25 °C <sup>c</sup>	P <sub>D</sub>	134	W
	T <sub>A</sub> = 25 °C <sup>b</sup>		3.57	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>sta</sub>	-55 to +175	°C

### THERMAL RESISTANCE RATINGS

PARAMETER		SYMBOL	TYPICAL	UNIT
Junction-to-Ambient	PCB mount <sup>b</sup>	R <sub>thJA</sub>	60	°C/W
Junction-to-Case		R <sub>thJC</sub>	0.8	

#### Notes

- Duty cycle ≤ 1 %.
- When mounted on 1" square PCB (FR4 material).
- See SOA curve for voltage derating.

SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 40			V
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS</sub> /T	I <sub>D</sub> = - 250 μA		65		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>			- 5.3		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	- 1		- 3	V
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 40 V, V <sub>GS</sub> = 0 V			- 1	μA
		V <sub>DS</sub> = - 32 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V	- 110			A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 50 A		2.9	3.5	mΩ
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 50 A		4.1	5.3	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 50 A		80		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V, f = 1 MHz		11000		pF
Output Capacitance	C <sub>oss</sub>			3200		
Reverse Transfer Capacitance	C <sub>rss</sub>			105		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 50 A		169		nC
Gate-Source Charge	Q <sub>gs</sub>			14		
Gate-Drain Charge	Q <sub>gd</sub>			30		
Gate Resistance	R <sub>g</sub>	f = 1 MHz		5.2		Ω
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = - 20 V, R <sub>L</sub> = 2 Ω I <sub>D</sub> = - 50 A, V <sub>GEN</sub> = - 10 V, R <sub>g</sub> = 1 Ω		25		ns
Rise Time	t <sub>r</sub>			18		
Turn-Off Delay Time	t <sub>d(off)</sub>			162		
Fall Time	t <sub>f</sub>			60		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 110	A
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 440	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 1 A		- 0.65	- 1	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 50 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C		45		ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			59		nC
Reverse Recovery Fall Time	t <sub>a</sub>			29		ns
Reverse Recovery Rise Time	t <sub>b</sub>			16		

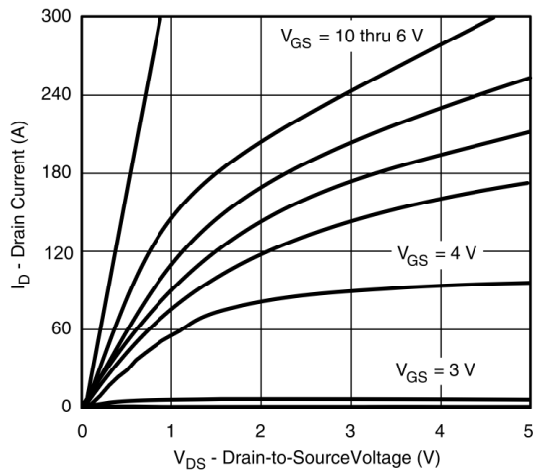
Notes:

a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

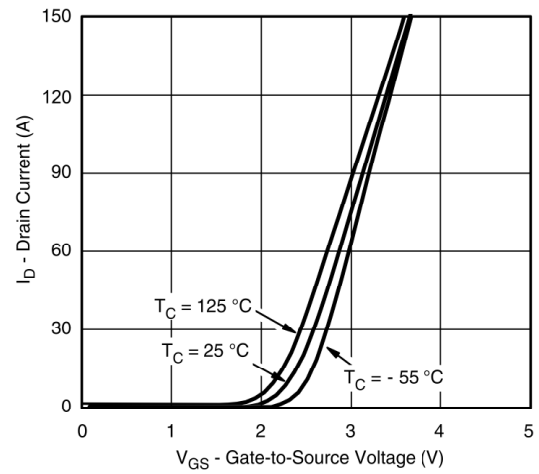
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

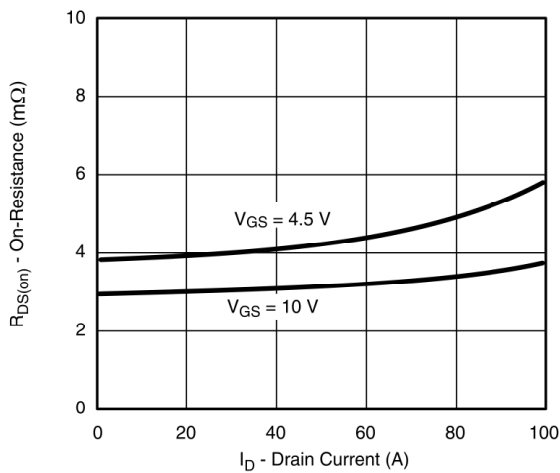
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



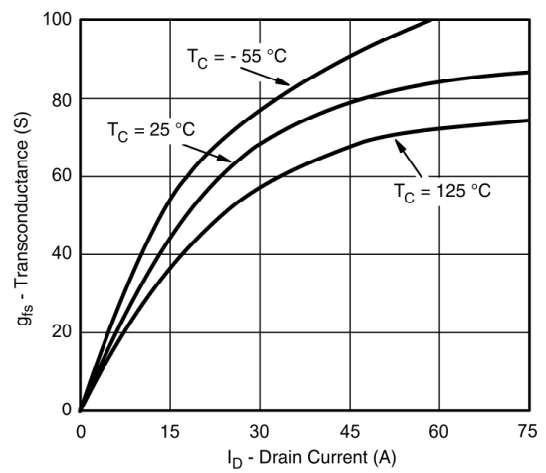
**Output Characteristics**



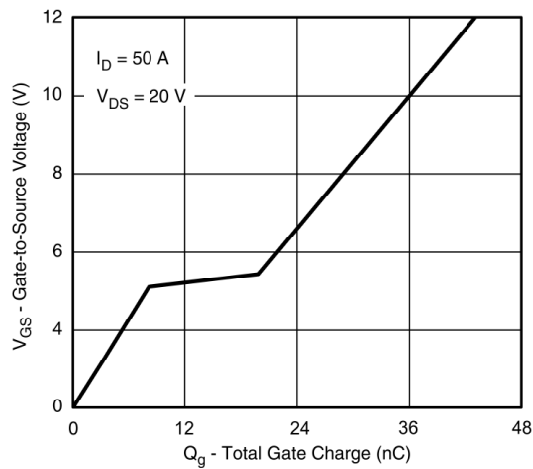
**Transfer Characteristics**



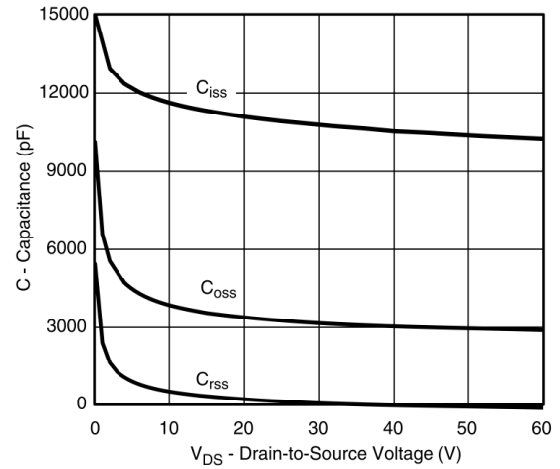
**On-Resistance vs. Drain Current**



**Transconductance**

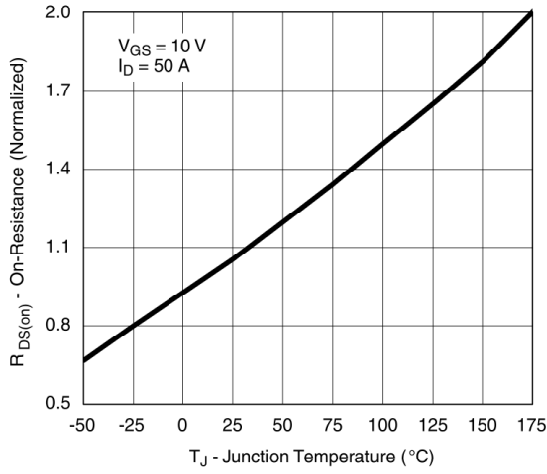


**Gate Charge**

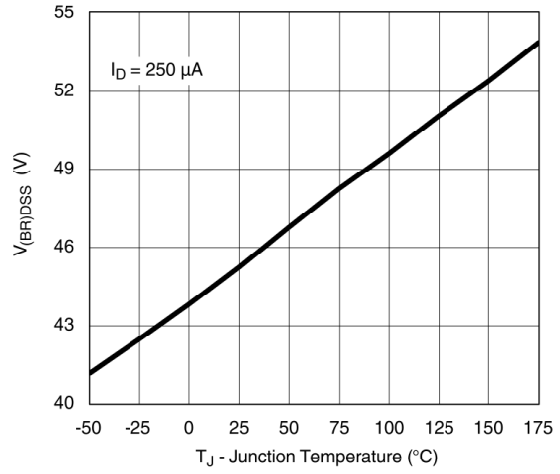


**Capacitance**

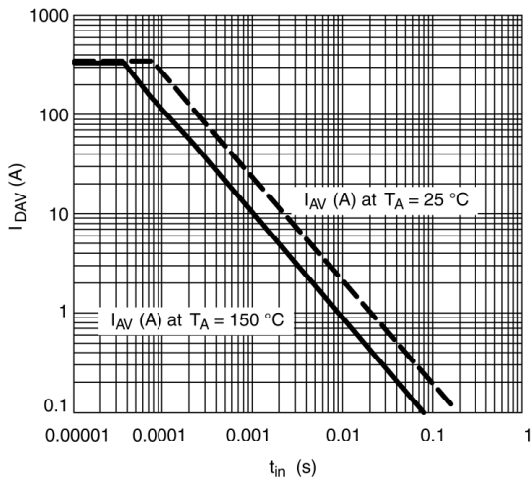
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



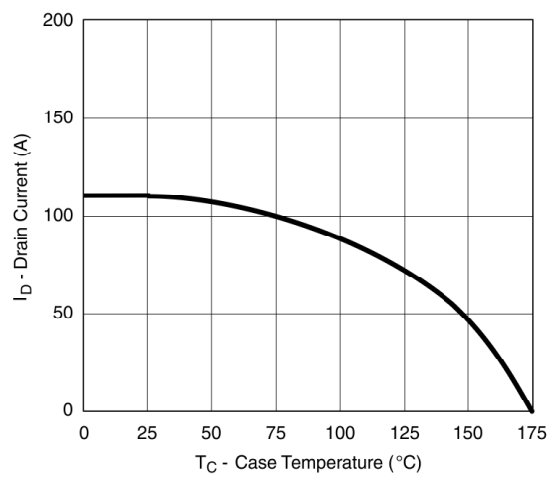
**On-Resistance vs. Junction Temperature**



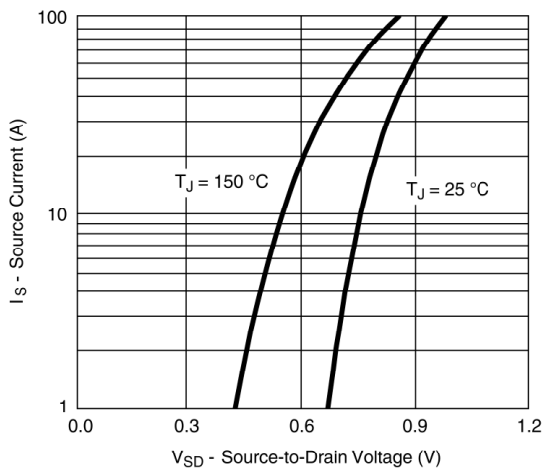
**Drain Source Breakdown vs. Junction Temperature**



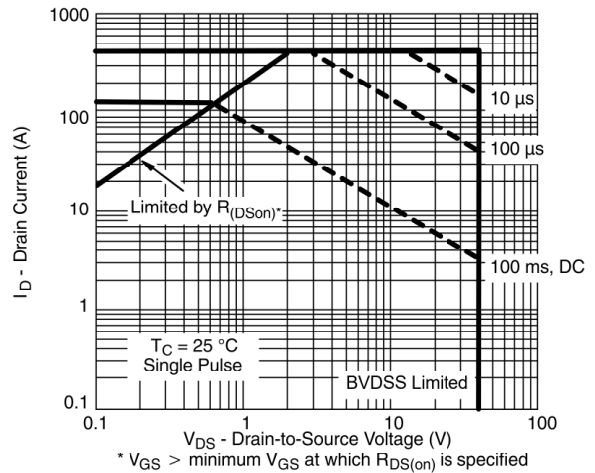
**Avalanche Current vs. Time**



**Maximum Avalanche and Drain Current vs. Case Temperature**

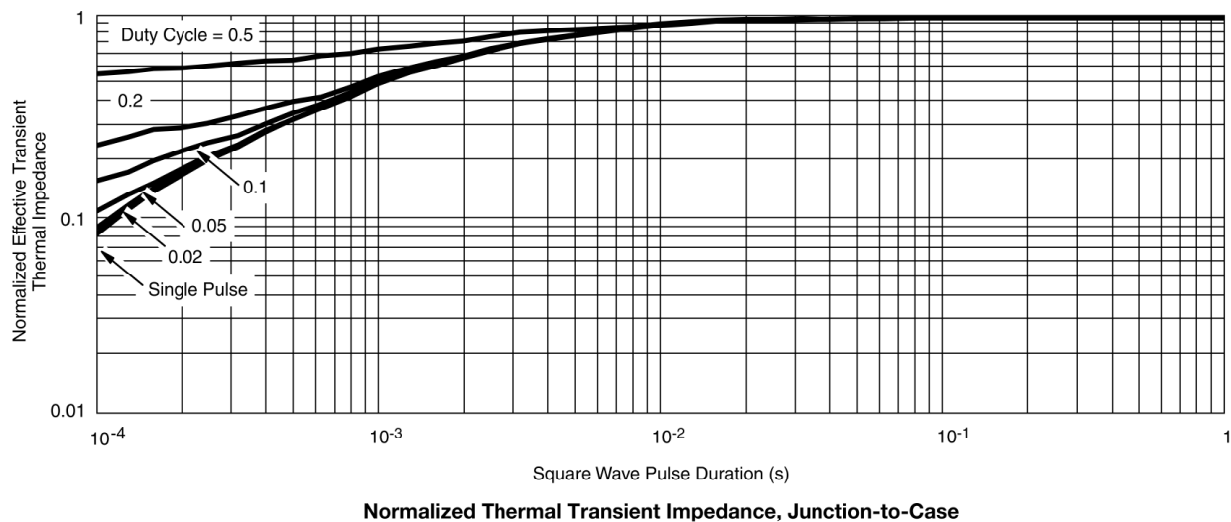


**Source-Drain Diode Forward Voltage**



**Safe Operating Area**

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



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