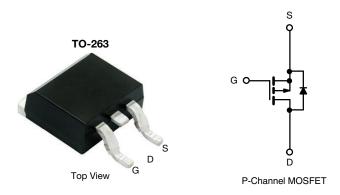




# P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>d</sup>		
-60	0.0037 at V <sub>GS</sub> = -10 V	-140		
-60	$0.0046$ at $V_{CS} = -4.5$ V	-140		



#### **FEATURES**

- DT-Trench Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested



#### **APPLICATIONS**

- Power Switch
- DC/DC Converters
- Portable equipment and battery powered systems

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V <sub>DS</sub>	-60	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	v
Continuous Drain Current d	T <sub>C</sub> = 25 °C		-140	
$(T_J = 175  ^{\circ}\text{C})$	T <sub>C</sub> = 125 °C	I <sub>D</sub>	-89	_
Pulsed Drain Current		I <sub>DM</sub>	-550	A
Avalanche Current	1 0.1 ml l	I <sub>AS</sub>	-108	
Single Pulse Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	505	mJ
Daway Dissination	T <sub>C</sub> = 25 °C °	P <sub>D</sub> 195 4.37	195	W
Power Dissipation	T <sub>A</sub> = 25 °C b		4.37	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	TYPICAL	UNIT	
Junction-to-Ambient	PCB mount <sup>b</sup>	R <sub>thJA</sub>	40	°C/W	
Junction-to-Case		$R_{thJC}$	0.4	C/ VV	

#### Notes

- a. Duty cycle ≤ 1 %.
- b. When mounted on 1" square PCB (FR4 material).
- c. See SOA curve for voltage derating.
- d. Limited by package.



PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-60	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \mu A$	-1	-	-3	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -60 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	-1	μΑ
	יטאי	$V_{DS} = -48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 \text{ °C}$	-	-	-10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$	-140	-	-	Α
Drain-Source On-State Resistance <sup>a</sup>	_	V <sub>GS</sub> = -10 V, I <sub>D</sub> = -50 A	-	0.0037	0.0046	Ω
	R <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V}, I_D = -25 \text{ A}$	-	0.0046	0.0062	22
Forward Transconductance <sup>a</sup>	9fs	V <sub>DS</sub> = -15 V, I <sub>D</sub> = -10 A	-	20	-	S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>		-	18800	-	pF
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = -30 V, f = 1 MHz	-	1750	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	725	-	
Total Gate Charge <sup>c</sup>	Qg		-	230	545	nC
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -10 \text{ A}$	-	50	-	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$		-	25	-	
Gate Resistance	Rg	f = 1 MHz	-	3	-	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	81	125	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = -30 \text{ V}, R_L = 0.27 \Omega$ $I_D \cong -110 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$	-	242	381	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>		-	510	703	ns
Fall Time <sup>c</sup>	t <sub>f</sub>		-	240	362	
<b>Drain-Source Body Diode Character</b>	istics (T <sub>C</sub> = 25	o°C b)				
Continuous Current	Is		-	-	-140	^
Pulsed Current	I <sub>SM</sub>		-	-	-550	A
Forward Voltage <sup>a</sup>	$V_{SD}$	$I_F = -85 \text{ A}, V_{GS} = 0 \text{ V}$	-	-0.7	-1.2	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = -85 A, dl/dt = 100 A/µs	-	41	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>	1 <sub>F</sub> = -03 A, αι/αι = 100 A/μS	-	0.21	0.44	μС

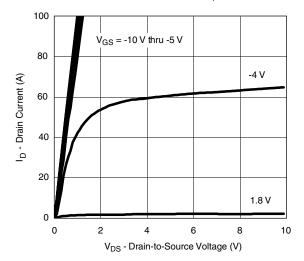
#### Notes

- a. Pulse test; pulse width  $\leq 300~\mu s,\,duty~cycle \leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

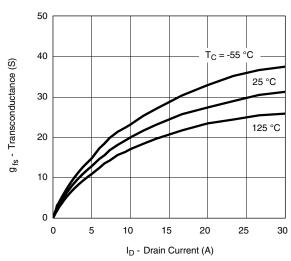
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



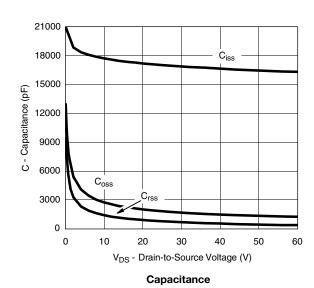
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

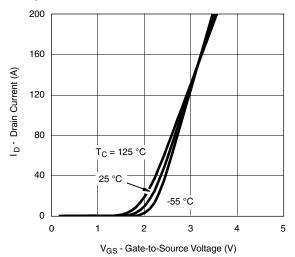


#### **Output Characteristics**

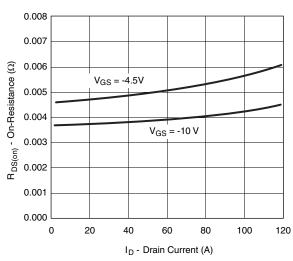


### Transconductance

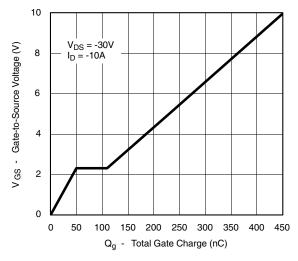




**Transfer Characteristics** 



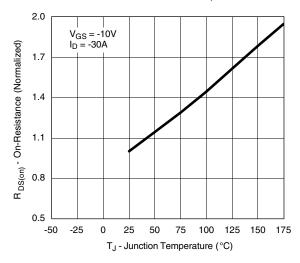
On-Resistance vs. Drain Current



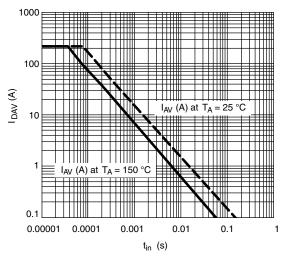
**Gate Charge** 



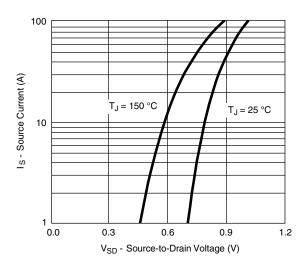
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



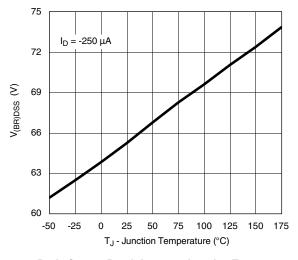
On-Resistance vs. Junction Temperature



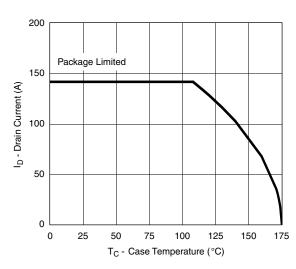
Avalanche Current vs. Time



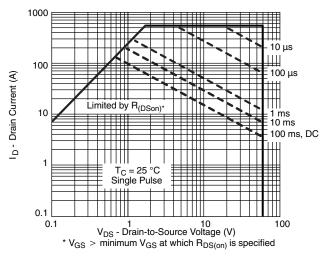
Source-Drain Diode Forward Voltage



**Drain Source Breakdown vs. Junction Temperature** 



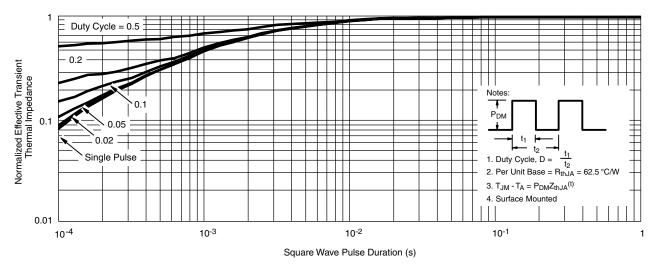
Maximum Avalanche and Drain Current vs. Case Temperature



Safe Operating Area



#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

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