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N-Channel 100 V (D-S) Power MOSFET

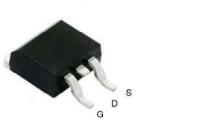
PRODUCT SUMMARY		
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A)(Max.)
100	8.2 at V _{GS} = 10 V	100 ^a

FEATURES

- DT-Trench II Power MOSFET
- 100 % Rg and UIS tested
- · Fast switching

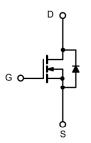
APPLICATIONS

- · Load Switch
- · LED applications
- Networking



Top View

TO-263 Pin Configuration



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _C = 25 °C, unless otherwise noted					
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	100	V		
Gate-Source Voltage	V _{GS}	± 20	v		
Continuous Dunin Comment (T. – 475 °C)	T _C = 25 °C		100 ^a	Α	
Continuous Drain Current (T _J = 175 °C)	T _C = 100 °C	─ I _D	48 ^a		
Pulsed Drain Current		I _{DM}	300	Α	
Avalanche Current ^b	I _{AR}	55			
Repetitive Avalanche Energy ^b	L = 0.1 mH	E _{AR}	115	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C	В	138 ^c	W	
waximum Fower Dissipation	T _A = 25 °C	$ P_{D}$	2.05		
Operating Junction and Storage Temperature Range		T _J , T _{sta}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient	PCB Mount (TO-263) ^d	R _{thJA}	60	°C/W	
Junction-to-Case (Drain)		R _{thJC}	1.5	C/VV	

Notes:

- a. Package limited.
- b. Duty cycle ≤ 1 %.c. See SOA curve for voltage derating.
- d. When mounted on 1" square PCB (FR-4 material).

Rev. A

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{DS} = 0 V, I _D = 250 μA	100			V
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1		3	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current		V _{DS} = 100 V, V _{GS} = 0 V			1	μА
	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V, T _J = 85 °C			10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	100			Α
Drain-Source On-State Resistance ^a	D	V _{GS} = 10 V, I _D = 20 A		8.2	10	mΩ
	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 20 A		9.5	13	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 20 A		65		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 50 V, f = 1 MHz		2158		pF
Output Capacitance	C _{oss}			897		
Reverse Transfer Capacitance	C _{rss}			22		
Total Gate Charge ^c	Qg			32		nC
Gate-Source Charge ^c	Q _{gs}	V _{DS} = 50 V, V _{GS} = 10 V, I _D = 20 A		3.7		
Gate-Drain Charge ^c	Q _{gd}			1.2		
Gate Resistance	R _g			3.5		Ω
Turn-On Delay Time ^c	t _{d(on)}			12		
Rise Time ^c	t _r	V_{DD} = 50 V, R_{L} = 0.6 Ω I_{D} = 20 A, V_{GEN} = 10 V, R_{g} = 2.5 Ω		8		
Turn-Off Delay Time ^c	t _{d(off)}			25		ns
Fall Time ^c	t _f			5		
Source-Drain Diode Ratings and Cha	aracteristics 7	T _C = 25 °C ^b				
Continuous Current	Is				100	^
Pulsed Current	I _{SM}				300	A
Forward Voltage ^a	V _{SD}	I _F = 1 A, V _{GS} = 0 V		0.6	1.0	V
Reverse Recovery Time	t _{rr}			30		ns
Reverse Recovery Charge	Q _{rr}	I _F = 20 A, dI/dt = 100 A/μs		190		nC

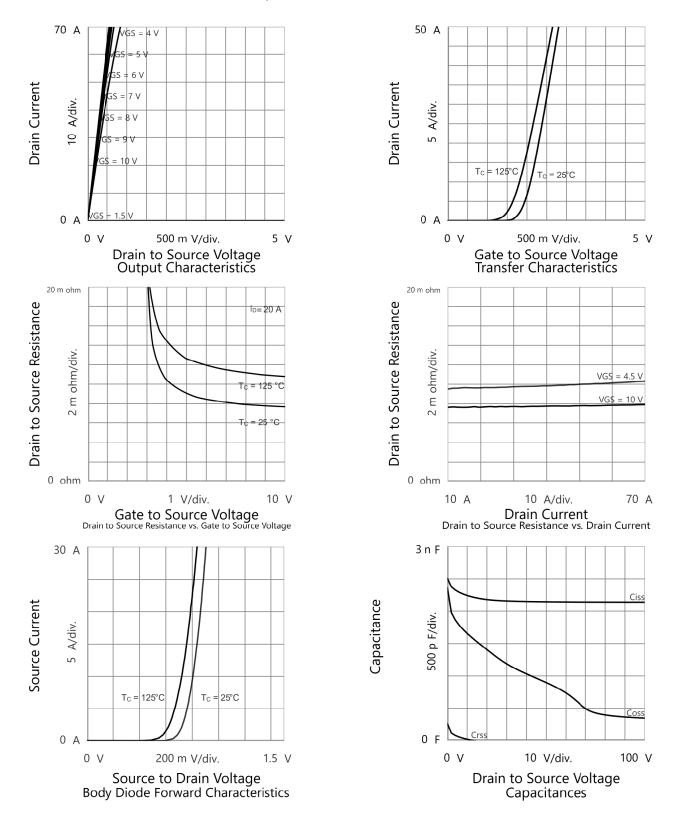
- a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing. c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



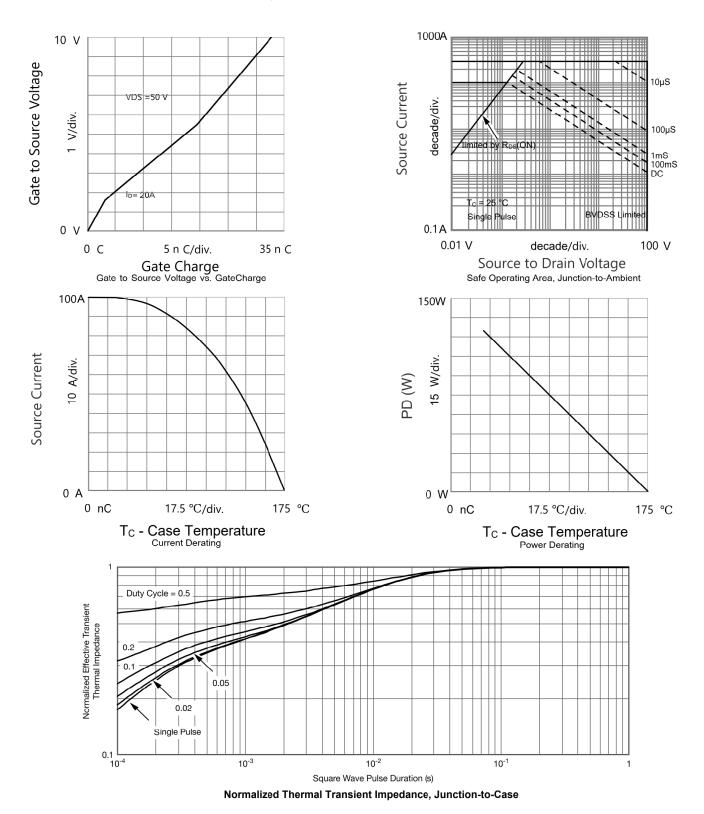


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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