

N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{(BR)DSS} (V)	$r_{DS(on)}(\Omega)$	I _D (A)		
100	0.018 at V _{GS} = 10 V	65 ^a		

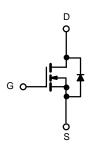
FEATURES

- DT-Trench Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 % R_g and UIS tested



APPLICATIONS

• Isolated DC/DC Converters



N-Channel MOSFET

D ² PAK (TO-263)
G D S
Top View

ABSOLUTE MAXIMUM RATINGS	$T_C = 25 ^{\circ}C$, unless oth	erwise noted			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	100	V	
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T _J = 175 °C)	T _C = 25 °C	I _D	65 ^a		
	T _C = 125 °C	d,	37 ^a	A	
Pulsed Drain Current		I _{DM}	195	Α	
Avalanche Current	L = 0.1 mH	I _{AS}	35		
Single Pulse Avalanche Energy ^b	L = 0.111111	E _{AS}	61	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C	P _D	375 ^c	W	
	T _A = 25 °C ^d	- FD	3.75	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient	PCB Mount (TO-263) ^d	R _{thJA}	40	°C/W	
Junction-to-Case (Drain)		R _{thJC}	0.4		

Notes:

- a. Package limited.
- b. Duty cycle \leq 1 %.
- c. See SOA curve for voltage derating.
- d. When Mounted on 1" square PCB (FR-4 material).

Rev. 1.0





Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	100			1	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		4	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		V _{DS} = 80 V , V _{GS} = 0 V			1	μA	
	I _{DSS}	V _{DS} = 80 V, V _{GS} = 0 V, T _J = 125 °C			50		
		$V_{DS} = 80 \text{ V} , V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$			250	1	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	65			Α	
Drain-Source On-State Resistance ^a		V _{GS} = 10 V, I _D = 20 A		0.018	0.025	Ω	
	r _{DS(on)}	V _{GS} = 10 V, I _D = 15 A, T _J = 125 °C		0.021	0.029		
		V _{GS} = 10 V, I _D = 10 A, T _J = 175 °C		0.024	0.033		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 80 V, I _D = 20 A		110		S	
Dynamic ^b	1			•			
Input Capacitance	C _{iss}			1298		pF	
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		280			
Reverse Transfer Capacitance	C _{rss}			960			
Total Gate Charge ^c	Q_g			90	130	nC	
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = 80 \text{ V}, \ \ V_{GS} = 10 \text{ V}, \ I_{D} = 20 \text{ A}$		23			
Gate-Drain Charge ^c	Q_{gd}			34			
Gate Resistance	R _g		0.5	1.7	3.3	Ω	
Turn-On Delay Time ^c	t _{d(on)}			24	35	- ns	
Rise Time ^c	t _r	$V_{DD} = 80 \text{ V}, R_L = 1.5 \Omega$ $I_D \cong 20 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		220	330		
Turn-Off Delay Time ^c	t _{d(off)}			45	70		
Fall Time ^c	t _f			200	300		
Source-Drain Diode Ratings and Cha	aracteristics 7	C _C = 25 °C ^b		•			
Continuous Current	Is				65	^	
Pulsed Current	I _{SM}				195	A	
Forward Voltage ^a	V _{SD}	I _F = 20 A, V _{GS} = 0 V		1.0	1.5	V	
Reverse Recovery Time	t _{rr}			130	200	ns	
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 20 A, di/dt = 100 A/μs		8	12	Α	
Reverse Recovery Charge	Q _{rr}			0.52	1.2	μC	

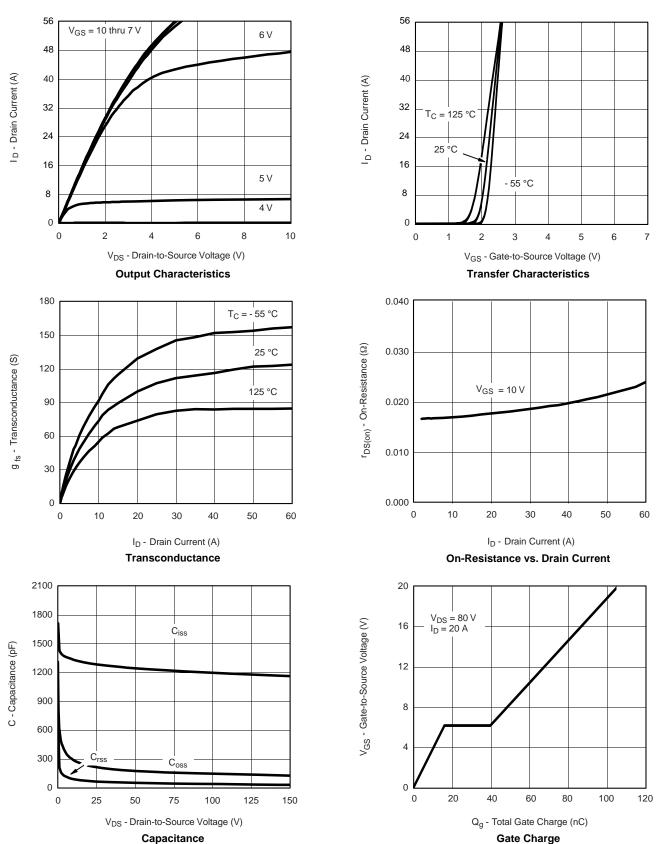
Notes:

- a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

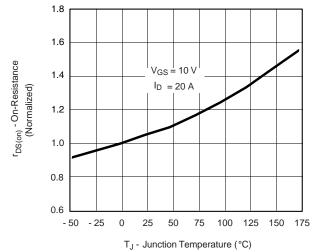


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

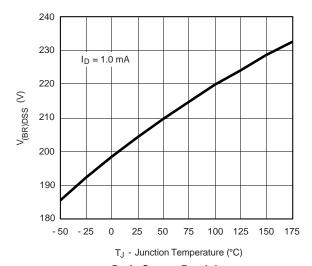




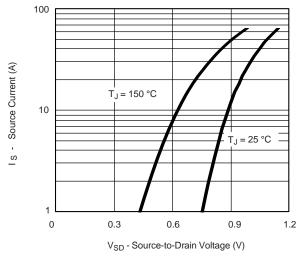
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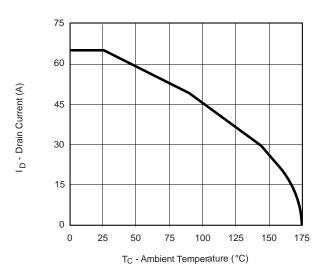
On-Resistance vs. Junction Temperature



Drain Source Breakdown vs. Junction Temperature



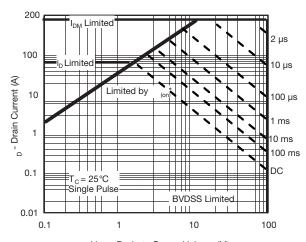
Source-Drain Diode Forward Voltage



Maximum Avalanche and Drain Current vs. Case Temperature

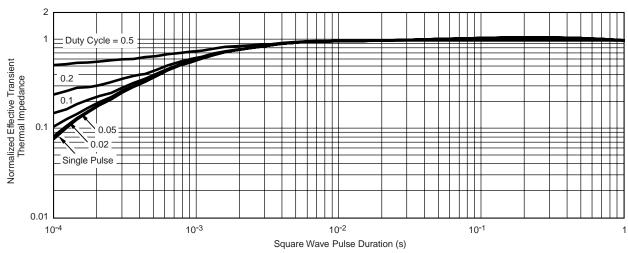


THERMAL RATINGS



 $\label{eq:VDS} $$V_{DS}$ - Drain-to-SourceVoltage (V) $$^*V_{GS} > minimum\,V_{GS}$ at which $r_{DS(on)}$ is specified$

Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case





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