

# N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY			
V <sub>(BR)DSS</sub> (V)	$r_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	
100	0.040 at V <sub>GS</sub> = 10 V	55 <sup>a</sup>	

#### **FEATURES**

- DT-Trench Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 %  $R_g$  and UIS Tested

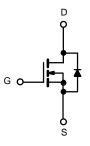


### **APPLICATIONS**

• Isolated DC/DC Converters



D<sup>2</sup>PAK



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	T <sub>C</sub> = 25 °C, unless oth	erwise noted		
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	100	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	
Continuous Drain Current (T <sub>J</sub> = 175 °C)	T <sub>C</sub> = 25 °C	1-	55 <sup>a</sup>	
	T <sub>C</sub> = 125 °C	I <sub>D</sub>	30 <sup>a</sup>	A
Pulsed Drain Current		I <sub>DM</sub>	165	7
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	30	
Single Pulse Avalanche Energy <sup>b</sup>	L = 0.111111	E <sub>AS</sub>	50	mJ
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	D.	350 <sup>c</sup>	W
	T <sub>A</sub> = 25 °C <sup>d</sup>	$ P_{D}$ $-$	3.50	VV
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient	PCB Mount (TO-263) <sup>d</sup>	R <sub>thJA</sub>	45	°C/W	
Junction-to-Case (Drain)		R <sub>thJC</sub>	0.45		

#### Notes:

- a. Package limited.
- b. Duty cycle  $\leq$  1 %.
- c. See SOA curve for voltage derating.
- d. When Mounted on 1" square PCB (FR-4 material).



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	100			V
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		4	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current		$V_{DS} = 80 \text{ V}$ , $V_{GS} = 0 \text{ V}$			1	μA
	I <sub>DSS</sub>	V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	
		V <sub>DS</sub> = 80 V , V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		0.040	0.055	Ω
	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A, T <sub>J</sub> = 125 °C		0.065	0.080	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A, T <sub>J</sub> = 175 °C		0.085	0.120	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 80 V, I <sub>D</sub> = 20 A		110		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			4980		pF
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		380		
Reverse Transfer Capacitance	C <sub>rss</sub>			210		
Total Gate Charge <sup>c</sup>	Qg			93		nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 80 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$		21		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			37		
Gate Resistance	R <sub>g</sub>			1.8		Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			24	35	- ns
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 80 V, $R_L$ = 1.5 $\Omega$ $I_D$ $\cong$ 20 A, $V_{GEN}$ = 10 V, $R_g$ = 2.5 $\Omega$		220	330	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			45	70	
Fall Time <sup>c</sup>	t <sub>f</sub>			200	300	
Source-Drain Diode Ratings and Cha	aracteristics 7	T <sub>C</sub> = 25 °C <sup>b</sup>				
Continuous Current	I <sub>S</sub>				55	A
Pulsed Current	I <sub>SM</sub>				165	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 20 A, V <sub>GS</sub> = 0 V		1.0	1.5	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs		110	180	ns
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>			7	11	Α
Reverse Recovery Charge	Q <sub>rr</sub>			0.49	1.0	μС

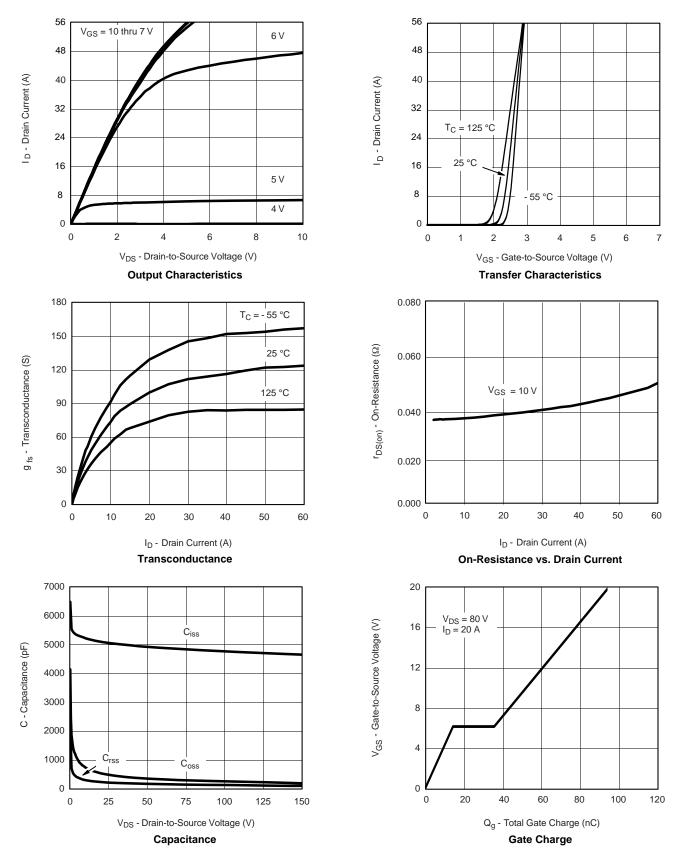
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

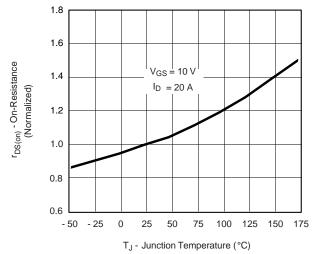


## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

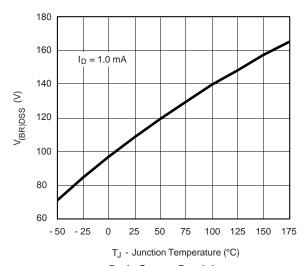




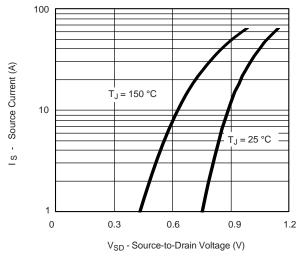
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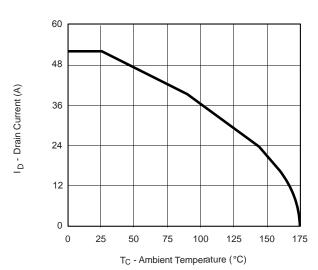
On-Resistance vs. Junction Temperature



Drain Source Breakdown vs. Junction Temperature



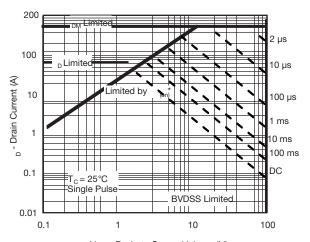
Source-Drain Diode Forward Voltage



Maximum Avalanche and Drain Current vs. Case Temperature

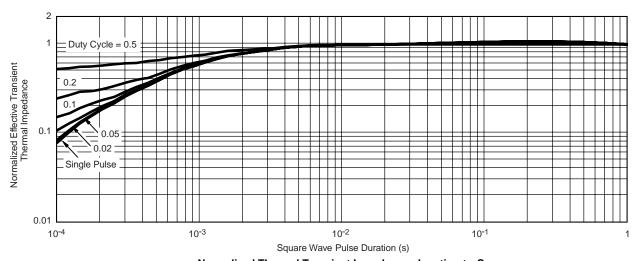


### **THERMAL RATINGS**



 $\label{eq:VDS} $$V_{DS}$ - Drain-to-SourceVoltage (V) $$^*V_{GS} > minimum\,V_{GS}$ at which $r_{DS(on)}$ is specified$ 

#### Safe Operating Area



 ${\bf Normalized\ Thermal\ Transient\ Impedance,\ Junction-to-Case}$ 





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