

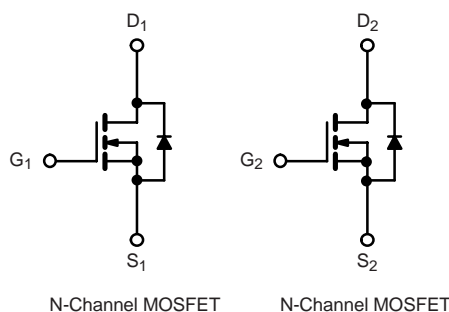
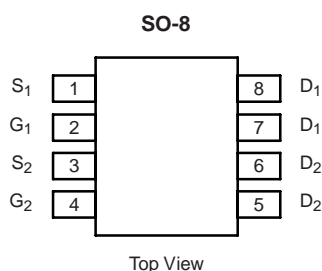
Dual N-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ.)
60	0.025 at $V_{GS} = 10$ V	8.9	9.5 nC
	0.030 at $V_{GS} = 4.5$ V	6.8	

FEATURES

- DT-Trench Power MOSFET
- 175 °C Maximum Junction Temperature
- 100 % R_g Tested
- Compliant to RoHS directive 2002/95/EC


RoHS
 COMPLIANT


ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150$ °C)	I_D	$T_C = 25$ °C	A
		$T_C = 70$ °C	
		$T_A = 25$ °C	
		$T_A = 70$ °C	
Pulsed Drain Current	I_{DM}	35	mJ
Continuous Source Drain Diode Current	I_S	$T_C = 25$ °C	
		$T_A = 25$ °C	
Avalanche Current	I_{AS}	35	
Single-Pulse Avalanche Energy	E_{AS}	20	
Maximum Power Dissipation	P_D	$T_C = 25$ °C	W
		$T_C = 70$ °C	
		$T_A = 25$ °C	
		$T_A = 70$ °C	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{a, c}	R_{thJA}	40	60	°C/W
Maximum Junction-to-Foot (Drain)	R_{thJF}	25	40	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

 b. $t = 10$ s.

c. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

d. Maximum under Steady State conditions is 110 °C/W.

SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	60			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		53		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 6.7		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.0	2.4	3.0	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 48 V, V _{GS} = 0 V			1	μA
		V _{DS} = 48 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	8.9			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 5.3 A		0.025	0.035	Ω
		V _{GS} = 4.5 V, I _D = 4.7 A		0.030	0.040	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 5.3 A		24		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = 30 V, V _{GS} = 0 V, f = 1 MHz		1940		pF
Output Capacitance	C _{oss}			105		
Reverse Transfer Capacitance	C _{rss}			40		
Total Gate Charge	Q _g	V _{DS} = 30 V, V _{GS} = 10 V, I _D = 5.3 A		17	25	nC
Gate-Source Charge	Q _{gs}	V _{DS} = 30 V, V _{GS} = 5 V, I _D = 5.3 A		9.5	12	
Gate-Drain Charge	Q _{gd}			3.3		
Gate Resistance	R _g			3.7		
Gate Resistance	R _g	f = 1 MHz	3.1	6.5	9.5	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 30 V, R _L = 6.8 Ω I _D ≅ 4.4 A, V _{GEN} = 4.5 V, R _g = 1 Ω		22	35	ns
Rise Time	t _r			125	188	
Turn-Off Delay Time	t _{d(off)}			18	33	
Fall Time	t _f			33	46	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 30 V, R _L = 6.8 Ω I _D ≅ 4.4 A, V _{GEN} = 10 V, R _g = 1 Ω		10	15	
Rise Time	t _r			12	21	
Turn-Off Delay Time	t _{d(off)}			25	40	
Fall Time	t _f			10	16	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			8.9	A
Pulse Diode Forward Current ^a	I _{SM}				35	
Body Diode Voltage	V _{SD}	I _S = 2 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 4.4 A, dI/dt = 100 A/μs, T _J = 25 °C		25	55	ns
Body Diode Reverse Recovery Charge	Q _{rr}			25	53	nC
Reverse Recovery Fall Time	t _a			19		ns
Reverse Recovery Rise Time	t _b			8		

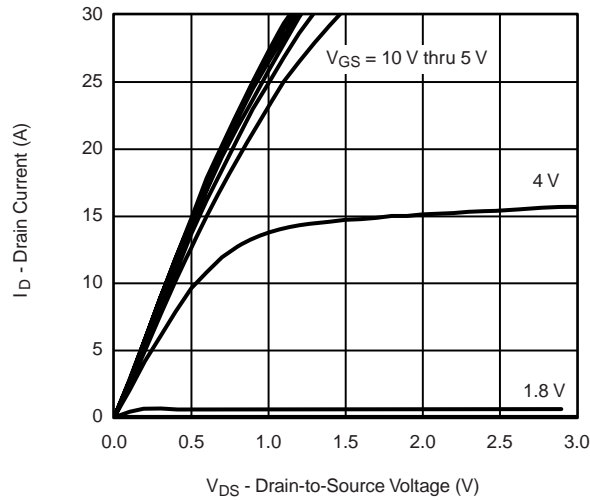
Notes:

 a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

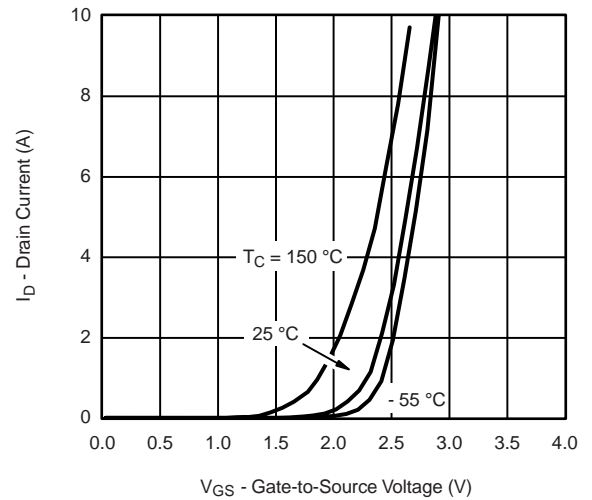
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

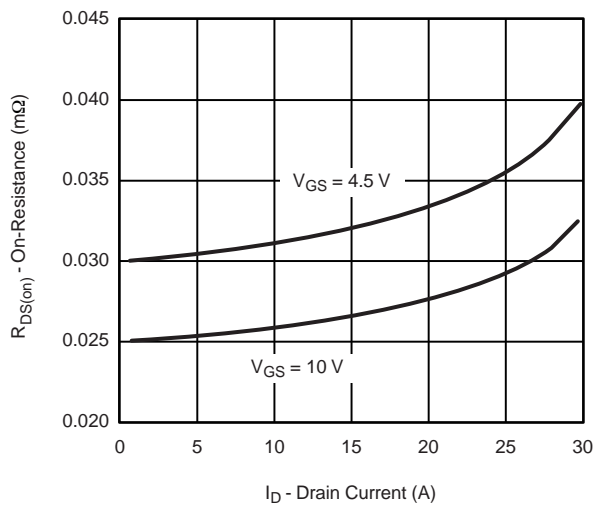
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



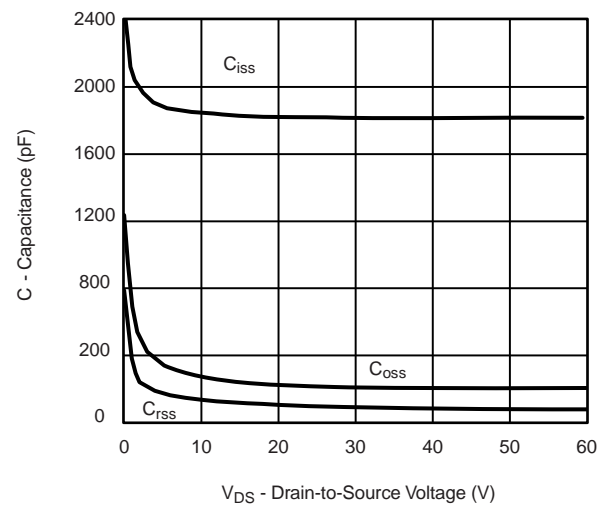
Output Characteristics



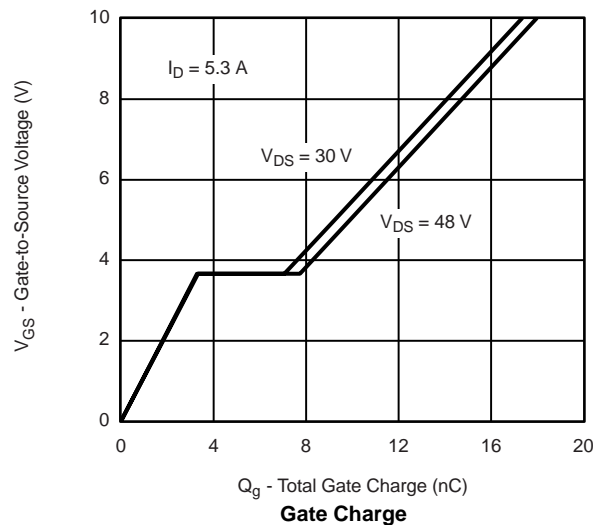
Transfer Characteristics



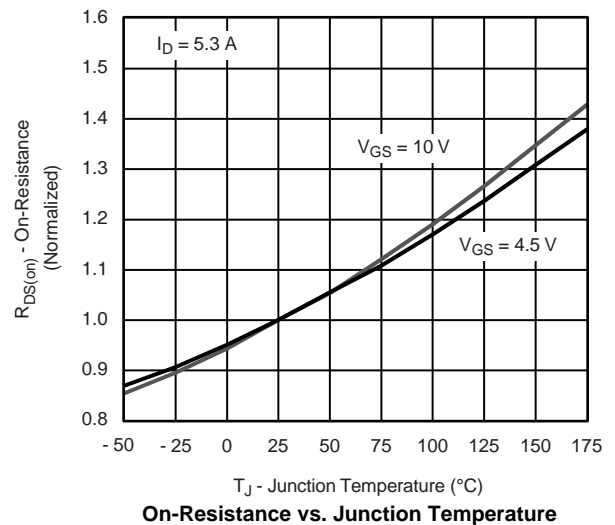
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

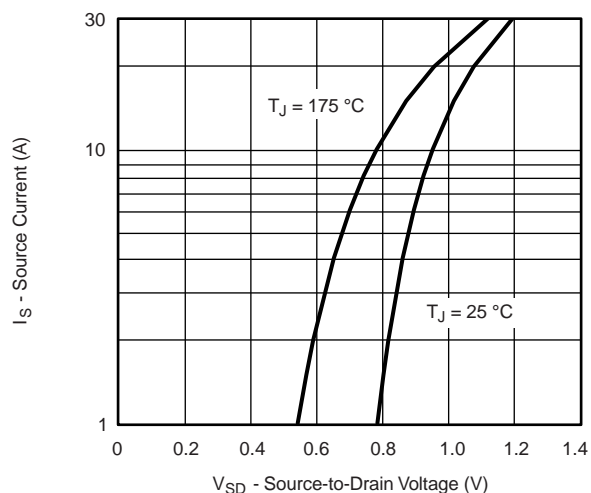


Gate Charge

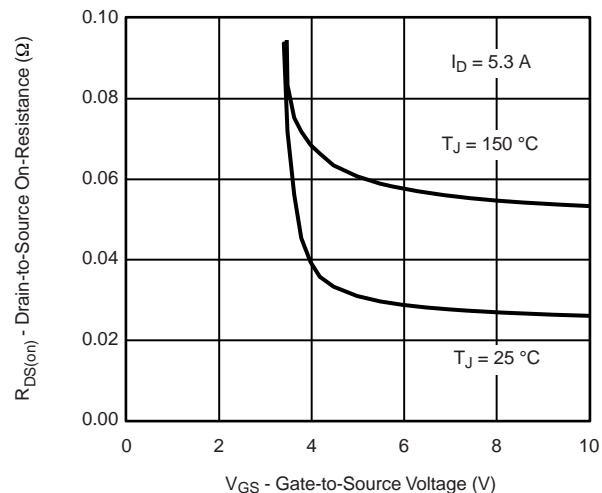


On-Resistance vs. Junction Temperature

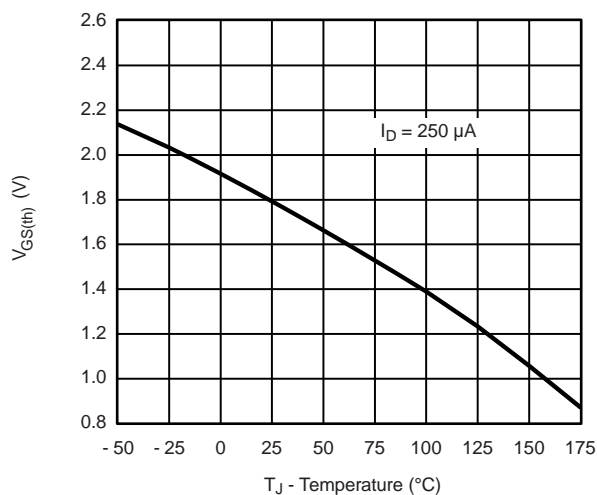
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



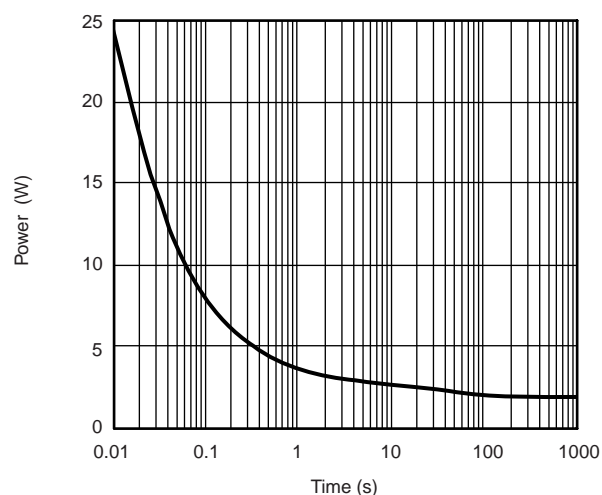
Source-Drain Diode Forward Voltage



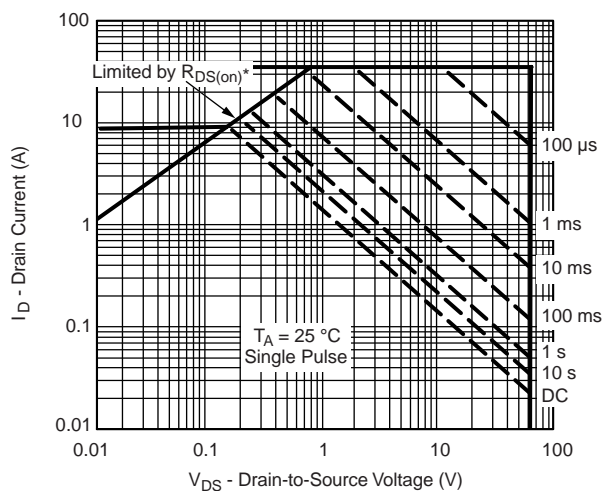
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



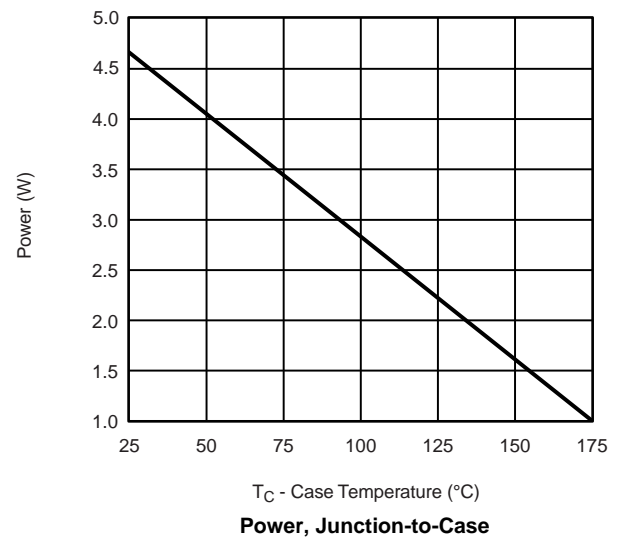
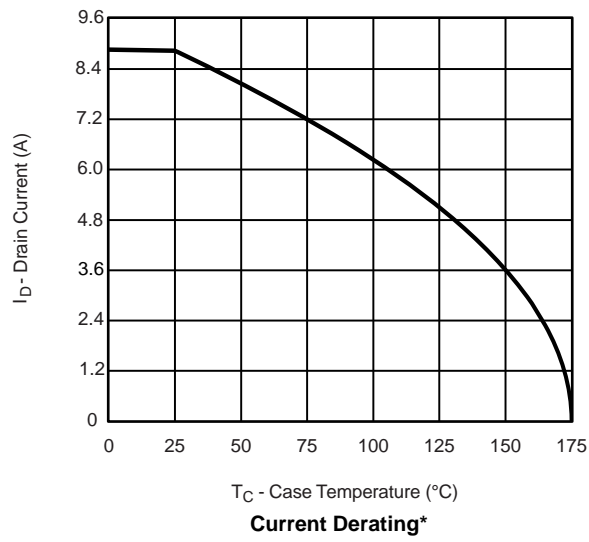
Single Pulse Power, Junction-to-Ambient



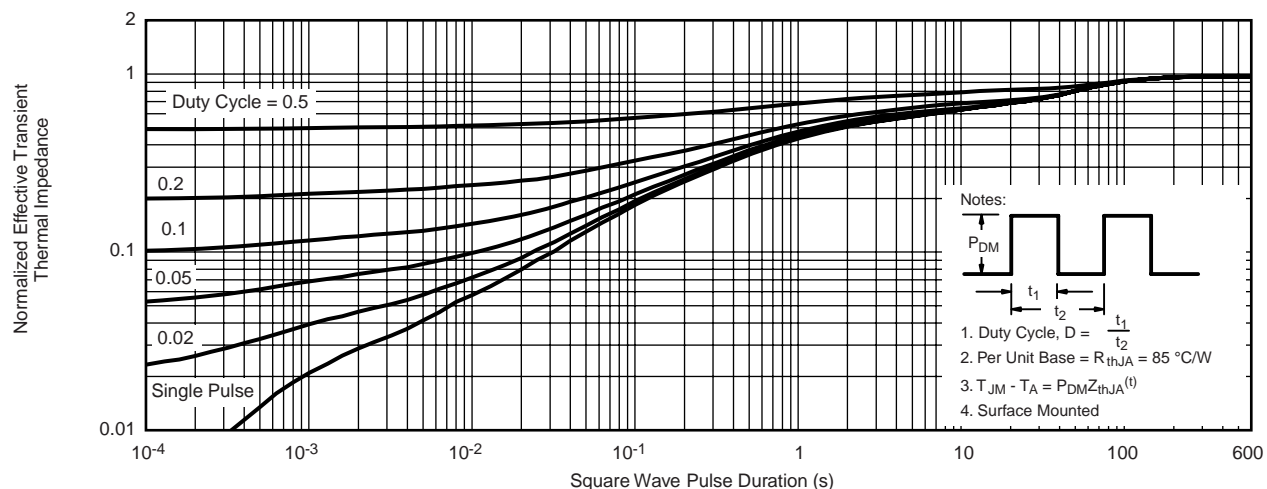
* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

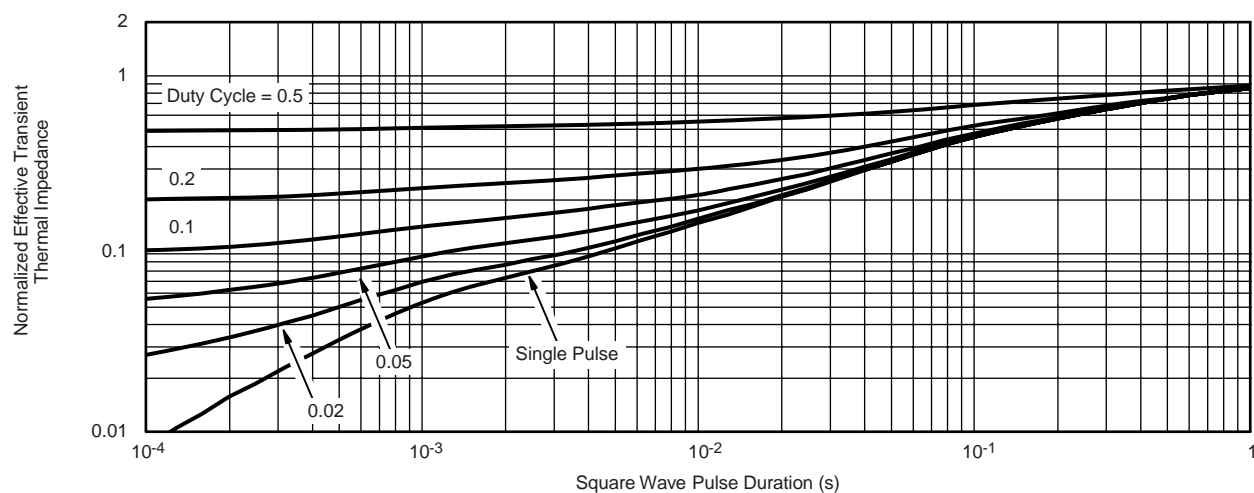
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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