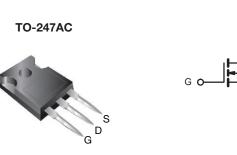


DTN18300 www.din-tek.jp

# N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω) MAX.	I <sub>D</sub> (A)	Q <sub>g</sub> (TYP.)
100	0.0019 at V <sub>GS</sub> = 10 V	250	258 nC



N-Channel MOSFET

S

D

#### FEATURES

- DT-TrenchPower MOSFET
- Maximum 175 °C junction temperature
- 100 % R<sub>g</sub> and UIS tested

#### **APPLICATIONS**

- Power supplies:
  - Uninterruptible power supplies
  - AC/DC switch-mode power supplies
  - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- Hard Switched and High Frequency Circuits

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_c = 25 \text{ °C}$ , unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-Source Voltage	V <sub>DS</sub>	100	v		
Gate-Source Voltage	V <sub>GS</sub>	± 20	V		
	T <sub>C</sub> = 25 °C		250		
Continuous Drain Current ( $T_J = 150 \ ^{\circ}C$ )	T <sub>C</sub> = 125 °C	I <sub>D</sub>	189	٨	
Pulsed Drain Current (t = 100 µs)	I <sub>DM</sub>	1150	A		
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	153	1	
Single Avalanche Energy <sup>a</sup>		E <sub>AS</sub>	825	mJ	
Movimum Dower Dissinction a	T <sub>C</sub> = 25 °C	D	553 <sup>b</sup>	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 125 °C	- P <sub>D</sub>	184 <sup>b</sup>	v	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W	
Junction-to-Case (Drain)	R <sub>thJC</sub>	0.3		

Notes

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.

c. When mounted on 1" square PCB (FR4 material).



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<b>SPECIFICATIONS</b> ( $T_J = 25 \circ C$	C, unless otl	nerwise noted)				
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$ 10	100	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS},\ I_{D}=250\ \mu A$	2	-	4	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = ± 20 V	-	-	± 100	nA
Zaus Oata Maltana Dusis Ourses	Inco	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	1	1
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = 80 V, $V_{GS}$ = 0 V, $T_J$ = 125 $^\circ C$	-	-	100	μA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \geq 10 \text{ V},  V_{GS} = 10 \text{ V}$	250	-	-	А
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 50 \text{ A}$	-	0.0019	0.0025	Ω
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 50 \text{ A}$	-	330	-	S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>		-	19260	-	pF
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 50 V, f = 1 MHz	-	1664	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	399	-	
Total Gate Charge <sup>c</sup>	Qg		-	258	660	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 50 \text{ A}$	-	92	-	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>		-	105	-	
Gate Resistance	Rg	f = 1 MHz	-	1.0	-	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	35	-	
Rise Time <sup>c</sup>	tr	$\label{eq:V_DD} \begin{array}{l} V_{\text{DD}} = 50 \text{ V}, \ R_{\text{L}} = 1.67 \ \Omega \\ I_{\text{D}} \cong 50 \text{ A}, \ V_{\text{GEN}} = 10 \text{ V}, \ R_{\text{g}} = 1 \ \Omega \end{array}$	-	190	-	ns
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>		-	110	-	
Fall Time <sup>c</sup>	t <sub>f</sub>		-	102	-	
Drain-Source Body Diode Ratings a	nd Characteri	stics <sup>b</sup> (T <sub>C</sub> = 25 °C)	·	<u>.</u>		
Pulsed Current (t = 100 µs)	I <sub>SM</sub>		-	-	1150	А
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_F = 30 \text{ A}, V_{GS} = 0 \text{ V}$	-	-	1.2	V
Reverse Recovery Time	t <sub>rr</sub>	l <sub>F</sub> = 50 A, di/dt = 100 A/μs	-	95	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>		-	350	-	μC

#### Notes

a. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%.$ 

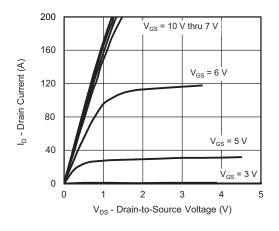
b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

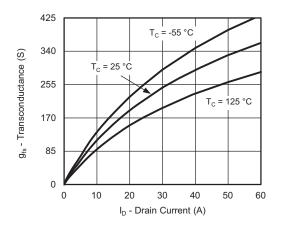
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



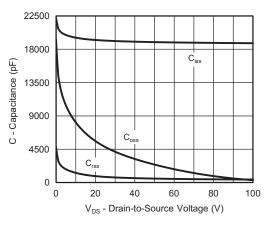
### **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



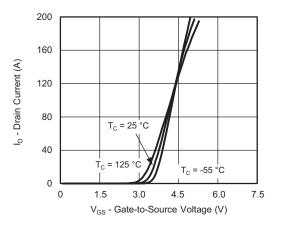
**Output Characteristics** 



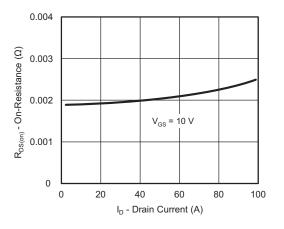
Transconductance



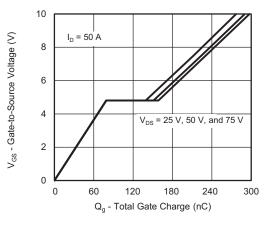
Capacitance



**Transfer Characteristics** 



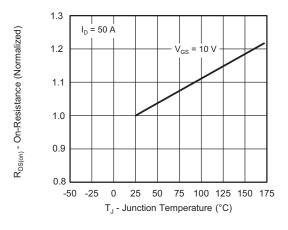
**On-Resistance vs. Drain Current** 



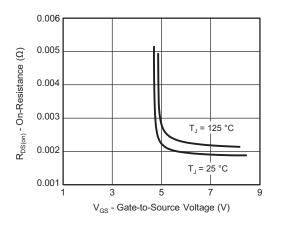
Gate Charge



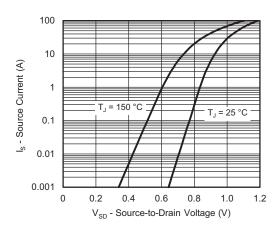
### **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



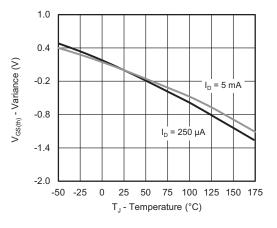
**On-Resistance vs. Junction Temperature** 



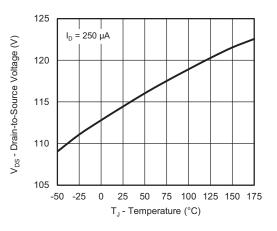
On-Resistance vs. Gate-to-Source Voltage



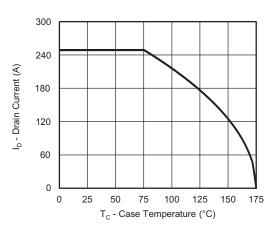
Source Drain Diode Forward Voltage



Threshold Voltage



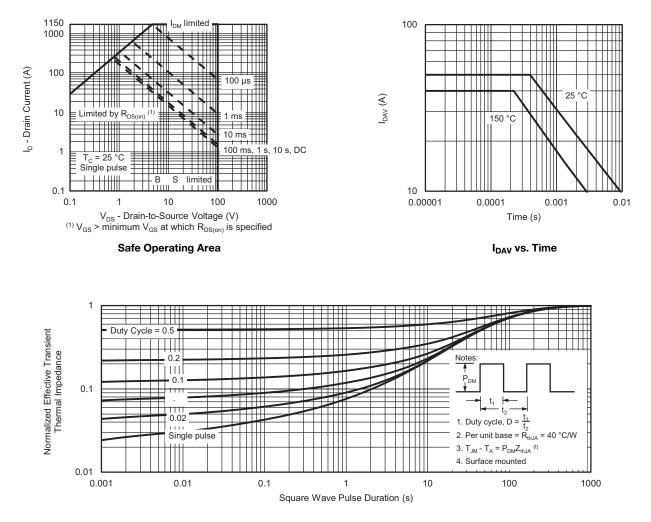
Drain Source Breakdown vs. Junction Temperature



**Current De-Rating** 



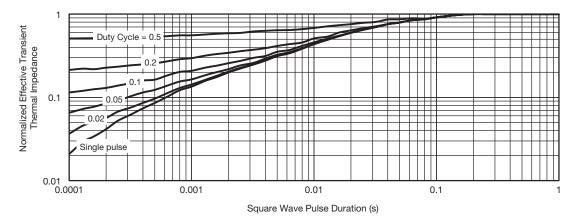
### **THERMAL RATINGS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



#### **THERMAL RATINGS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
- Normalized Transient Thermal Impedance Junction to Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



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