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N-Channel 55 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^d	Q _g (Typ.)	
55	0.0043 at $V_{GS} = 10 \text{ V}$	100	79	
33	0.0054 at $V_{GS} = 4.5 \text{ V}$	80	'9	

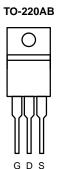
FEATURES

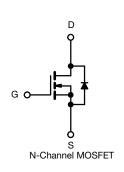
- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

- Power Supply
 - Secondary Synchronous Rectification
- DC/DC Converter





ABSOLUTE MAXIMUM RATINGS	(T _C = 25 °C, unless oth	nerwise noted)		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	55	V	
Gate-Source Voltage		V _{GS}	± 20	
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 25 °C	I _D	100 ^d	
Softinuous Drain Gunerit (1) = 130 G)	T _C = 70 °C	ا ^{ان} [80 ^d	A
Pulsed Drain Current		I _{DM}	350	٦ ^
Avalanche Current		I _{AS}	50	
Single Avalanche Energy ^a L = 0.1 mH		E _{AS}	170	mJ
Maximum Power Dissipation ^a	T _C = 25 °C	В	125 ^b	10/
	T _A = 25 °C ^c	$ P_D$ $-$	3.0	W
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Limit	Unit	
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	40	°C/W	
Junction-to-Case (Drain)	R _{thJC}	1	- C/VV	

Notes:

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR-4 material).
- d. Package limited.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	55			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1		4	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 250	nA
Zero Gate Voltage Drain Current		$V_{DS} = 55 \text{ V}, V_{GS} = 0 \text{ V}$			1	
	I _{DSS}	$V_{DS} = 55 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$			50	μA
		V _{DS} = 55 V, V _{GS} = 0 V, T _J = 150 °C			250	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α
Drain-Source On-State Resistance ^a		V _{GS} = 10 V, I _D = 22 A		0.0043	0.0048	Ω
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		0.0054	0.0060	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 20 A		159		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 20 V, f = 1 MHz		3286		pF
Output Capacitance	C _{oss}			705		
Reverse Transfer Capacitance	C _{rss}			283		
Total Gate Charge ^c	Q_g			87	131	nC
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = 20 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$		15.3		
Gate-Drain Charge ^c	Q_{gd}			12.2		
Gate Resistance	R _g	f = 1 MHz	0.5	2.7	5.4	Ω
Turn-On Delay Time ^c	t _{d(on)}			11	20	
Rise Time ^c	t _r	V_{DD} = 20 V, R_L = 2 Ω		7	14	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		45	68	ns
Fall Time ^c	t _f			7	14	
Drain-Source Body Diode Ratings ar	nd Characteris	stics T _C = 25 °C ^b				
Continuous Current	Is				100	
Pulsed Current	I _{SM}				350	Α
Forward Voltage ^a	V _{SD}	I _F = 10 A, V _{GS} = 0 V		0.72	1.2	V
Reverse Recovery Time	t _{rr}			42	63	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 10 A, dI/dt = 100 A/μs		2.5	3.8	Α
Reverse Recovery Charge	Q _{rr}			52	78	nC

Notes:

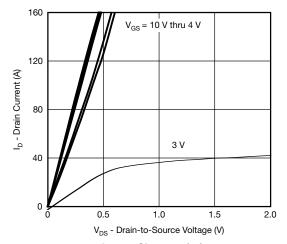
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$ b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

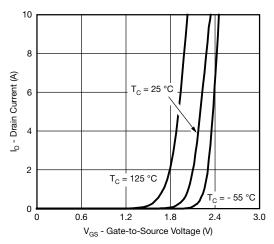




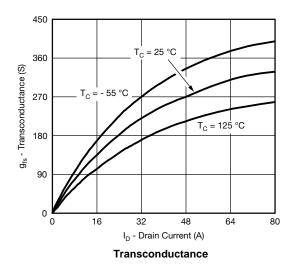
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

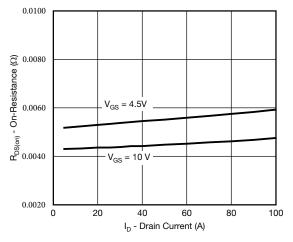


Output Characteristics

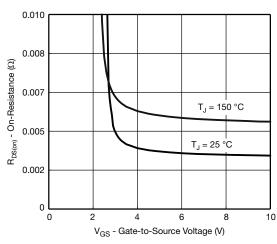


Transfer Characteristics

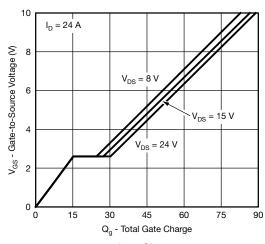




On-Resistance vs. Drain Current



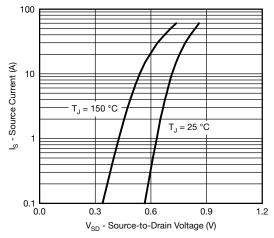
On-Resistance vs. Gate-to-Source Voltage



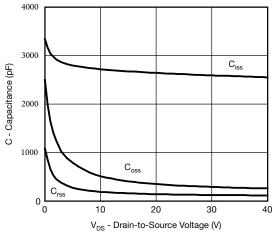
Gate Charge



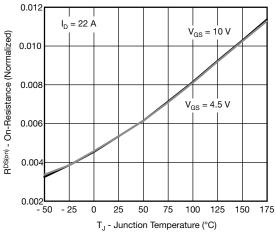
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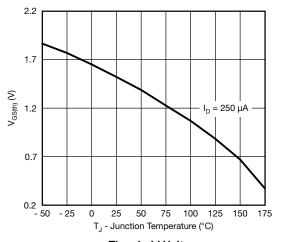
Source-Drain Diode Forward Voltage



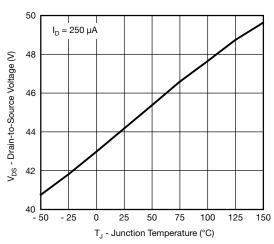
Capacitance



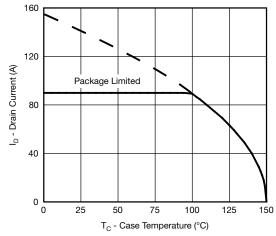
On-Resistance vs. Junction Temperature



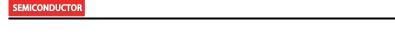
Threshold Voltage



Drain Source Breakdown vs. Junction Temperature

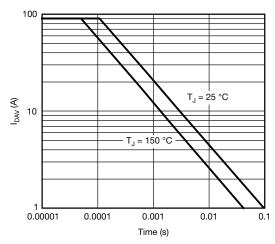


Current Derating

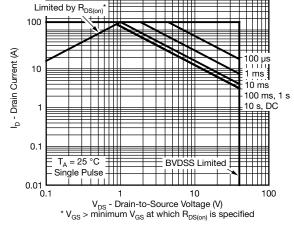


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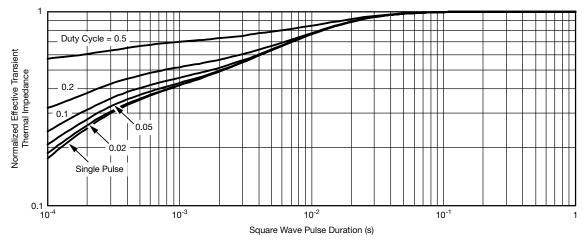
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Single Pulse Avalanche Current Capability vs. Time





Normalized Thermal Transient Impedance, Junction-to-Case





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