

P-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (m Ω)	I_D (A) ^a	Q_g (Typ.)
- 40	4.6 at $V_{GS} = - 10$ V	- 100	106 nC
	6.5 at $V_{GS} = - 4.5$ V	- 80	

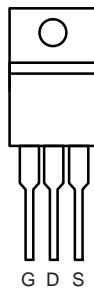
FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested

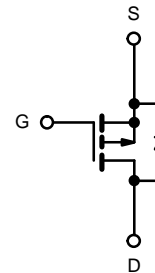

RoHS
 COMPLIANT

APPLICATIONS

- PWMApplications
- LoadSwitch
- PowerManagement

TO-220


Top View



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	- 40	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150^\circ\text{C}$)	I_D	$T_C = 25^\circ\text{C}$ - 100 ^a	A
		$T_C = 70^\circ\text{C}$ - 80 ^a	
		$T_A = 25^\circ\text{C}$ - 32 ^{b, c}	
		$T_A = 70^\circ\text{C}$ - 20 ^{b, c}	
Pulsed Drain Current	I_{DM}	- 400	
Continuous Source-Drain Diode Current	I_S	$T_C = 25^\circ\text{C}$ - 100 ^a	
		$T_A = 25^\circ\text{C}$ - 47 ^{b, c}	
Single Pulse Avalanche Current	I_{AS}	- 98	mJ
Single Pulse Avalanche Energy	E_{AS}	352	
Maximum Power Dissipation	P_D	$T_C = 25^\circ\text{C}$ 143	W
		$T_C = 70^\circ\text{C}$ 91.5	
		$T_A = 25^\circ\text{C}$ 7.55 ^{b, c}	
		$T_A = 70^\circ\text{C}$ 4.83 ^{b, c}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	$^\circ\text{C}$
Soldering Recommendations (Peak Temperature) ^d		260	

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	R_{thJA}	20	35	$^\circ\text{C/W}$
Maximum Junction-to-Case (Drain)	R_{thJC}	0.9	1.2	

Notes:

- Package limited.
- Surface Mounted on 1" x 1" FR4 board.
- $t = 10$ s.
- Maximum under Steady State conditions is 54 $^\circ\text{C/W}$.

SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 40			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = - 250 μA		- 30		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			6.3		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 1.0		- 3.0	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 40 V, V _{GS} = 0 V			- 1	μA
		V _{DS} = - 40 V, V _{GS} = 0 V, T _J = 55 °C			- 10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 100			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 20 A		4.6	5.9	mΩ
		V _{GS} = - 4.5 V, I _D = - 15 A		6.5	8.8	
Forward Transconductance ^a	g _{fs}	V _{DS} = - 15 V, I _D = - 20		57		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = - 20, V _{GS} = 0 V, f = 1 MHz		7090		pF
Output Capacitance	C _{oss}			678		
Reverse Transfer Capacitance	C _{rss}			390		
Total Gate Charge	Q _g	V _{DS} = - 20, V _{GS} = - 10 V, I _D = - 20 A		106		nC
Gate-Source Charge	Q _{gs}			19		
Gate-Drain Charge	Q _{gd}			27		
Gate Resistance	R _g	f = 1 MHz		3.9		Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = - 20 V, R _L = 15 Ω I _D ≡ - 20 A, V _{GEN} = - 10 V, R _g = 3Ω		20		ns
Rise Time	t _r			65		
Turn-Off Delay Time	t _{d(off)}			100		
Fall Time	t _f			93		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 100	A
Pulse Diode Forward Current ^a	I _{SM}				- 400	
Body Diode Voltage	V _{SD}	I _S = - 20 A		- 0.8	- 1.5	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = - 20 A, dI/dt = 100 A/μs, T _J = 25 °C		28		ns
Body Diode Reverse Recovery Charge	Q _{rr}			20		nC
Reverse Recovery Fall Time	t _a			16		ns
Reverse Recovery Rise Time	t _b			19		

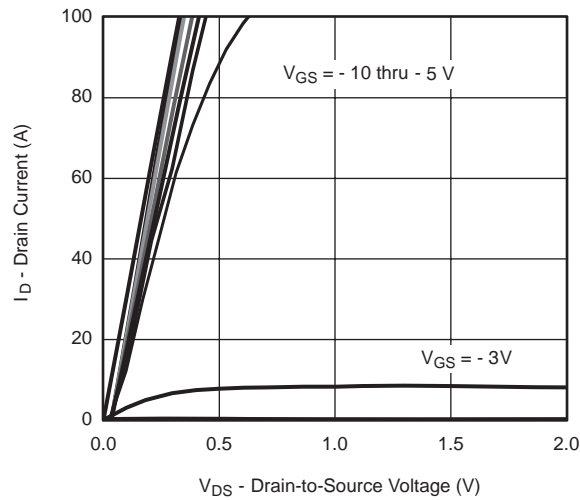
Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

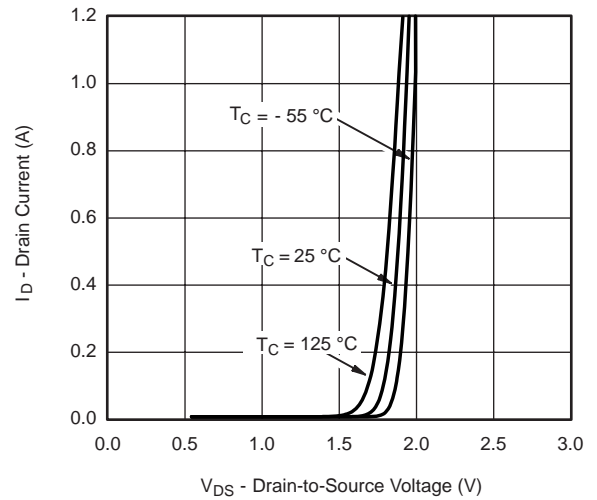
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

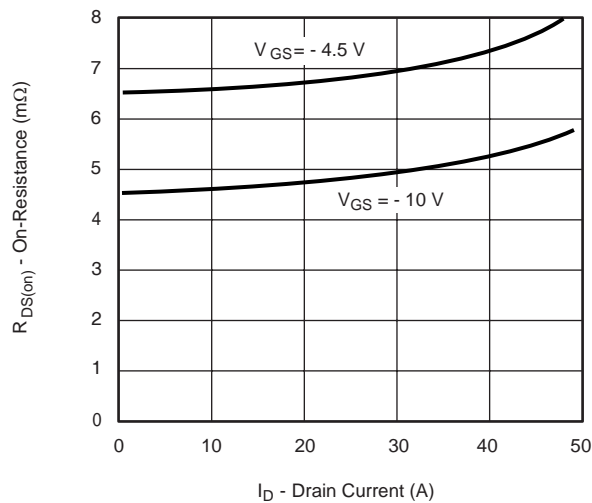
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



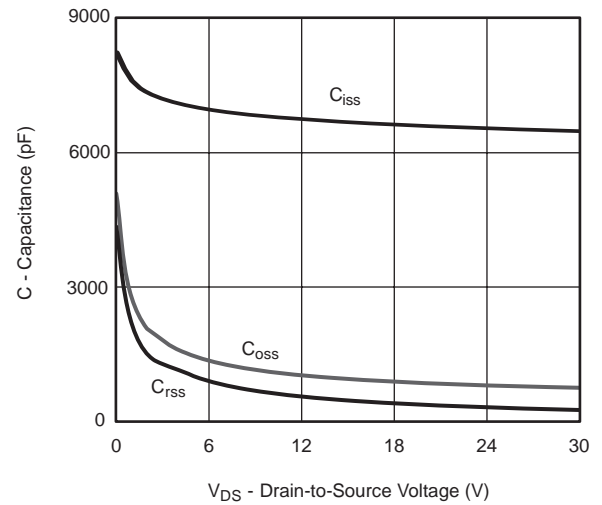
Output Characteristics



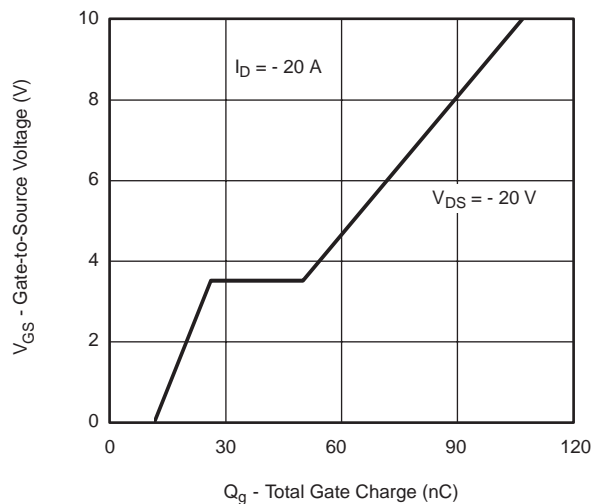
Transfer Characteristics



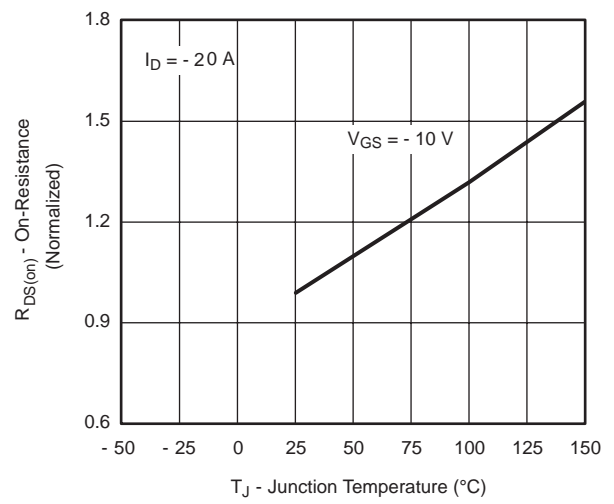
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

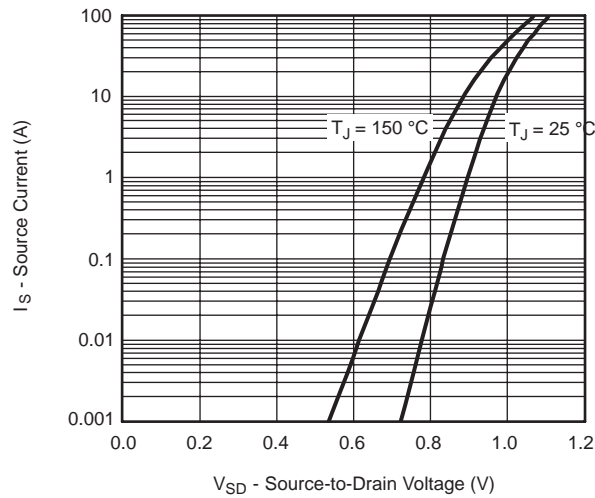


Gate Charge

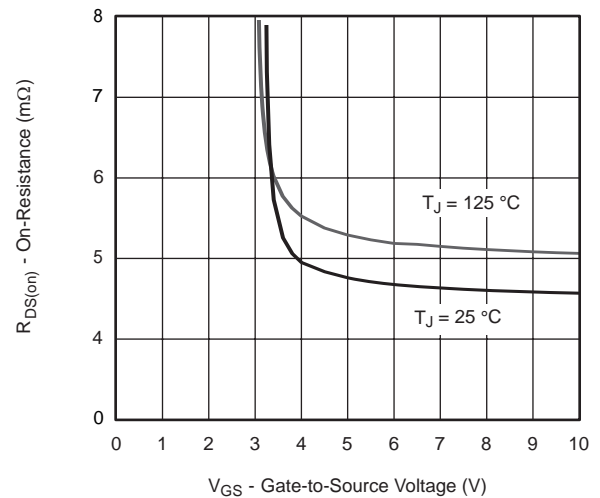


On-Resistance vs. Junction Temperature

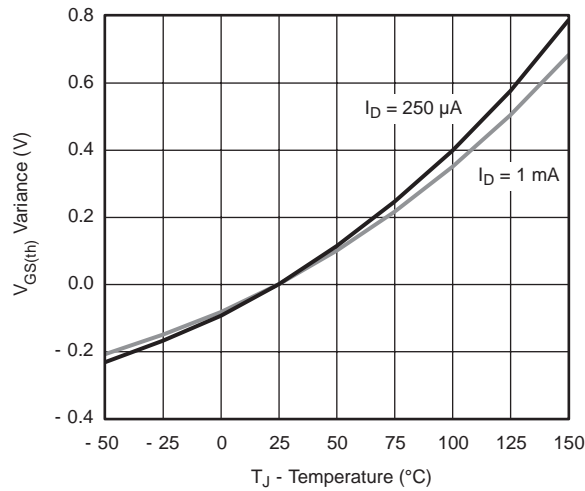
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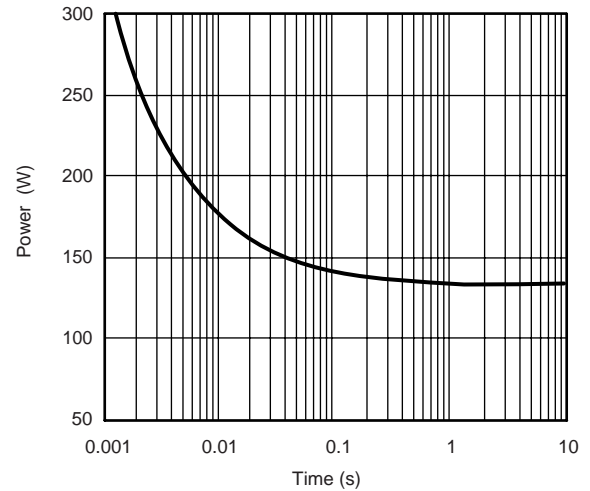
Source-Drain Diode Forward Voltage



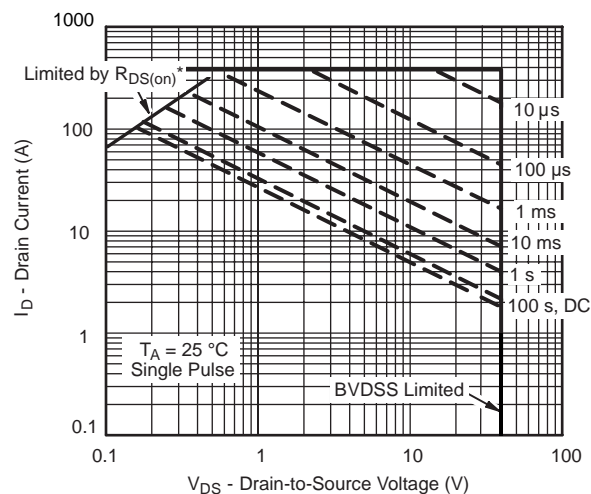
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

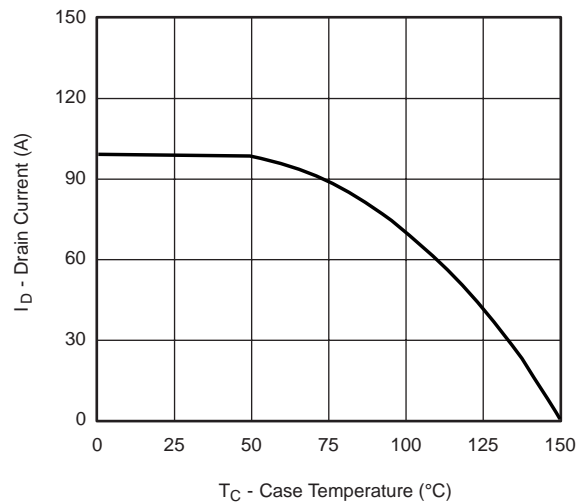


Single Pulse Power, Junction-to-Ambient

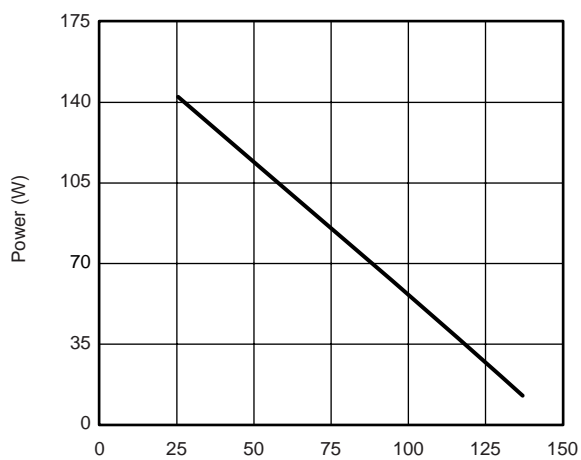


Safe Operating Area, Junction-to-Ambient
* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

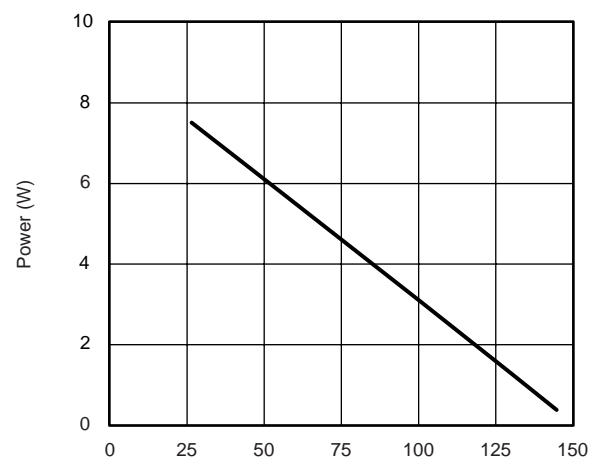
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Current Derating*



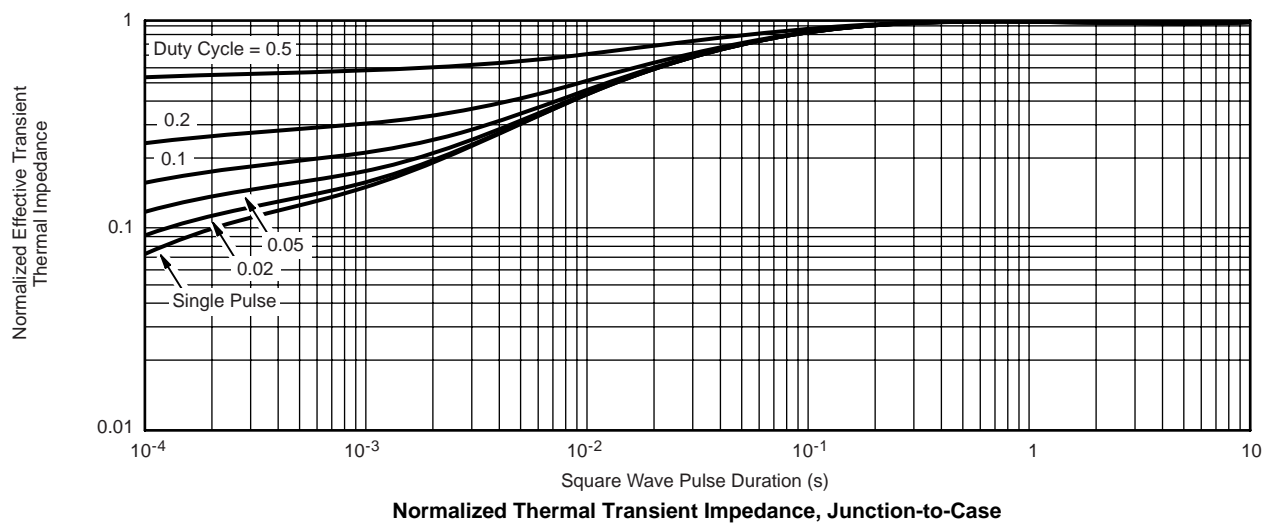
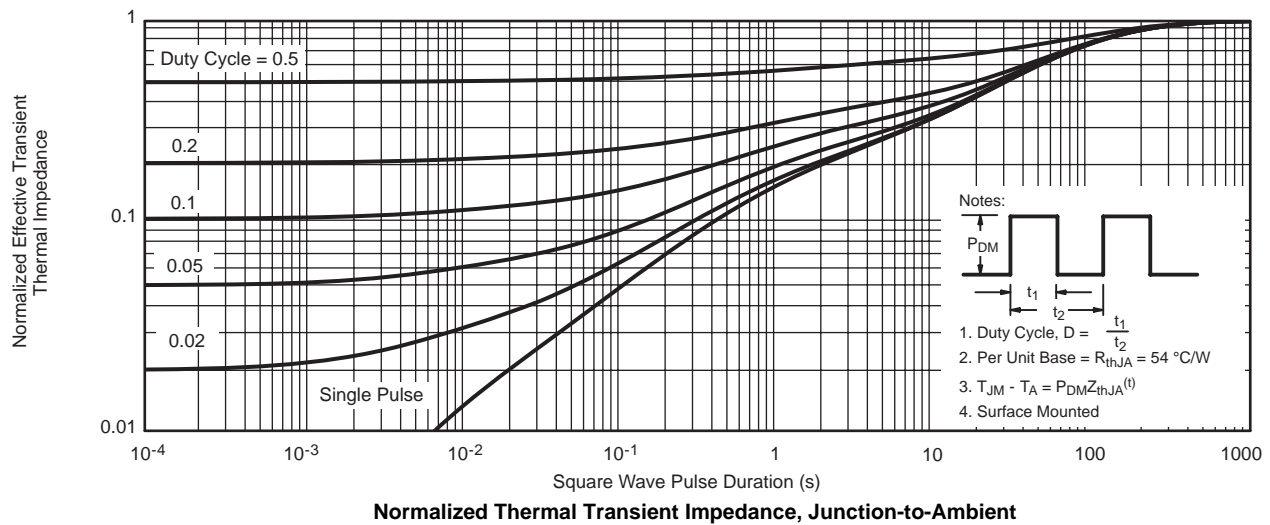
Power, Junction-to-Case



Power, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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