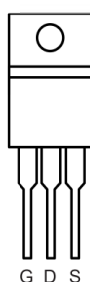


## N-Channel 100 V (D-S) MOSFET

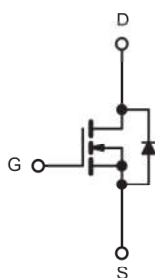
### PRODUCT SUMMARY

$V_{DS}$ (V)	$R_{DS(on)}$ (m $\Omega$ ) (TYP.)	$I_D$ (A)	$Q_g$ (TYP.)
100	1.7 at $V_{GS} = 10$ V	280	134 nC

TO-220AB



Top View



N-Channel MOSFET

### FEATURES

- DT-TrenchPower MOSFET
- 100 %  $R_g$  and UIS tested

### APPLICATIONS

- Power supplies:
  - Uninterruptible power supplies
  - AC/DC switch-mode power supplies
  - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- Battery management



**RoHS**  
COMPLIANT

### ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ , unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ )	$I_D$	$T_C = 25^\circ\text{C}$ 280	A
		$T_C = 100^\circ\text{C}$ 153	
Pulsed Drain Current ( $t = 100 \mu\text{s}$ )	$I_{DM}$	1120	
Avalanche Current	$I_{AS}$	155	
Single Avalanche Energy <sup>a</sup>	$E_{AS}$	2388	mJ
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_C = 25^\circ\text{C}$ 415 <sup>b</sup>	W
		$T_C = 100^\circ\text{C}$ 166 <sup>b</sup>	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

### THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) <sup>c</sup>	$R_{thJA}$	65	$^\circ\text{C/W}$
Junction-to-Case (Drain)	$R_{thJC}$	0.35	

#### Notes

- Duty cycle  $\leq 1\%$ .
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR4 material).

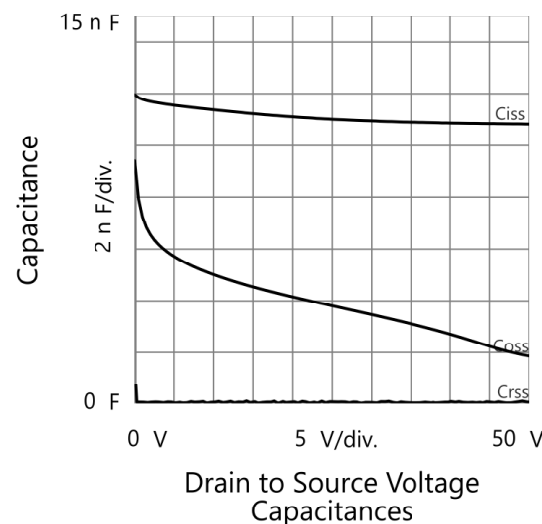
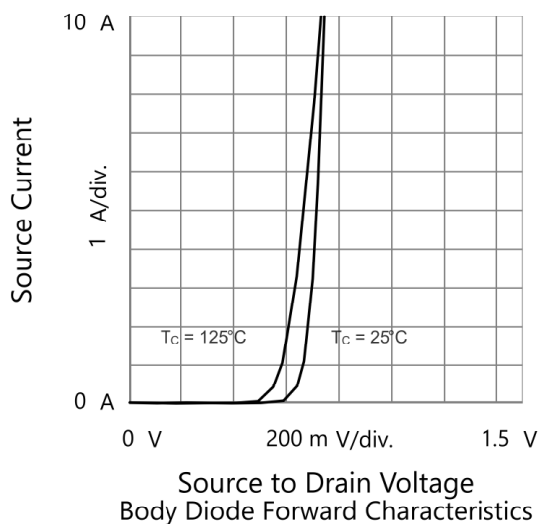
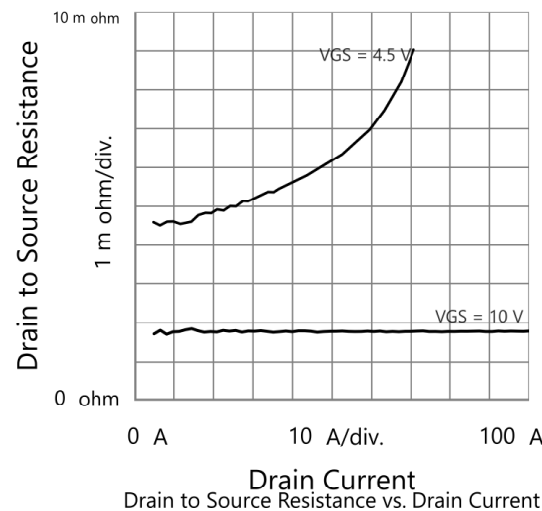
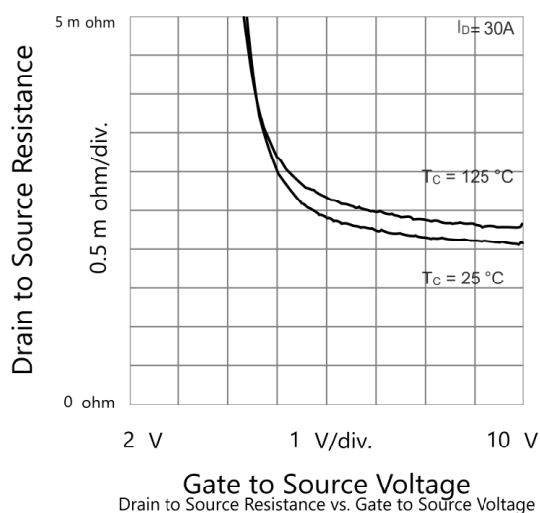
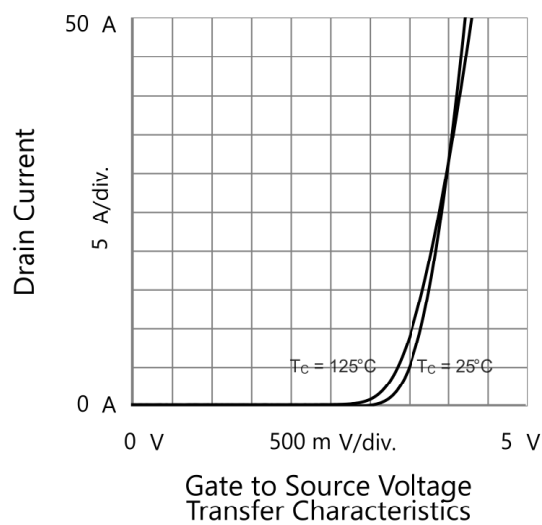
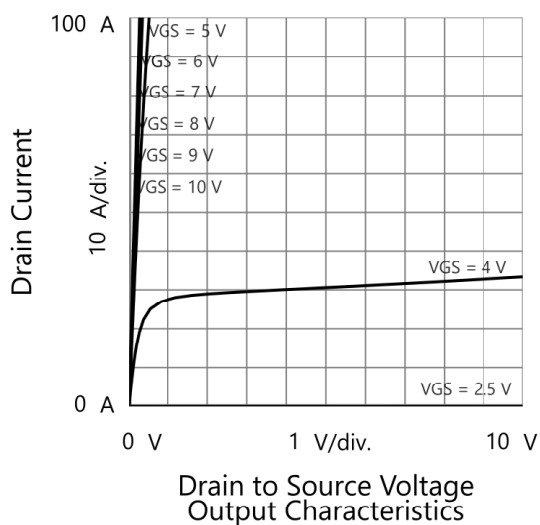
SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	100	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	2	-	4	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V	-	-	1	μA
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	-	-	10	
		V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C	-	-	2	mA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 10 V, V <sub>GS</sub> = 10 V	280	-	-	A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A	-	1.7	2.2	mΩ
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 30 A	-	65	-	S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 50 V, f = 1 MHz	-	10800	-	pF
Output Capacitance	C <sub>oss</sub>		-	1860	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	52	-	
Total Gate Charge <sup>c</sup>	Q <sub>g</sub>	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A	-	166	-	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>		-	36	-	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>		-	35	-	
Gate Resistance	R <sub>g</sub>	f = 1 MHz	-	1.4	-	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 50 V, R <sub>L</sub> = 1.67 Ω I <sub>D</sub> ≅ 30 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 1 Ω	-	43	-	ns
Rise Time <sup>c</sup>	t <sub>r</sub>		-	52	-	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>		-	107	-	
Fall Time <sup>c</sup>	t <sub>f</sub>		-	79	-	
Drain-Source Body Diode Ratings and Characteristics <sup>b</sup> (T <sub>C</sub> = 25 °C)						
Pulsed Current (t = 100 μs)	I <sub>SM</sub>		-	-	1120	A
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 30 A, V <sub>GS</sub> = 0 V	-	-	1.2	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 30 A, di/dt = 100 A/μs	-	88	-	ns
Peak Reverse Recovery Charge	I <sub>RM(REC)</sub>		-	5	-	A
Reverse Recovery Charge	Q <sub>rr</sub>		-	300	-	nC

**Notes**

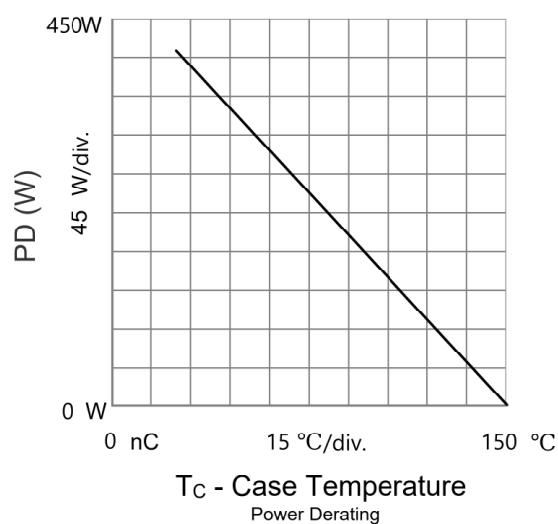
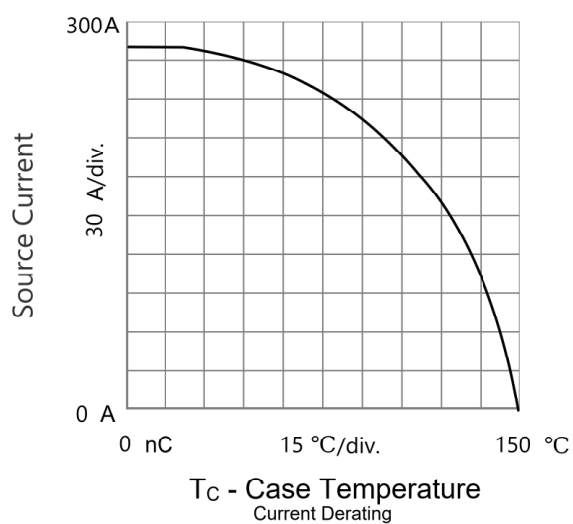
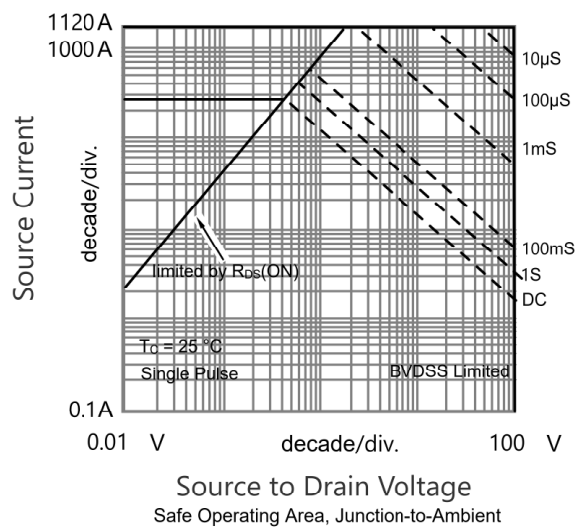
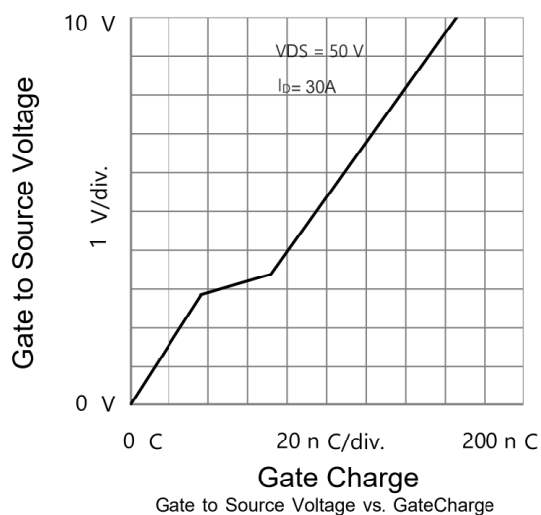
- Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

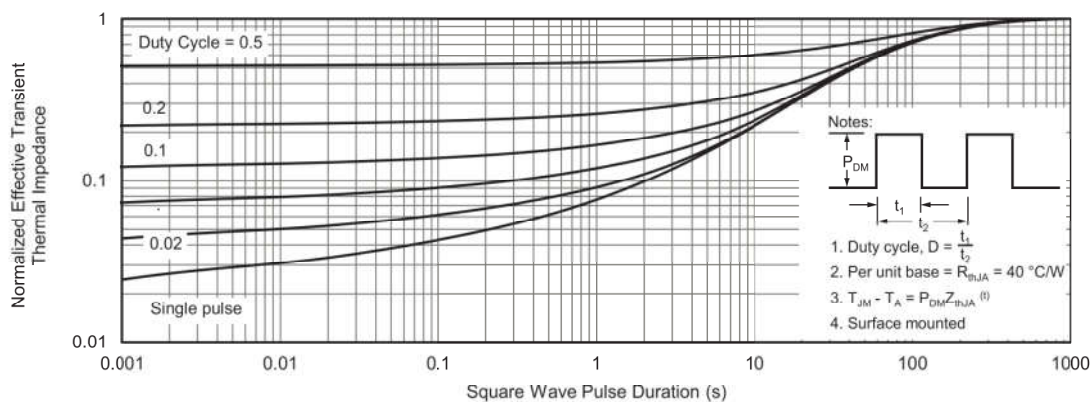
**TYPICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)



**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



**THERMAL RATINGS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient

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