

N-Channel 120 V (D-S) 175 °C MOSFET

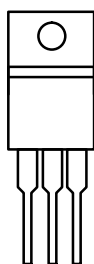
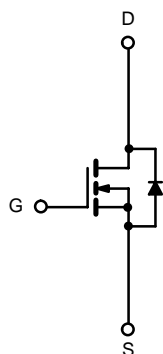
PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)
120	0.0029 at $V_{GS} = 10$ V	188 ^a

FEATURES

- TrenchFET® Power MOSFET
- New Package with Low Thermal Resistance
- 100 % R_g Tested


RoHS
 COMPLIANT

TO-220AB

 G D S
 Top View


N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_C = 25$ °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	120	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 175$ °C)	I_D	188 ^a	A
		143 ^a	
Pulsed Drain Current	I_{DM}	650	
Avalanche Current	I_{AR}	180	mJ
Repetitive Avalanche Energy ^b	E_{AR}	2200	
Maximum Power Dissipation ^b	P_D	398 ^c	W
		5.9	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient	R_{thJA}	38	°C/W
Junction-to-Case (Drain)	R_{thJC}	0.4	

Notes:

a. Package limited.

 b. Duty cycle ≤ 1 %.

c. See SOA curve for voltage derating.

d. When mounted on 1" square PCB (FR-4 material).

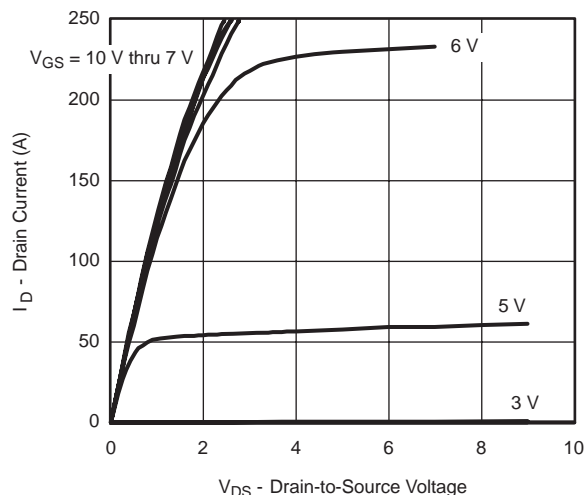
SPECIFICATIONS $T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{DS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	120			V
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2		4	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 100\text{ V}, V_{GS} = 0\text{ V}$			1	μA
		$V_{DS} = 100\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^{\circ}\text{C}$			50	
		$V_{DS} = 100\text{ V}, V_{GS} = 0\text{ V}, T_J = 175\text{ }^{\circ}\text{C}$			250	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	188			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 20\text{ A}$		0.0029	0.0040	Ω
		$V_{GS} = 10\text{ V}, I_D = 15\text{ A}, T_J = 125\text{ }^{\circ}\text{C}$			0.0053	
		$V_{GS} = 10\text{ V}, I_D = 15\text{ A}, T_J = 175\text{ }^{\circ}\text{C}$			0.0065	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15\text{ V}, I_D = 20\text{ A}$	68			S
Dynamic ^b						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = 60\text{ V}, f = 1\text{ MHz}$		8150		pF
Output Capacitance	C_{oss}			937		
Reverse Transfer Capacitance	C_{rss}			110		
Total Gate Charge ^c	Q_g	$V_{DS} = 60\text{ V}, V_{GS} = 10\text{ V}, I_D = 20\text{ A}$		110	150	nC
Gate-Source Charge ^c	Q_{gs}			46		
Gate-Drain Charge ^c	Q_{gd}			24		
Gate Resistance	R_g		1.0		6.3	Ω
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = 60\text{ V}, R_L = 0.6\text{ }\Omega$ $I_D \cong 20\text{ A}, V_{GEN} = 10\text{ V}, R_g = 2.5\text{ }\Omega$		22	33	ns
Rise Time ^c	t_r			102	180	
Turn-Off Delay Time ^c	$t_{d(off)}$			53	85	
Fall Time ^c	t_f			18	35	
Source-Drain Diode Ratings and Characteristics $T_C = 25\text{ }^{\circ}\text{C}$ ^b						
Continuous Current	I_S				188	A
Pulsed Current	I_{SM}				650	
Forward Voltage ^a	V_{SD}	$I_F = 20\text{ A}, V_{GS} = 0\text{ V}$		1.0	1.5	V
Reverse Recovery Time	t_{rr}	$I_F = 20\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		55	149	ns
Peak Reverse Recovery Charge	$I_{RM(REC)}$			5.3	10	A
Reverse Recovery Charge	Q_{rr}			0.15	0.37	μC

Notes:

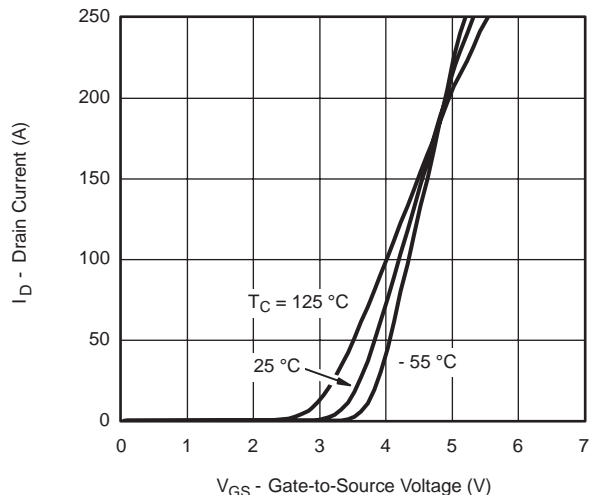
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

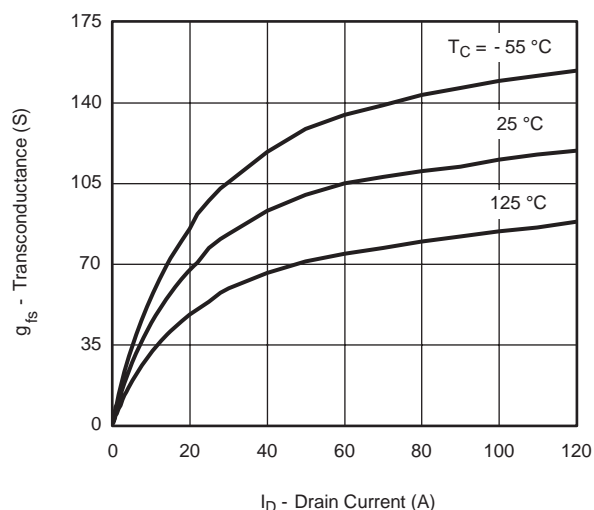
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



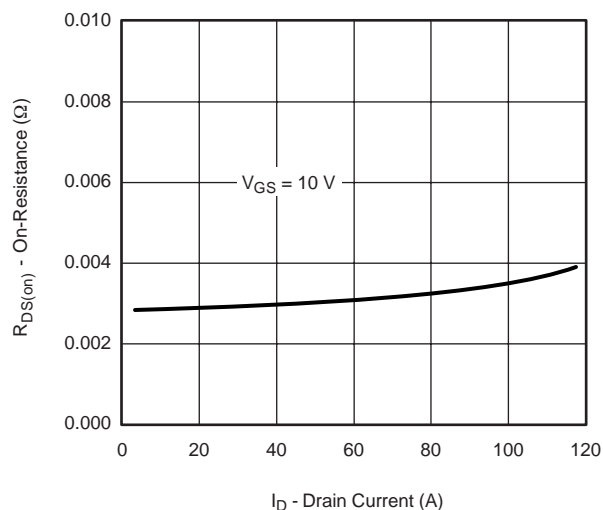
Output Characteristics



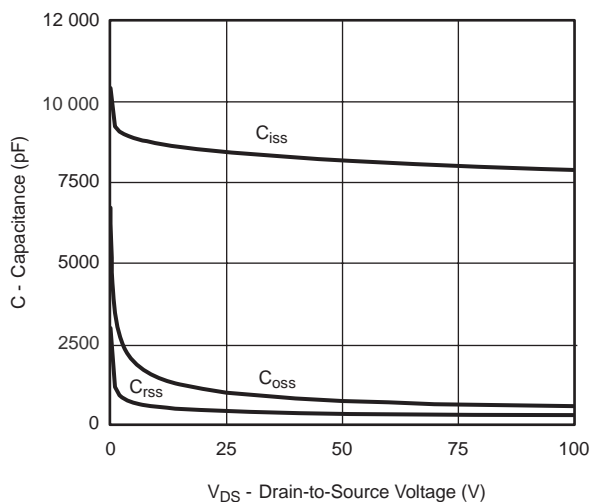
Transfer Characteristics



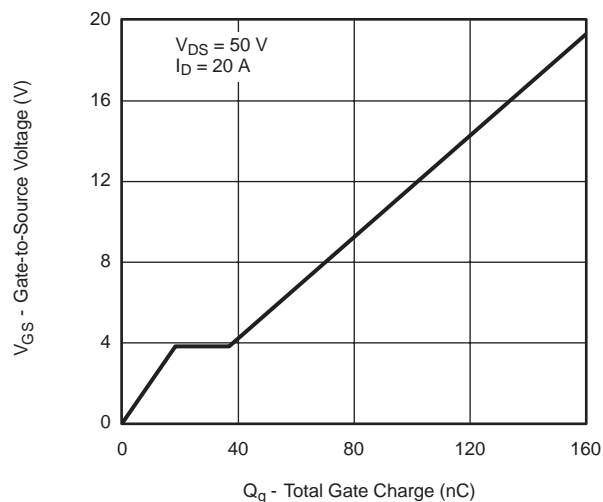
Transconductance



On-Resistance vs. Drain Current

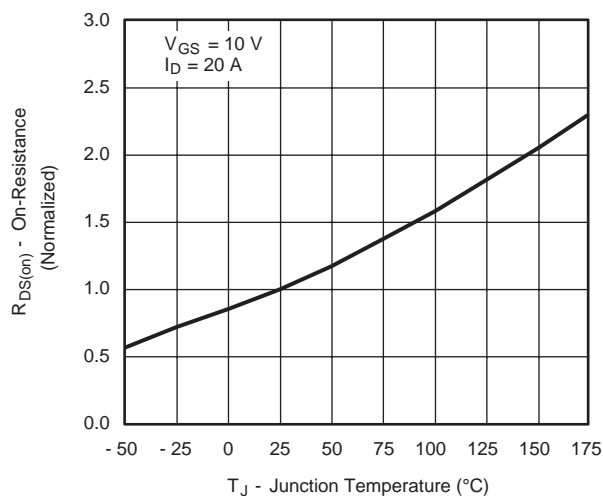


Capacitance

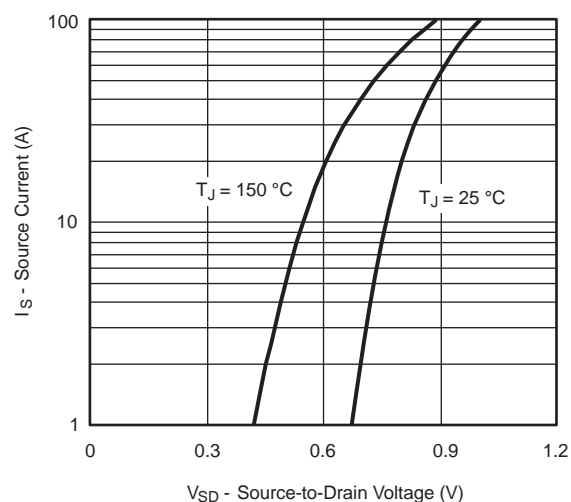


Gate Charge

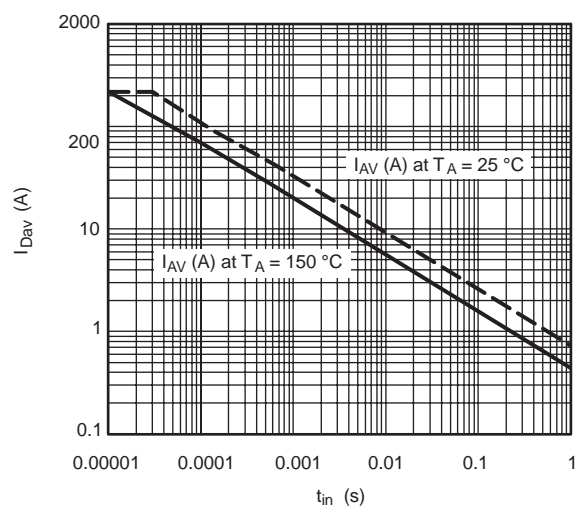
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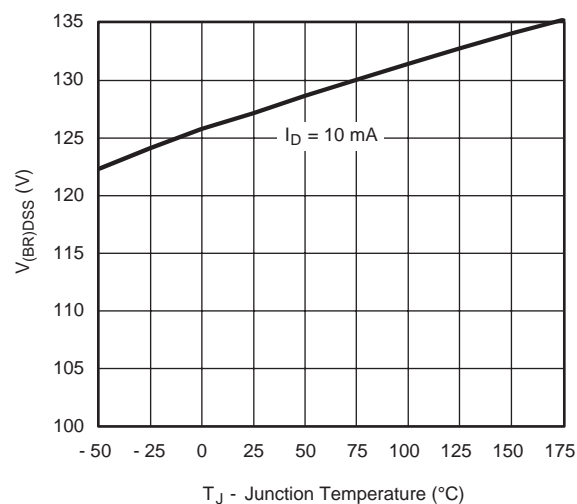
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

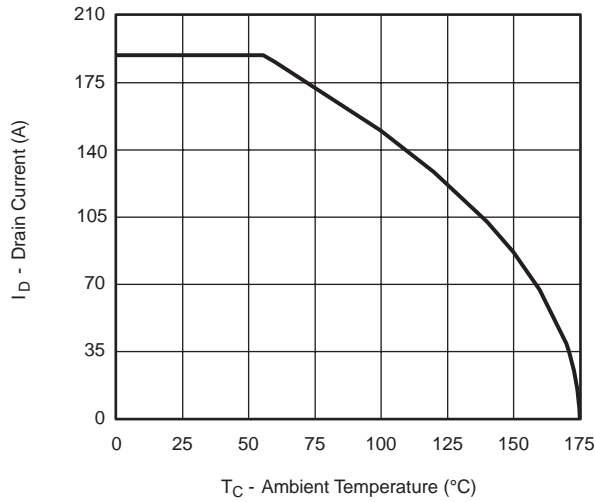


Avalanche Current vs. Time

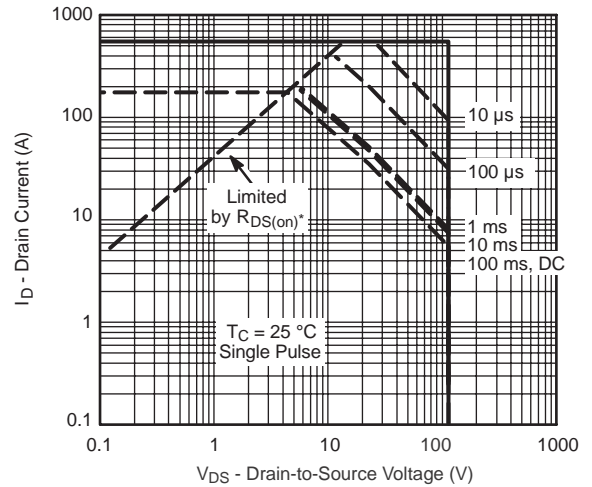


Drain Source Breakdown vs. Junction Temperature

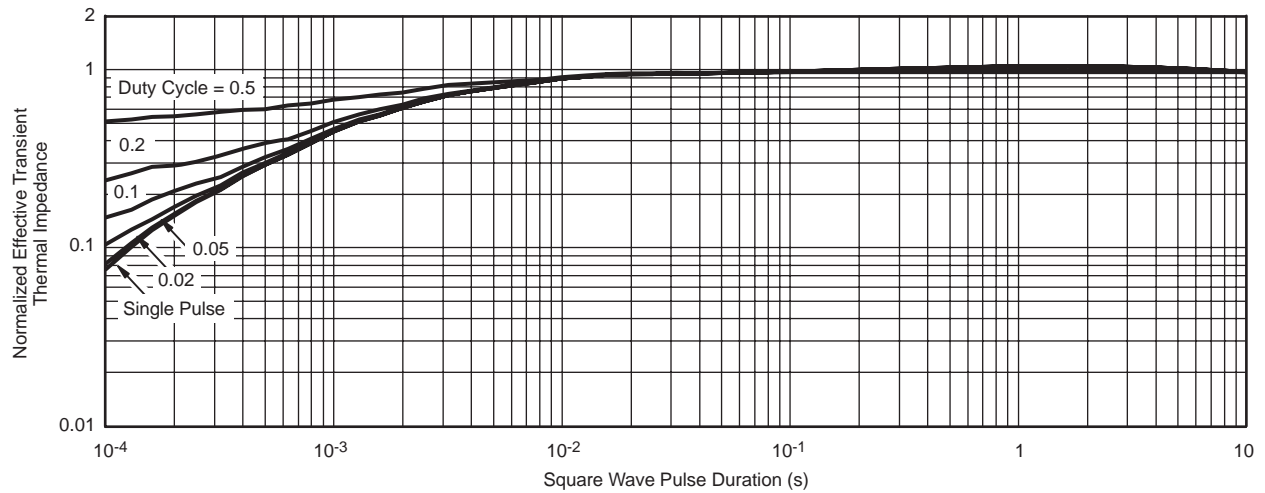
THERMAL RATINGS



**Maximum Avalanche and Drain Current
vs. Case Temperature**



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

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