

N-Channel 900 V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω) TYP.	I_D (A)	Q_g (Typ.)
900	4.6 at $V_{GS} = 10$ V	3	15

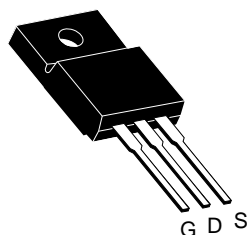
FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested
- Gate-Source ESD Protected

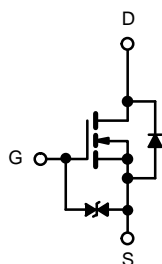

RoHS
 COMPLIANT

APPLICATIONS

- High efficient switched mode power supplies
- TV Power
- Adapter/charger
- Server Power
- LED Lighting

TO-220 FULLPAK


Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	900	V
Gate-Source Voltage	V_{GS}	± 30	
Continuous Drain Current	I_D	$T_C = 25$ °C	A
		$T_C = 100$ °C	
Pulsed Drain Current ($t = 300$ μ s)	I_{DM}	12	
Avalanche Current	I_{AS}	2.6	
Single Avalanche Energy ^a	E_{AS}	125	mJ
Maximum Power Dissipation ^a	P_D	$T_C = 25$ °C	W
		$T_A = 25$ °C ^c	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) ^c	R_{thJA}	65	°C/W
Junction-to-Case (Drain)	R_{thJC}	5.5	

Notes:

 a. Duty cycle ≤ 1 %.

b. See SOA curve for voltage derating.

c. When mounted on 1" square PCB (FR-4 material).

 d. Base on $T_C = 25$ °C.

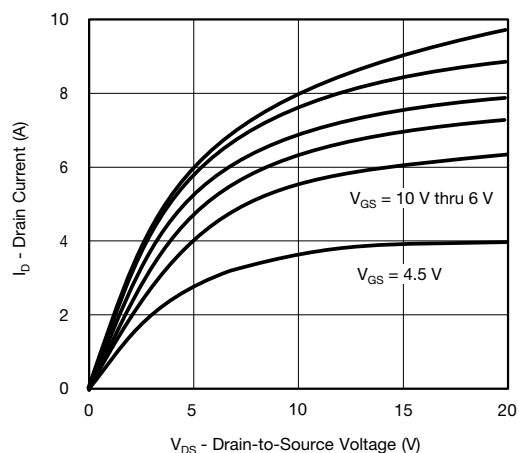
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	900			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2		4	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 30 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 900 V, V _{GS} = 0 V			25	μA
		V _{DS} = 900 V, V _{GS} = 0 V, T _J = 125 °C			100	
		V _{DS} = 900 V, V _{GS} = 0 V, T _J = 150 °C			250	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 10 V, V _{GS} = 10 V	3			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 2 A, T _J = 25 °C		4.6	5.5	Ω
		V _{GS} = 10 V, I _D = 2 A, T _J = 150 °C		8.8		
Forward Transconductance ^a	g _{fs}	V _{DS} = 20 V, I _D = 2 A		9		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		561		pF
Output Capacitance	C _{oss}			47		
Reverse Transfer Capacitance	C _{rss}			3.8		
Total Gate Charge ^c	Q _g	V _{DS} = 720 V, V _{GS} = 10 V, I _D = 2 A		15		nC
Gate-Source Charge ^c	Q _{gs}			2.6		
Gate-Drain Charge ^c	Q _{gd}			7.1		
Gate Resistance	R _g	f = 1 MHz		1		Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} =720 V, R _L = 9.6 Ω I _D ≅ 2 A, V _{GEN} = 10 V, R _g = 10Ω		8		ns
Rise Time ^c	t _r			12		
Turn-Off Delay Time ^c	t _{d(off)}			48		
Fall Time ^c	t _f			15		
Drain-Source Body Diode Ratings and Characteristics ^b T _C = 25 °C						
Continuous Current	I _S				3	A
Pulsed Current	I _{SM}				12	
Forward Voltage ^a	V _{SD}	I _F = 2 A, V _{GS} = 0 V		0.9		V
Reverse Recovery Time	t _{rr}	I _F = 1.5 A, dI/dt = 50 A/μs		626		ns
Peak Reverse Recovery Current	I _{RM(REC)}			6		A
Reverse Recovery Charge	Q _{rr}			3		μC

Notes:

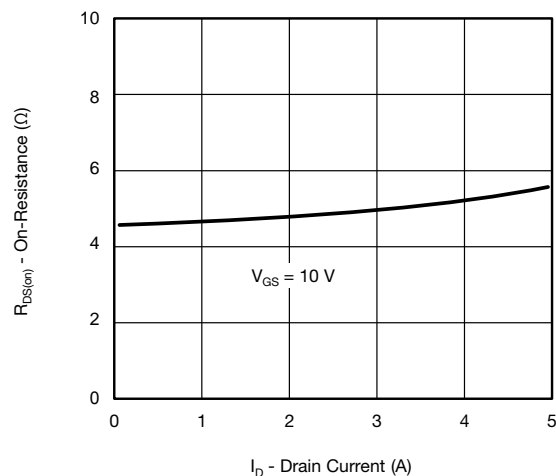
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

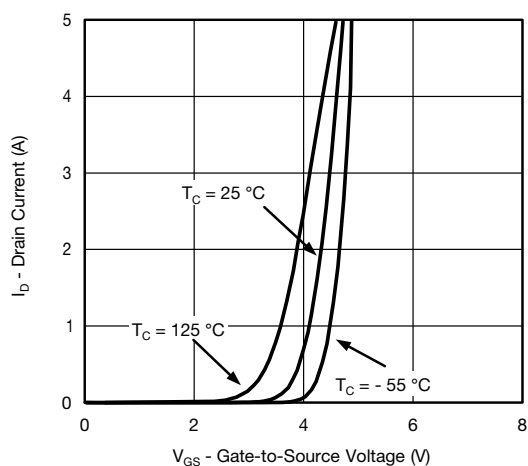
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



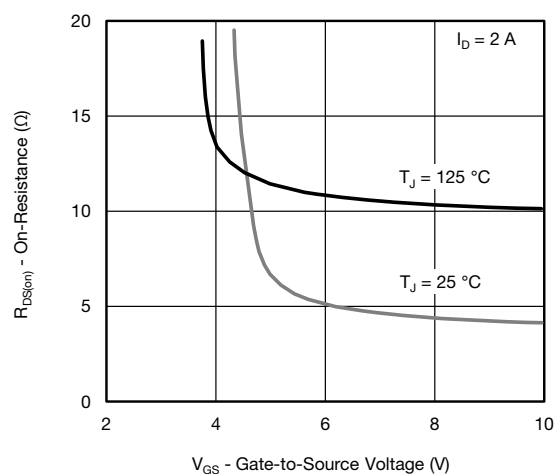
Output Characteristics



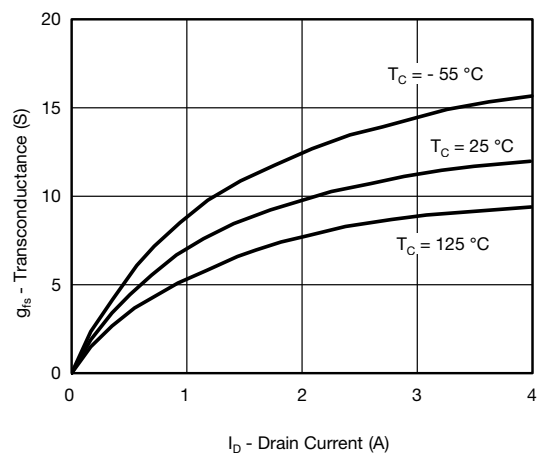
On-Resistance vs. Drain Current



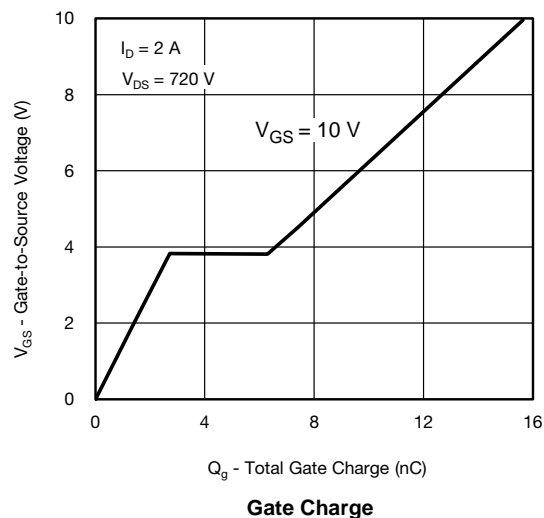
Transfer Characteristics



On-Resistance vs. Gate-to-Source Voltage

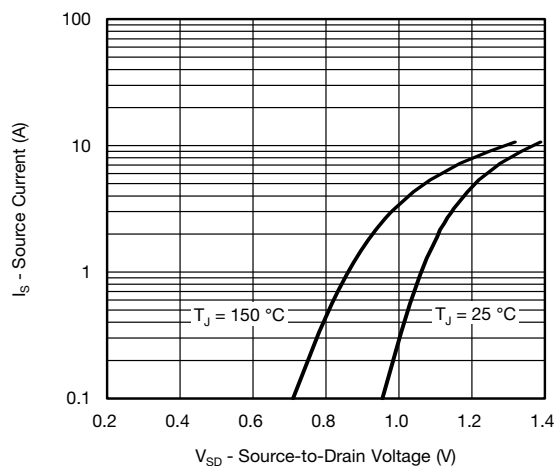


Transconductance

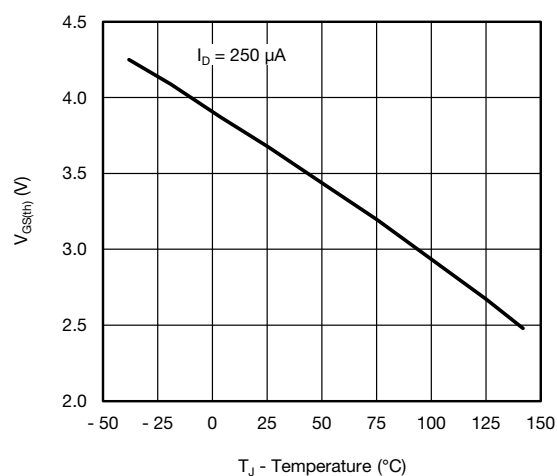


Gate Charge

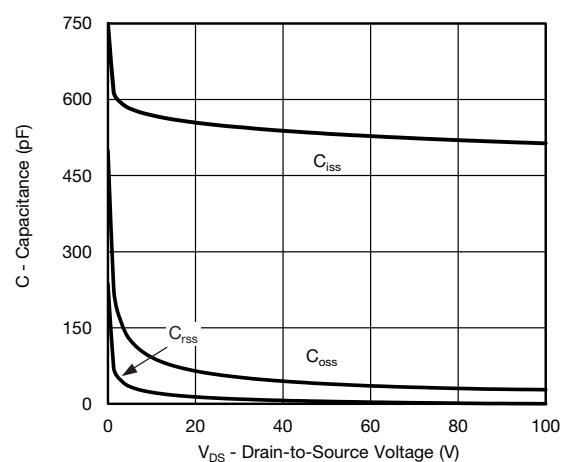
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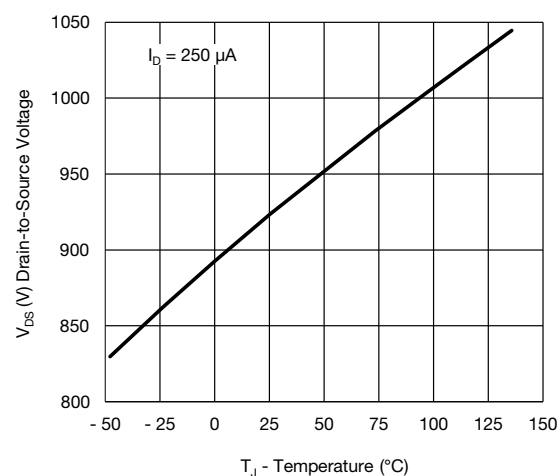
Source-Drain Diode Forward Voltage



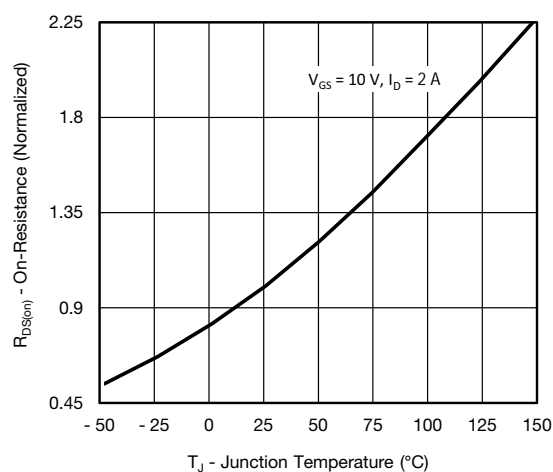
Threshold Voltage



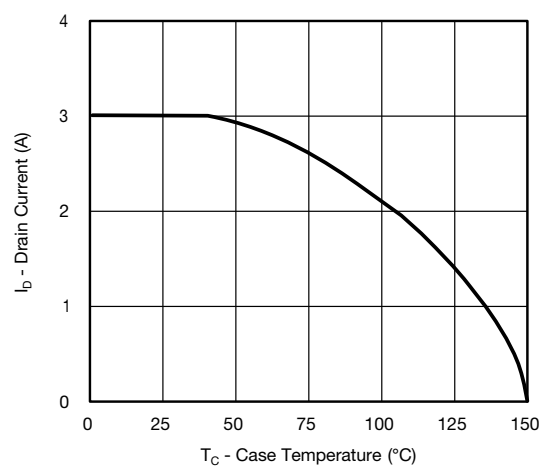
Capacitance



Drain Source Breakdown vs. Junction Temperature

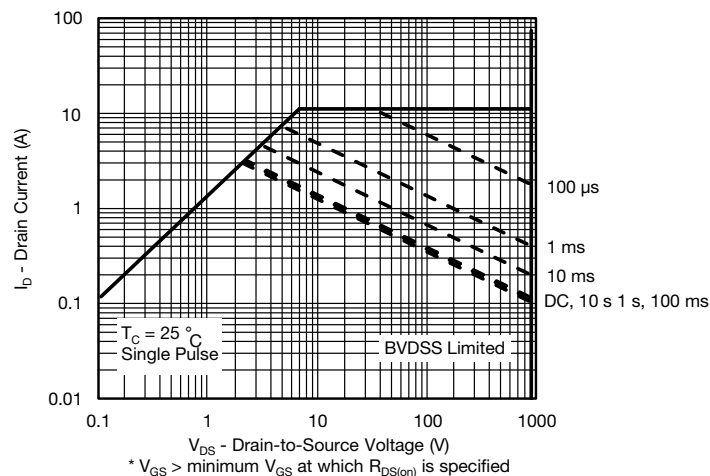
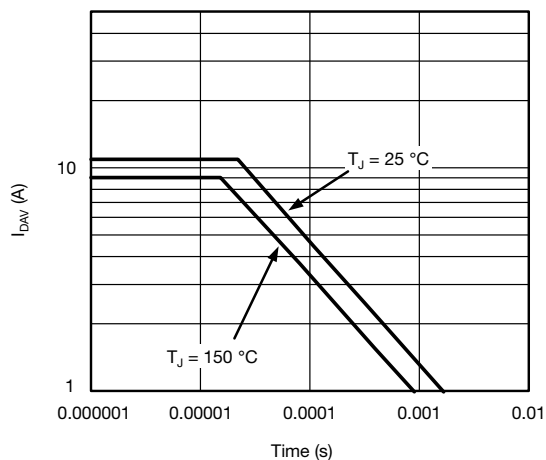


On-Resistance vs. Junction Temperature



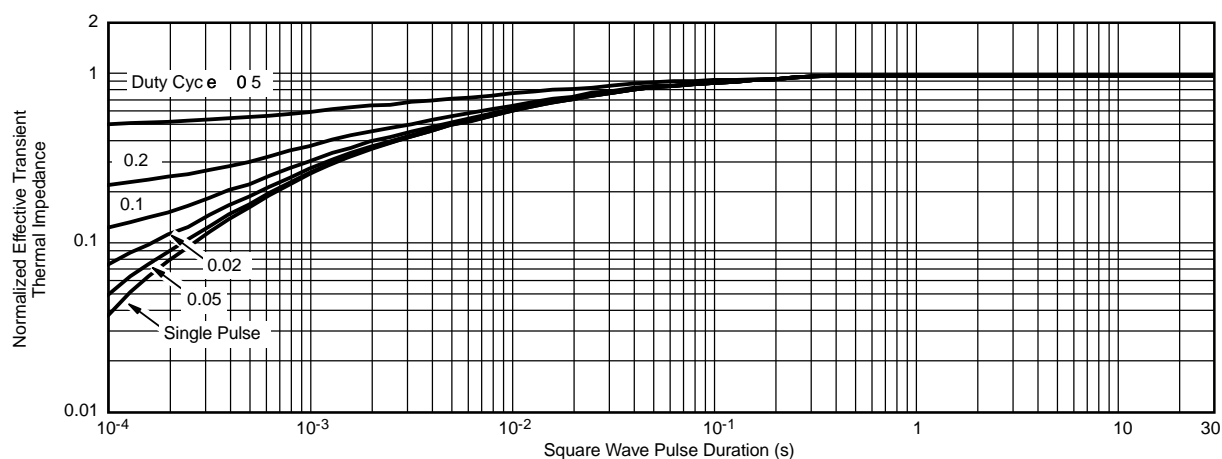
Current Derating

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Single Pulse Avalanche Current Capability vs. Time

Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

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