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# N-Channel 60 V (D-S) Super Junction Power MOSFET

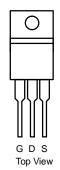
PRODUCT SUMMARY			
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A) <sup>a</sup>	
60	0.0015 at V <sub>GS</sub> = 10 V	240	

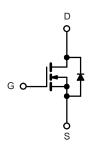
#### **FEATURES**

- 175 °C Junction Temperature
- DT-Trench Power MOSFET
- Material categorization:









N-Channel MOSFET

Parameter		Symbol	Limit	Unit
Gate-Source Voltage		V <sub>GS</sub>	± 20	V
Continuous Drain Current (T <sub>J</sub> = 175 °C) <sup>b</sup>	T <sub>C</sub> = 25 °C	1_	240	
	T <sub>C</sub> = 100 °C	l l <sub>D</sub>	220 <sup>a</sup>	
Pulsed Drain Current		I <sub>DM</sub>	960	А
Continuous Source Current (Diode Conduction)		I <sub>S</sub>	240 <sup>a</sup>	
Avalanche Current		I <sub>AS</sub>	220	1
Single Avalanche Energy (Duty Cycle ≤ 1 %)	L = 0.1 mH	E <sub>AS</sub>	614	mJ
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	P <sub>D</sub>	285	W
	T <sub>A</sub> = 25 °C	'D	2.5 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range	•	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maniana landina ta Ambianta	t ≤ 10 sec	R <sub>thJA</sub>	13	20	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	'`thJA	40	50	°C/W
Maximum Junction-to-Case		R <sub>thJC</sub>	0.55	1.0	

#### Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- c.  $t \le 10 \text{ s}$ .





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Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60			v	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		3		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nΑ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V			1		
		V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	50 μA	
		V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250	1	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	240			Α	
Drain-Source On-State Resistance <sup>b</sup>		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		0.0015	0.0020		
	D	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C		0.0022	0.0032	Ω	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A, T <sub>J</sub> = 175 °C		0.0029	0.0043		
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 48 V, I <sub>D</sub> = 20 A		68		S	
Dynamic							
Input Capacitance	C <sub>iss</sub>			11580		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 30 \text{ V}, f = 1 \text{ MHz}$		1346			
Reverse Transfer Capacitance	C <sub>rss</sub>			43			
Total Gate Charge <sup>c</sup>	$Q_g$			135	163	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$		51			
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			20			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			31			
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 30 \text{ V}, R_L = 0.6 \Omega$ $I_D \cong 20 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		116		ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			81			
Fall Time <sup>c</sup>	t <sub>f</sub>			53		1	
Source-Drain Diode Ratings and Cha	racteristics (	T <sub>C</sub> = 25 °C)					
Pulsed Current	I <sub>SM</sub>				960	Α	
Diode Forward Voltage	$V_{SD}$	$I_F = 20 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V	
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs		75	150	ns	

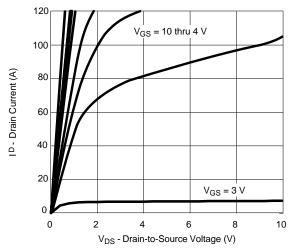
#### Notes:

- a. For design aid only; not subject to production testing.
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- c. Independent of operating temperature.

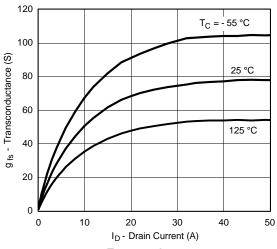
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



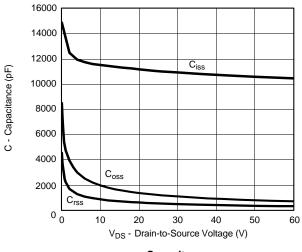
## TYPICAL CHARACTERISTICS (25 °C unless noted)



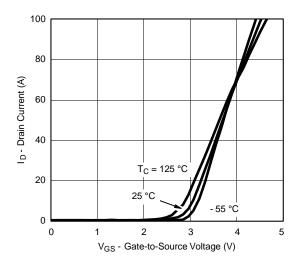
## **Output Characteristics**



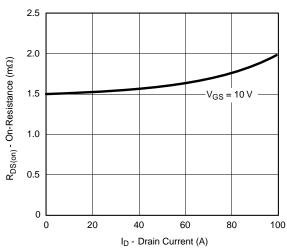
Transconductance



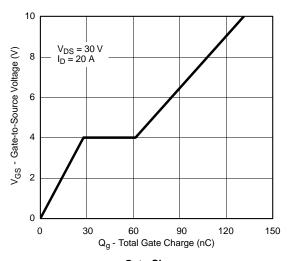
Capacitance



**Transfer Characteristics** 



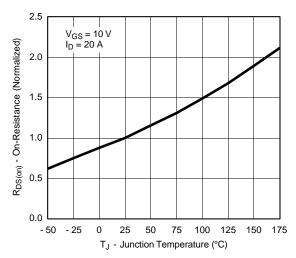
On-Resistance vs. Drain Current



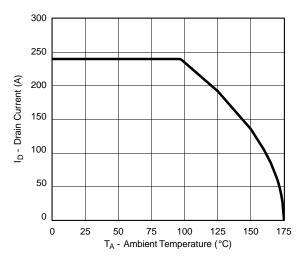
**Gate Charge** 



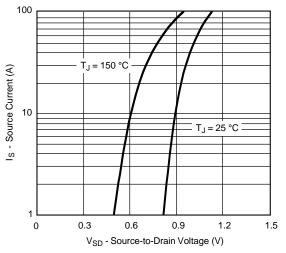
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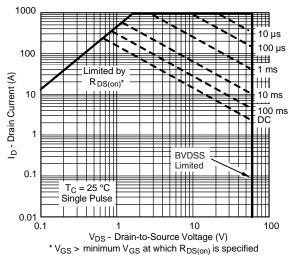
On-Resistance vs. Junction Temperature



**Maximum Drain Current vs. Ambient Temperature** 



Source-Drain Diode Forward Voltage



Safe Operating Area





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