

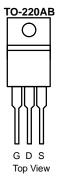
# N-Channel 60 V (D-S) Super Junction Power MOSFET

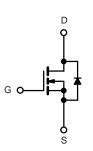
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>a</sup>		
60	0.0035 at V <sub>GS</sub> = 10 V	130		
	0.0042 at V <sub>GS</sub> = 4.5 V	110		

#### **FEATURES**

- 175 °C Junction Temperature
- DT-Trench Power MOSFET
- Material categorization:







N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> $(T_C = 25)$	5 °C, unless othe	rwise noted)			
Parameter		Symbol	Limit	Unit	
Gate-Source Voltage		$V_{GS}$	± 20	V	
Continuous Drain Current (T <sub>.1</sub> = 175 °C) <sup>b</sup>	T <sub>C</sub> = 25 °C	I-	130		
Continuous Drain Current (1 <sub>J</sub> = 175 C) <sup>2</sup>	T <sub>C</sub> = 100 °C	I <sub>D</sub>	110 <sup>a</sup>		
Pulsed Drain Current		I <sub>DM</sub>	480	A	
Continuous Source Current (Diode Conduction)		I <sub>S</sub>	110 <sup>a</sup>		
Avalanche Current		I <sub>AS</sub>	110		
Single Avalanche Energy (Duty Cycle ≤ 1 %)	L = 0.1 mH	E <sub>AS</sub>	500	mJ	
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	P <sub>D</sub>	180	W	
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C	' D	3 <sup>b</sup> , 8.5 <sup>b, c</sup>	]	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Mariana la Ambianta	t ≤ 10 sec	D	15	18	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	$R_{thJA}$	40	50	°C/W
Maximum Junction-to-Case		R <sub>thJC</sub>	0.85	1.1	

#### Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- c.  $t \le 10 \text{ s}$ .



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Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit
Static					<u> </u>	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		2.5	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current		V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V			1	μΑ
	I <sub>DSS</sub>	V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	
		V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V	130			Α
Drain-Source On-State Resistance <sup>b</sup>		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		0.0035	0.0040	
	D	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C		0.0040	0.0050	Ω
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A, T <sub>J</sub> = 175 °C		0.0045	0.0055	
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 15 A		0.0042	0.0050	
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	$V_{DS} = 48 \text{ V}, I_D = 20 \text{ A}$		50		S
Dynamic						
Input Capacitance	C <sub>iss</sub>			3950		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 48 \text{ V}, f = 1 \text{ MHz}$		670		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			23		
Total Gate Charge <sup>c</sup>	$Q_g$			67	78	
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 48 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$		12		nC
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			8.5		
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			10	20	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 48 \text{ V}, R_{L} = 0.6 \Omega$ $I_{D} \cong 20 \text{ A}, V_{GEN} = 10 \text{ V}, R_{g} = 2.5 \Omega$		5	15	ns
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			55	70	
Fall Time <sup>c</sup>	t <sub>f</sub>			12	20	
Source-Drain Diode Ratings and Cha	aracteristics (	T <sub>C</sub> = 25 °C)				
Pulsed Current	I <sub>SM</sub>				130	Α
Diode Forward Voltage	V <sub>SD</sub>	$I_F = 20 \text{ A}, V_{GS} = 0 \text{ V}$		1	1.2	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs	]	45	100	ns

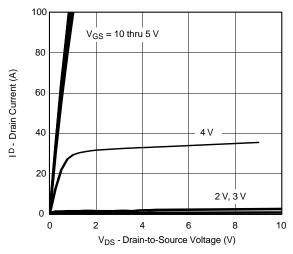
#### Notes:

- a. For design aid only; not subject to production testing.
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- c. Independent of operating temperature.

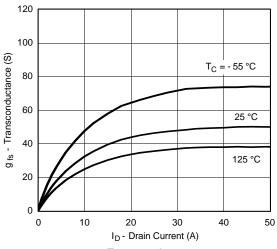
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



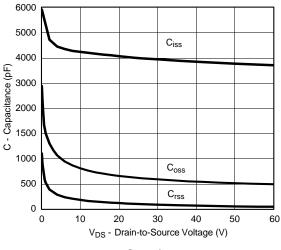
### TYPICAL CHARACTERISTICS (25 °C unless noted)



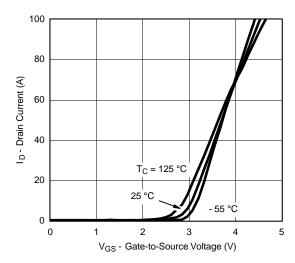
### **Output Characteristics**



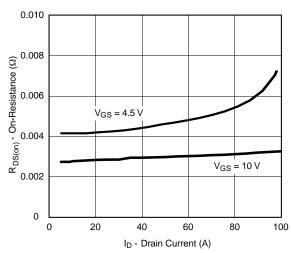
Transconductance



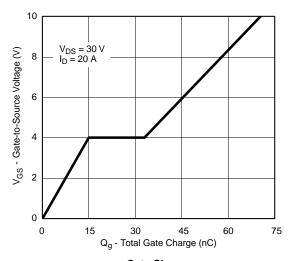
Capacitance



**Transfer Characteristics** 



On-Resistance vs. Drain Current

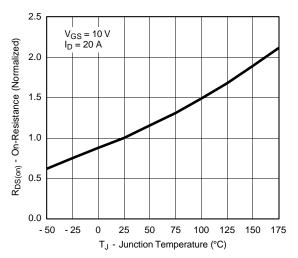


**Gate Charge** 

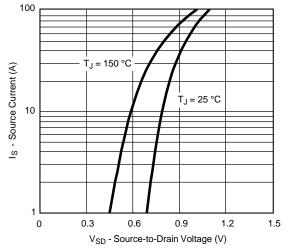




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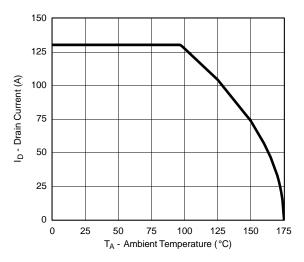
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

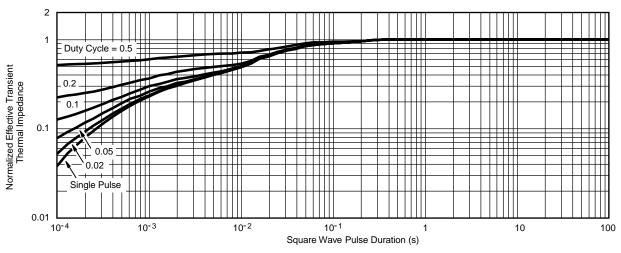


#### **THERMAL RATINGS**



R<sub>DS(on)\*</sub> 10 µs 100 µs 100 Ш 1 ms I<sub>D</sub> - Drain Current (A) 10 10 ms 100 ms DC T<sub>C</sub> = 25 °C Single Pulse 0.1 0.01 <del>-</del> 0.1 100  $V_{DS} - Drain-to-Source \ Voltage \ (V) \\ ^*V_{GS} > minimum \ V_{GS} \ at \ which \ R_{DS(on)} \ is \ specified$ Safe Operating Area

Maximum Drain Current vs. Ambient Temperature



1000

Limited by

Normalized Thermal Transient Impedance, Junction-to-Case





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