

Dual N-Channel 40 V (D-S) Super Junction MOSFET

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (mΩ) (Typ.)	I _D (A) ^a	Q _g (Typ.)
40	6.6 at V _{GS} = 10 V	48	13 nC
	9.7 at V _{GS} = 4.5 V		

FEATURES

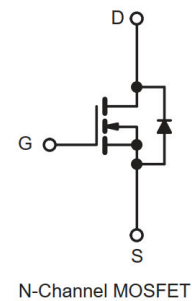
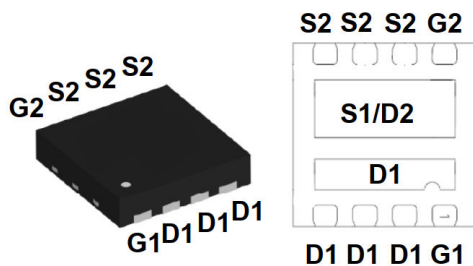
- DT-SJ Power MOSFET
- 100 % R_g and UIS tested
- Integrated MOSFET half-bridge power stage



APPLICATIONS

- Synchronous Rectification
- Motor Drives and Uninterruptible Power Supplies

DFN3x3 Asymmetric Dual Pin Configuration



ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V _{DS}	40	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current (T _J = 175 °C)	I _D	48	A
		32	
Pulsed Drain Current	I _{DM}	160	
Single Avalanche Energy ^a	E _{AS}	50	mJ
Maximum Power Dissipation	P _D	30 ^{b,c}	W
		21.2 ^{b,c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C
Soldering Recommendations (Peak Temperature)		260	

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) ^{b,d}	R _{thJA}	30	°C/W
Junction-to-Case (Drain)	R _{thJC}	4.2	

Notes:

- T_C = 25 °C.
- Surface mounted on 1" x 1" FR4 board.
- t = 10 s.
- Maximum under steady state conditions is 40 °C/W.

SPECIFICATIONS ($T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

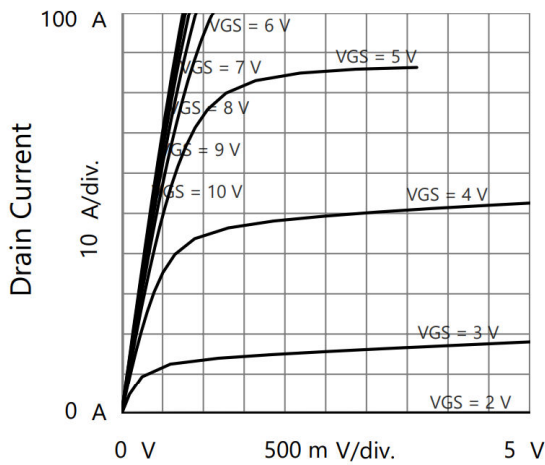
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	40	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1	-	2.5	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 40 V, V _{GS} = 0 V	-	-	1	μA
		V _{DS} = 32 V, V _{GS} = 0 V, T _J = 55 °C	-	-	10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 10 V, V _{GS} = 10 V	48	-	-	A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 10 A	-	6.6	8	mΩ
		V _{GS} = 4.5 V, I _D = 7 A	-	9.7	13	
Forward Transconductance ^a	g _{fs}	V _{DS} = 5 V, I _D = 10 A	-	40	-	S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 20 V, f = 1 MHz	-	712	-	pF
Output Capacitance	C _{oss}		-	423	-	
Reverse Transfer Capacitance	C _{rss}		-	26	-	
Total Gate Charge ^c	Q _g	V _{DS} = 20 V, V _{GS} = 10 V, I _D = 10 A	-	13	-	nC
Gate-Source Charge ^c	Q _{gs}		-	1.3	-	
Gate-Drain Charge ^c	Q _{gd}		-	2.4	-	
Gate Resistance	R _g	f = 1 MHz	-	6	-	Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DS} = 20 V, I _D = 10 A, R _g = 2.5 Ω V _{GS} = 10 V	-	10	-	ns
Rise Time ^c	t _r		-	9	-	
Turn-Off Delay Time ^c	t _{d(off)}		-	52	-	
Fall Time ^c	t _f		-	27	-	
Drain-Source Body Diode Ratings and Characteristics ^b (T _C = 25 °C)						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	-	-	48	A
Pulsed Current (t = 100 μs)	I _{SM}		-	-	160	A
Forward Voltage ^a	V _{SD}	I _F = 100 A, V _{GS} = 0 V	-	-	1.2	V
Reverse Recovery Time	t _{rr}	I _F = 10 A, di/dt = 300 A/μs	-	19	-	ns
Reverse Recovery Charge	Q _{rr}		-	10	-	nC

Notes

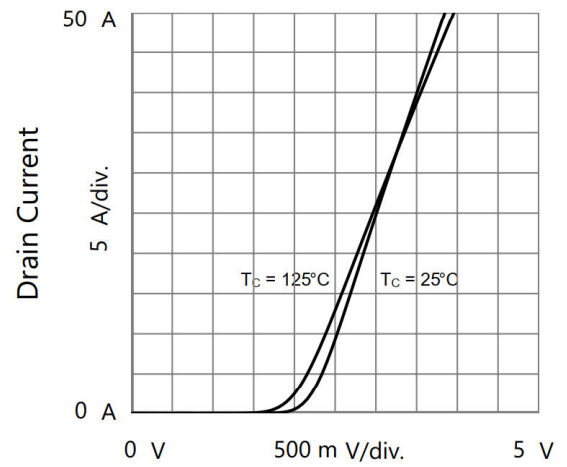
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.
 c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

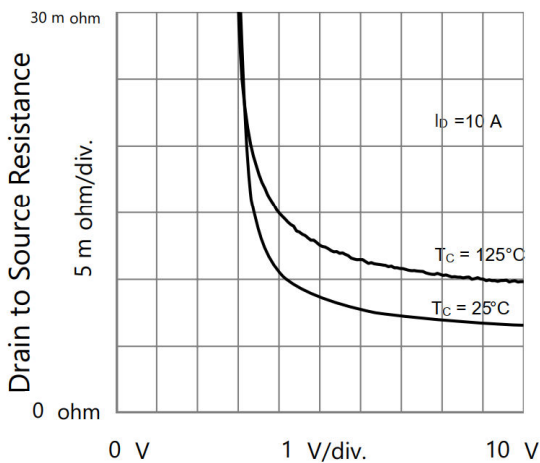
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



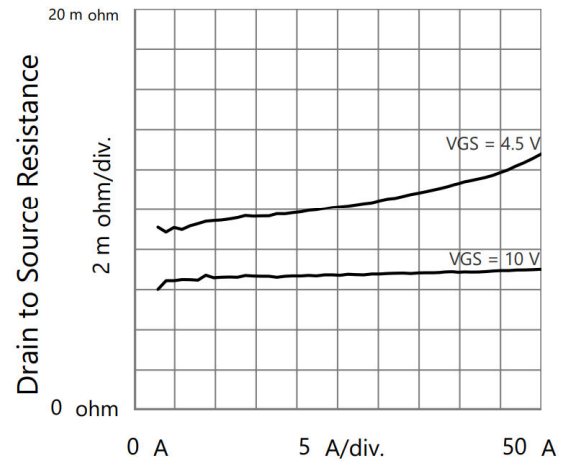
Drain to Source Voltage
Output Characteristics



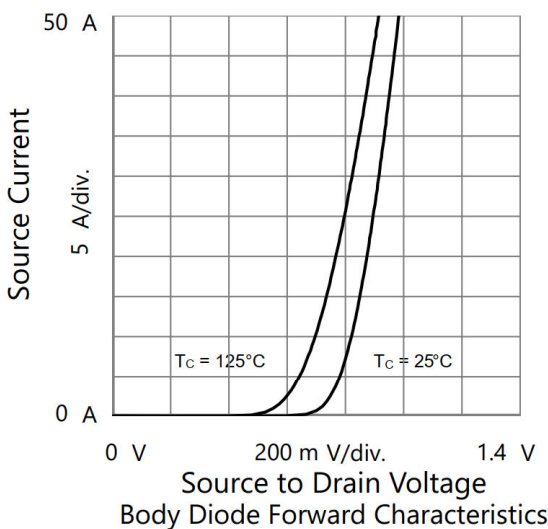
Gate to Source Voltage
Transfer Characteristics



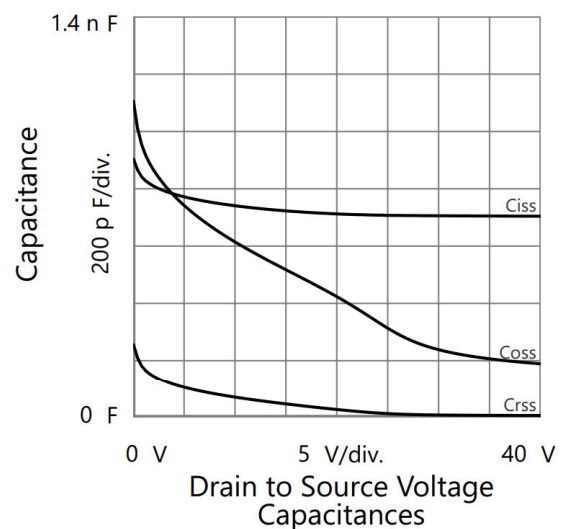
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



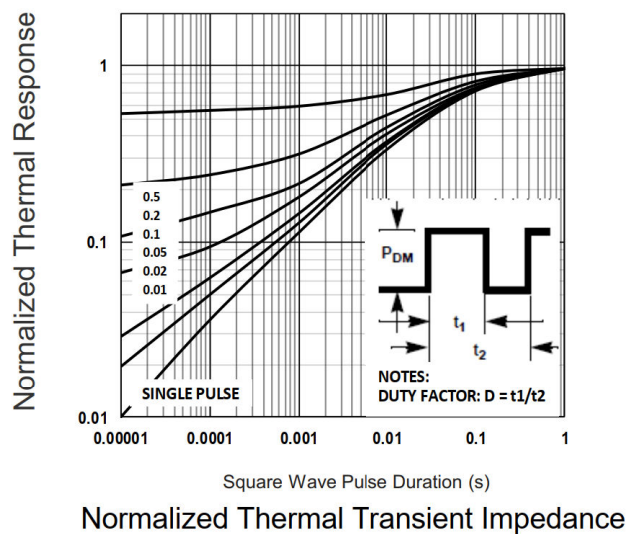
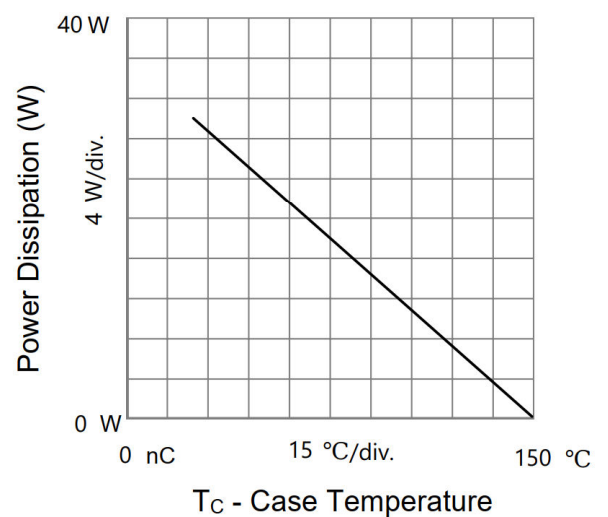
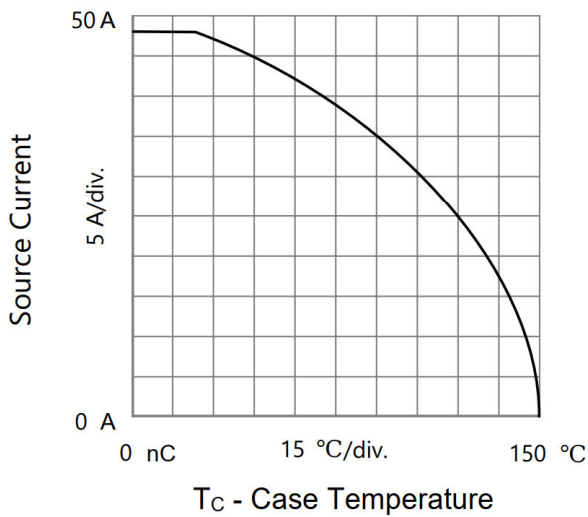
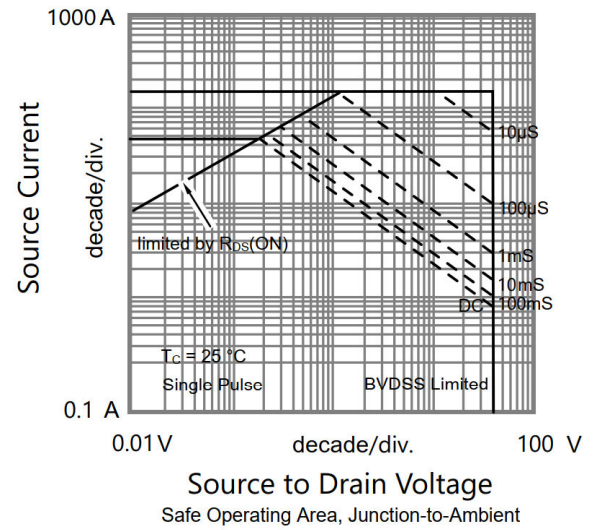
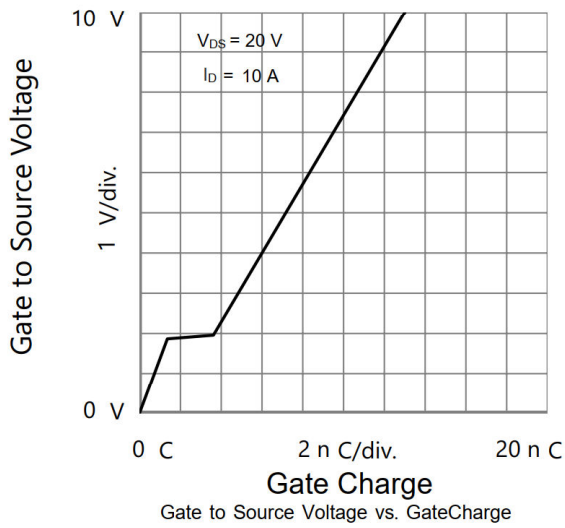
Drain Current
Drain to Source Resistance vs. Drain Current



Source to Drain Voltage
Body Diode Forward Characteristics



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance

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