Din-Tek SEMICONDUCTOR

DTQ3D308

www.din-tek.jp

N- and P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY				
	V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)[a]	Q_g (Typ.)
N-Channel	30	0.015 at V_{GS} = 10 V	18	12.5
		0.020 at V_{GS} = 4.5 V	14	
P-Channel	- 30	0.026 at V_{GS} = - 10 V	- 16	8
		0.039 at V_{GS} = - 4.5 V	- 12	

FEATURES

- DT-Trench Power MOSFET
- 100% R_g and UIS Tested

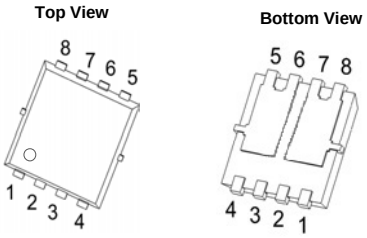
Pb-free

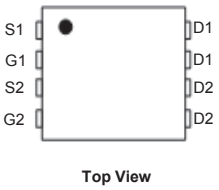
RoHS COMPLIANT

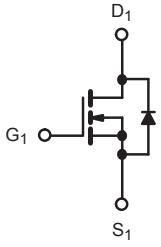
APPLICATIONS

- Networking DC-DC Power System
- Load Switch

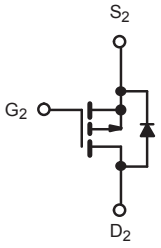
PDFN 3.3x3.3







N-Channel MOSFET



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted					
Parameter		Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage		V_{DS}	30	- 30	V
Gate-Source Voltage		V_{GS}	± 20		
Continuous Drain Current (T_J = 150 °C)	T_C = 25 °C	I_D	18	-16	A
	T_C = 70 °C		15	-12	
	T_A = 25 °C		12 [b, c]	- 9.6[b, c]	
	T_A = 70 °C		9.4[b, c]	- 7.3[b, c]	
Pulsed Drain Current		I_{DM}	72	-64	
Source-Drain Current Diode Current	T_C = 25 °C	I_S	15	- 10	
	T_A = 25 °C		8[b, c]	- 5[b, c]	
Pulsed Source-Drain Current		I_{SM}	72	-48	
Single Pulse Avalanche Current	L = 0 1 mH	I_{AS}	40	-32	
Single Pulse Avalanche Energy		E_{AS}	20	-17	mJ
Maximum Power Dissipation	T_C = 25 °C	P_D	12.5	8	W
	T_C = 70 °C		8	5.12	
	T_A = 25 °C		4.3[b, c]	2.0[b, c]	
	T_A = 70 °C		2.75[b, c]	1.28[b, c]	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS							
			N-Channel		P-Channel		
Parameter		Symbol	Typ.	Max.	Typ.	Max.	Unit
Maximum Junction-to-Ambient[b, d]	t ≤ 10 s	R_{thJA}	20	30	23	30	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	40	55	45	60	

Notes:
a. Based on T_C = 25 °C.
b. Surface Mounted on 1" x 1" FR4 board.
c. t = 10 s.
d. Maximum under Steady State conditions is 120 °C/W.

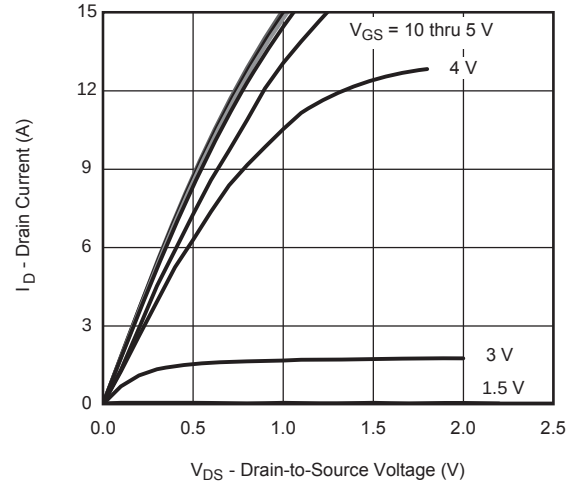
SPECIFICATIONS T_J = 25 °C, unless otherwise noted							
Parameter	**Symbol**	**Test Conditions**		**Min.**	**Typ.[a]**	**Max.**	**Unit**
Static							
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	N-Ch	30			V
		V_{GS} = 0 V, I_D = - 250 µA	P-Ch	- 30			
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	I_D = 250 µA	N-Ch		44		mV/°C
		I_D = - 250 µA	P-Ch		- 42		
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I_D = 250 µA	N-Ch		- 5.5		
		I_D = - 250 µA	P-Ch		4.6		
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	N-Ch	1		3	V
		V_{DS} = V_{GS}, I_D = - 250 µA	P-Ch	- 1		- 3	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V	N-Ch			100	nA
			P-Ch			- 100	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 24 V, V_{GS} = 0 V	N-Ch			1	µA
		V_{DS} = - 24 V, V_{GS} = 0 V	P-Ch			- 1	
		V_{DS} = 24 V, V_{GS} = 0 V, T_J = 55 °C	N-Ch			10	
		V_{DS} = - 24 V, V_{GS} = 0 V, T_J = 55 °C	P-Ch			- 10	
On-State Drain Current[b]	$I_{D(on)}$	V_{DS} = 5 V, V_{GS} = 10 V	N-Ch	22			A
		V_{DS} = - 5 V, V_{GS} = - 10 V	P-Ch	- 20			
Drain-Source On-State Resistance[b]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 10 A	N-Ch		0.015	0.018	Ω
		V_{GS} = - 10 V, I_D = - 8 A	P-Ch		0.026	0.035	
		V_{GS} = 4.5 V, I_D = 8 A	N-Ch		0.020	0.025	
		V_{GS} = - 4.5 V, I_D = - 5 A	P-Ch		0.039	0.050	
Forward Transconductance[b]	g_{fs}	V_{DS} = 15 V, I_D = 10 A	N-Ch		25		S
		V_{DS} = - 15 V, I_D = - 8 A	P-Ch		13		
Dynamic[a]							
Input Capacitance	C_{iss}	N-Channel V_{DS} = 24 V, V_{GS} = 0 V, f = 1 MHz P-Channel V_{DS} = - 24 V, V_{GS} = 0 V, f = 1 MHz	N-Ch		1150		pF
			P-Ch		1823		
Output Capacitance	C_{oss}		N-Ch		260		
			P-Ch		375		
Reverse Transfer Capacitance	C_{rss}		N-Ch		110		
			P-Ch		135		
Total Gate Charge	Q_g	V_{DS} = 24 V, V_{GS} = 10 V, I_D = 10 A	N-Ch		12.5	22	nC
		V_{DS} = - 24 V, V_{GS} = - 10 V, I_D = - 8 A	P-Ch		8	20	
		N-Channel V_{DS} = 24 V, V_{GS} = 4.5 V I_D = 8 A	N-Ch		5.3	9	
			P-Ch		2.1	3.5	
Gate-Source Charge	Q_{gs}	P-Channel V_{DS} = - 24 V, V_{GS} = - 4.5 V, I_D = - 5 A	N-Ch		1.4		
			P-Ch		0.58		
Gate-Drain Charge	Q_{gd}		N-Ch		1.9		
			P-Ch		1.3		
Gate Resistance	R_g	f = 1 MHz	N-Ch	0.5	2.0	4.5	Ω
			P-Ch	1.0	3.5	11	

SPECIFICATIONS T_J = 25 °C, unless otherwise noted							
Parameter	**Symbol**	**Test Conditions**		**Min.**	**Typ.[a]**	**Max.**	**Unit**
Dynamic[a]							
Turn-On Delay Time	$t_{d(on)}$	N-Channel V_{DD} = 24 V, R_L = 4 Ω $I_D \cong$ 10 A, V_{GEN} = 10 V, R_g = 1 Ω	N-Ch		7	14	ns
			P-Ch		9	16	
Rise Time	t_r		N-Ch		10	25	
			P-Ch		12	27	
Turn-Off Delay Time	$t_{d(off)}$	P-Channel V_{DD} = - 24 V, R_L = 4 Ω $I_D \cong$ - 8 A, V_{GEN} = - 10 V, R_g = 1 Ω	N-Ch		15	32	
			P-Ch		31	65	
Fall Time	t_f		N-Ch		9	22	
			P-Ch		10	23	
Turn-On Delay Time	$t_{d(on)}$	N-Channel V_{DD} = 24 V, R_L = 4 Ω $I_D \cong$ 8 A, V_{GEN} = 4.5 V, R_g = 1 Ω	N-Ch		16	30	
			P-Ch		45	80	
Rise Time	t_r		N-Ch		19	30	
			P-Ch		35	50	
Turn-Off Delay Time	$t_{d(off)}$	P-Channel V_{DD} = - 24 V, R_L = 4 Ω $I_D \cong$ - 5 A, V_{GEN} = - 4.5 V, R_g = 1 Ω	N-Ch		16	30	
			P-Ch		28	60	
Fall Time	t_f		N-Ch		10	20	
			P-Ch		11	25	
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I_S	T_C = 25 °C	N-Ch			18	A
			P-Ch			-16	
Pulse Diode Forward Current[a]	I_{SM}		N-Ch			72	
			P-Ch			-64	
Body Diode Voltage	V_{SD}	I_S = 1.6 A	N-Ch		0.78	1.2	V
		I_S = - 1.6 A	P-Ch		- 0.75	- 1.2	
Body Diode Reverse Recovery Time	t_{rr}	N-Channel I_F = 2 A, dI/dt = 100 A/µs, T_J = 25 °C P-Channel I_F = - 2 A, dI/dt = - 100 A/µs, T_J = 25 °C	N-Ch		7	34	ns
			P-Ch		5	50	
Body Diode Reverse Recovery Charge	Q_{rr}		N-Ch		4	25	nC
			P-Ch		1.3	35	
Reverse Recovery Fall Time	t_a		N-Ch		17		ns
			P-Ch		18		
Reverse Recovery Rise Time	t_b		N-Ch		6		
			P-Ch		14		

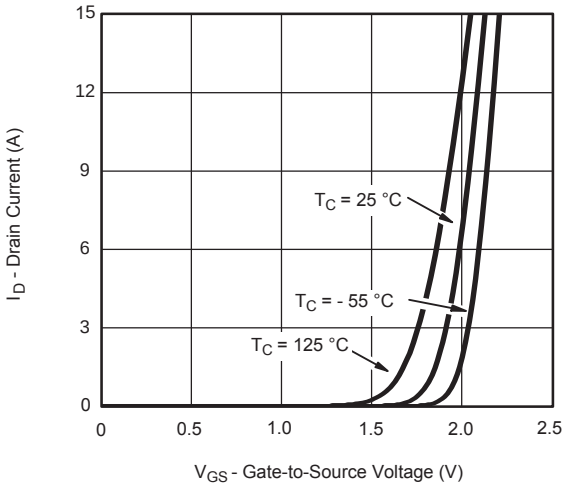
Notes:
a. Guaranteed by design, not subject to production testing.
b. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

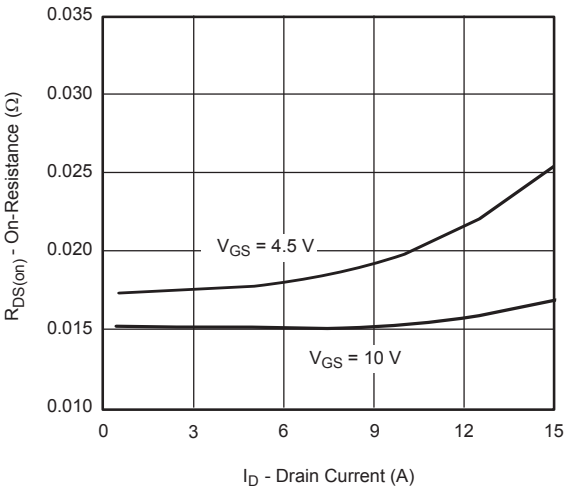
N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



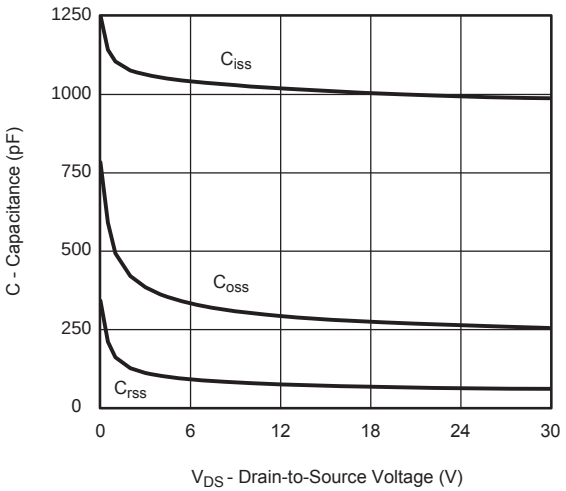
Output Characteristics



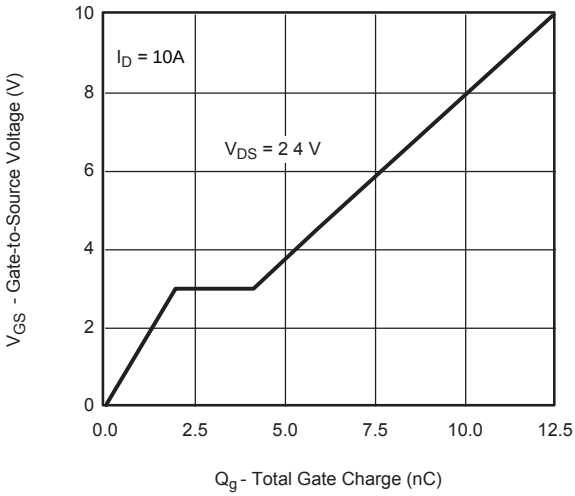
Transfer Characteristics



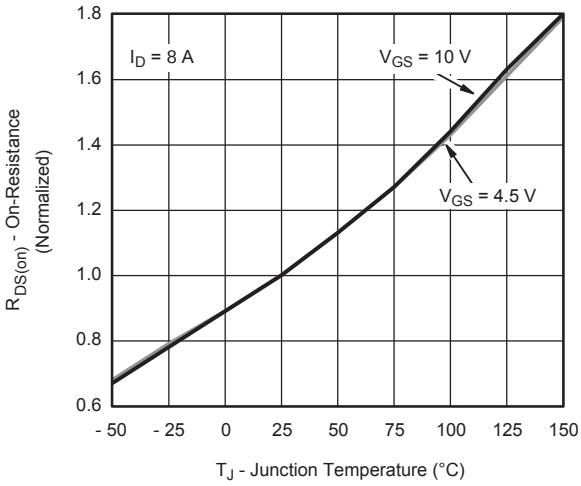
On-Resistance vs. Drain Current



Capacitance

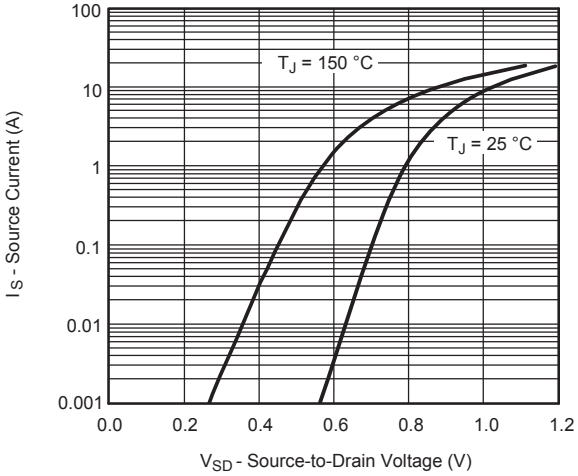


Gate Charge

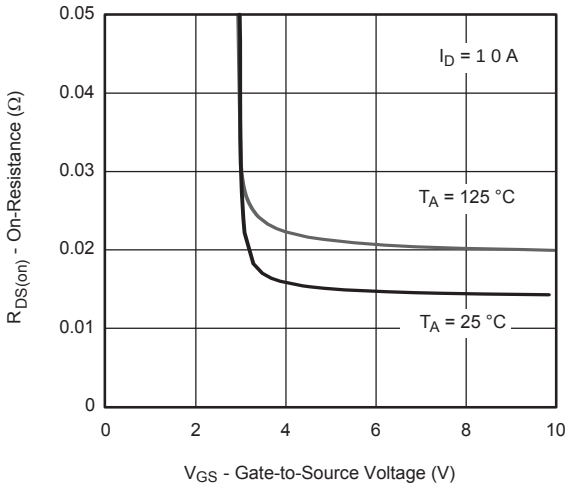


On-Resistance vs. Junction Temperature

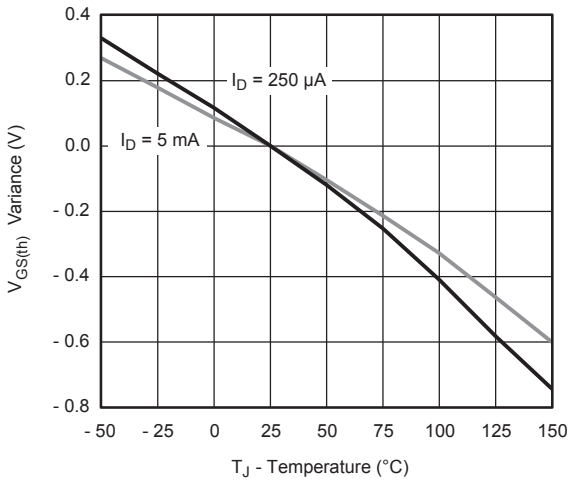
N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



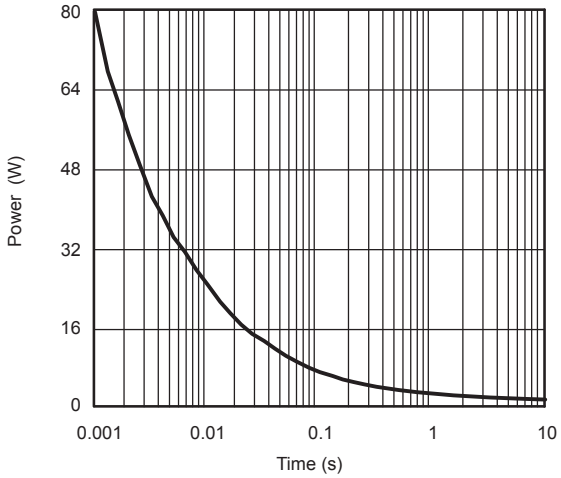
Source-Drain Diode Forward Voltage



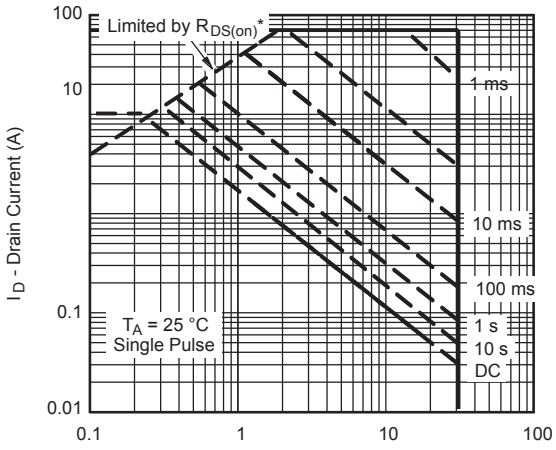
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



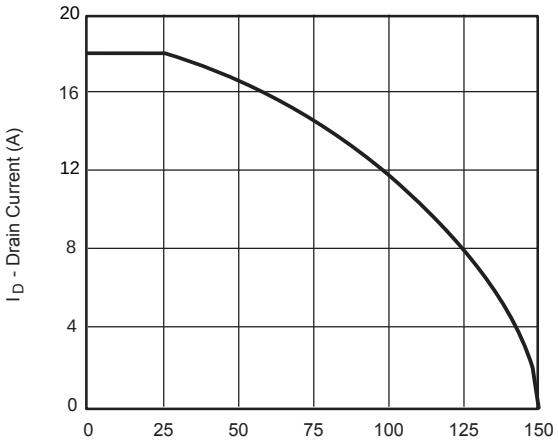
Single Pulse Power, Junction-to-Ambient



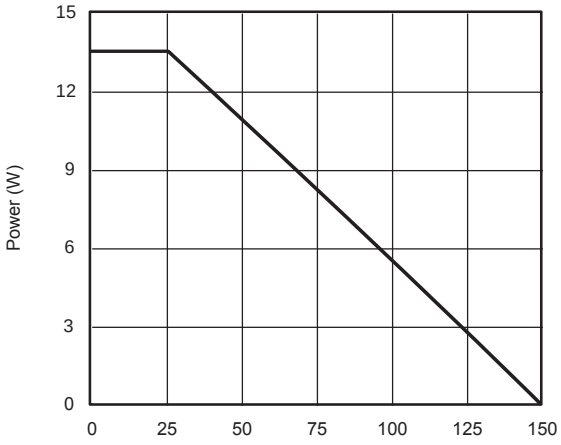
* V_{GS} > minimum V_{GS} at which $r_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

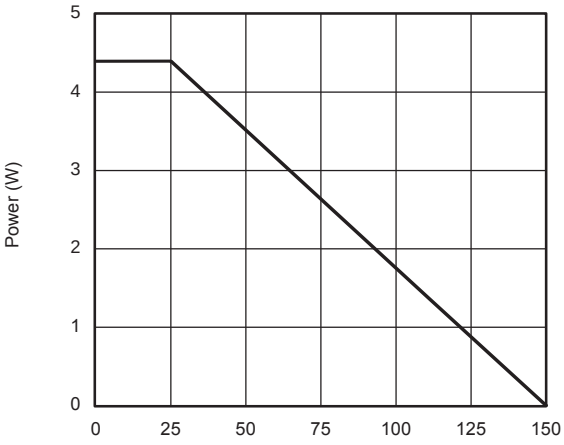
N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Current Derating*



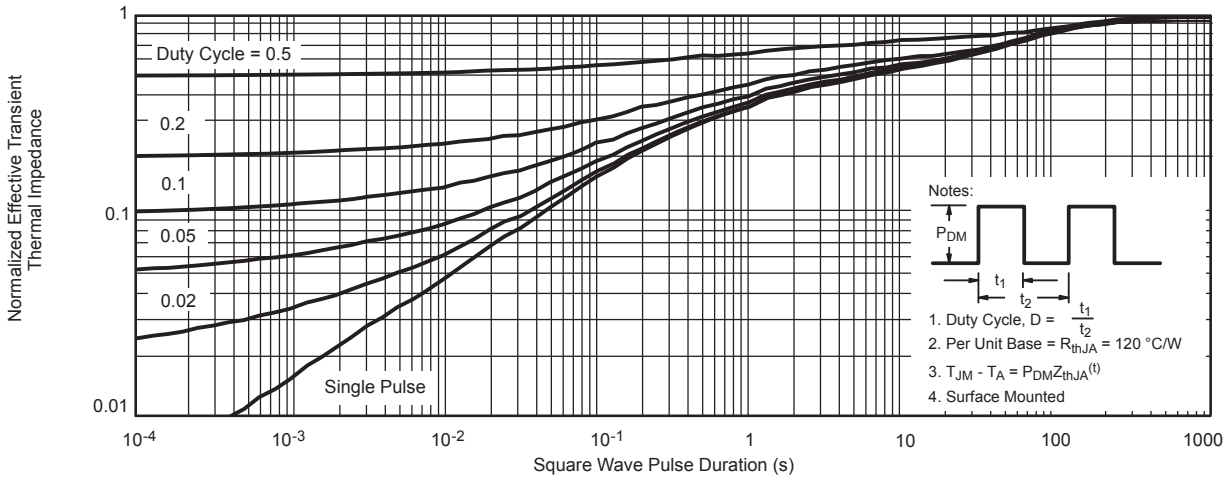
Power Derating, Junction-to-Foot



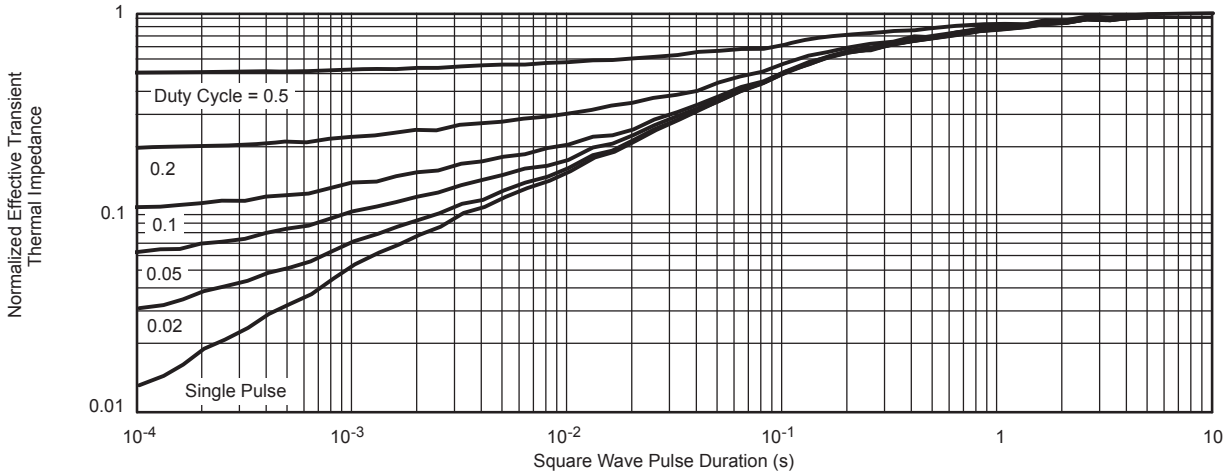
Power Derating, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

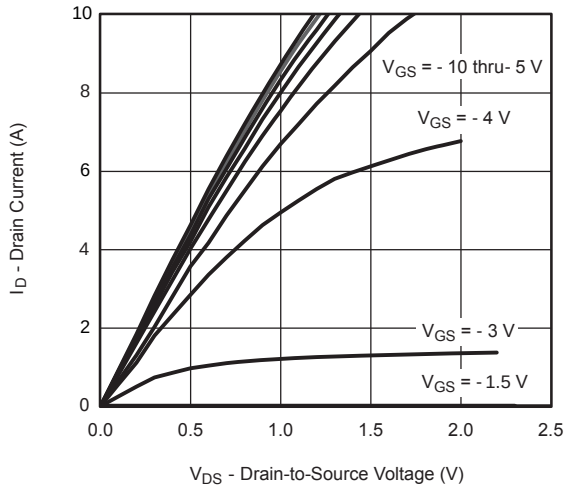


Normalized Thermal Transient Impedance, Junction-to-Ambient

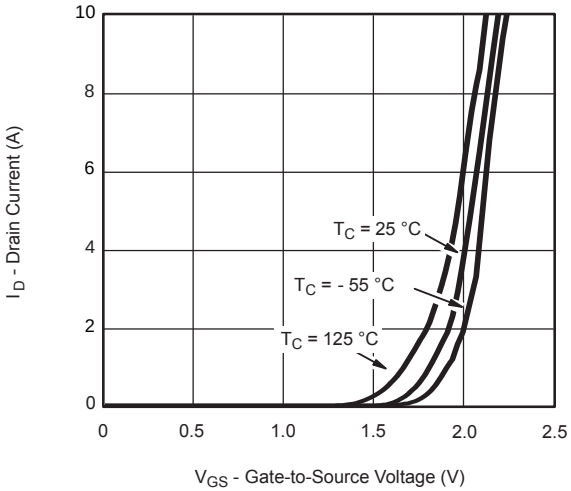


Normalized Thermal Transient Impedance, Junction-to-Foot

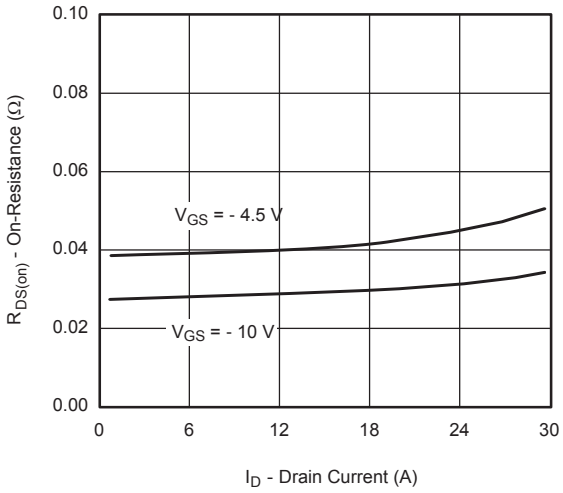
P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



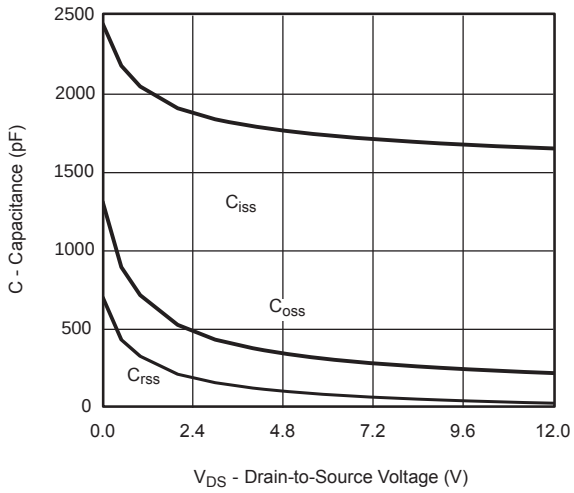
Output Characteristics



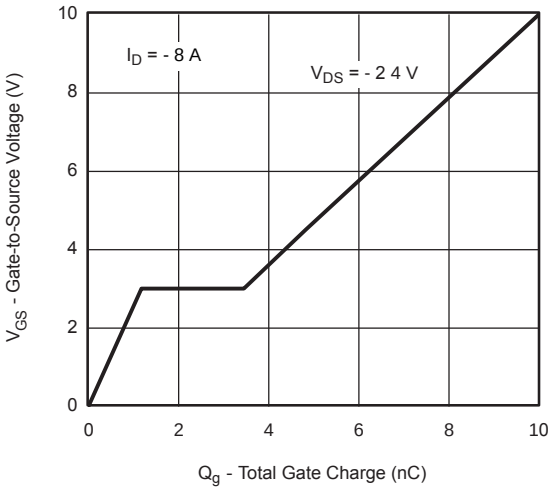
Transfer Characteristics



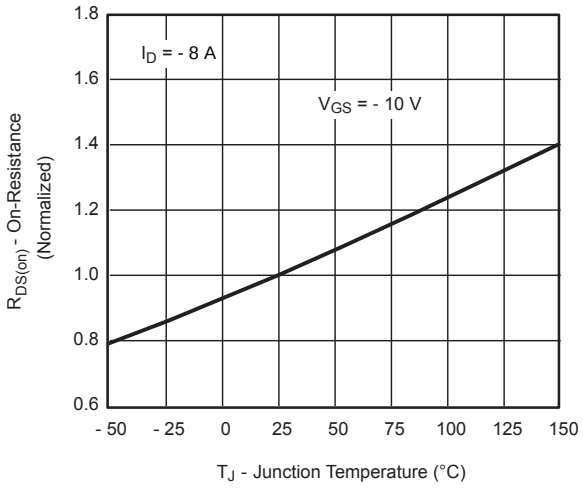
On-Resistance vs. Drain Current



Capacitance

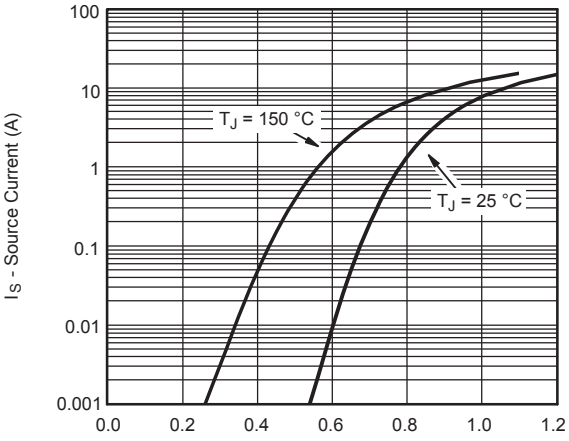


Gate Charge

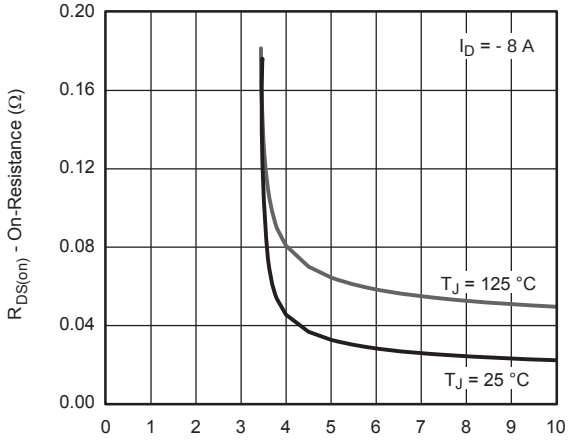


On-Resistance vs. Junction Temperature

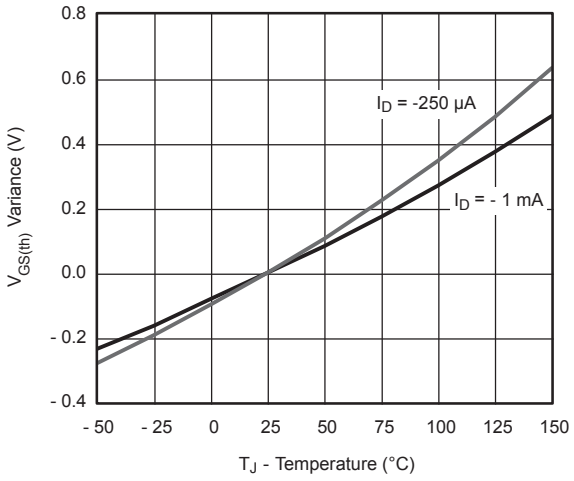
P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



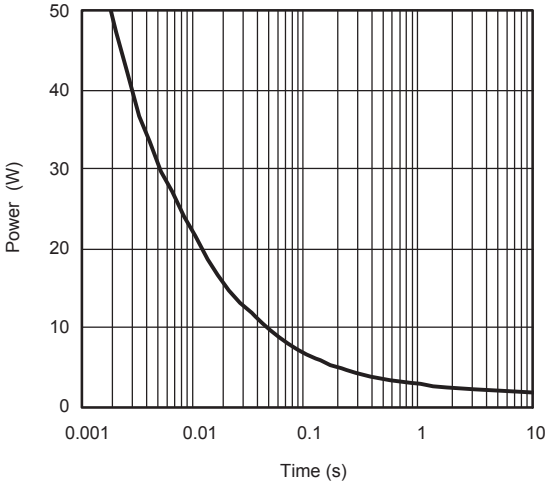
Source-Drain Diode Forward Voltage



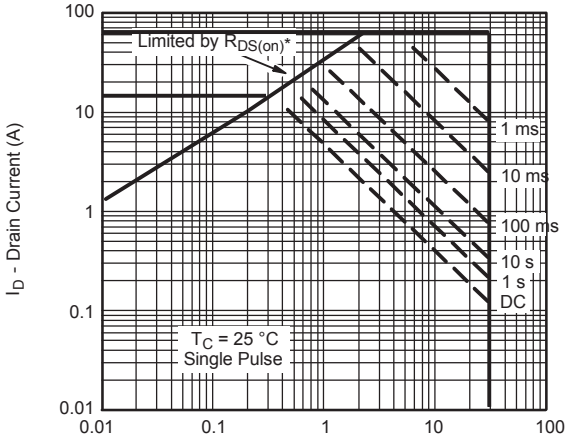
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



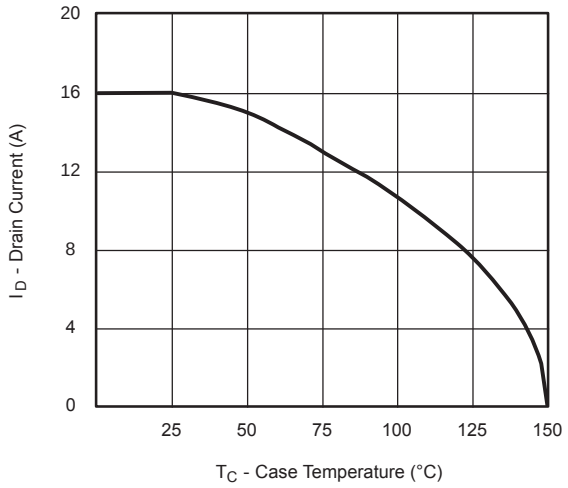
Single Pulse Power, Junction-to-Ambient



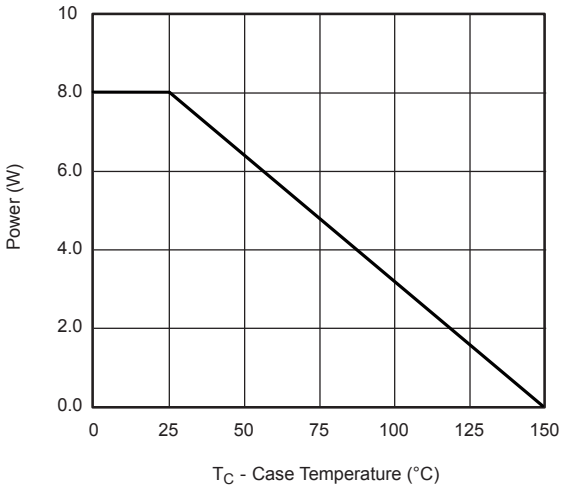
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

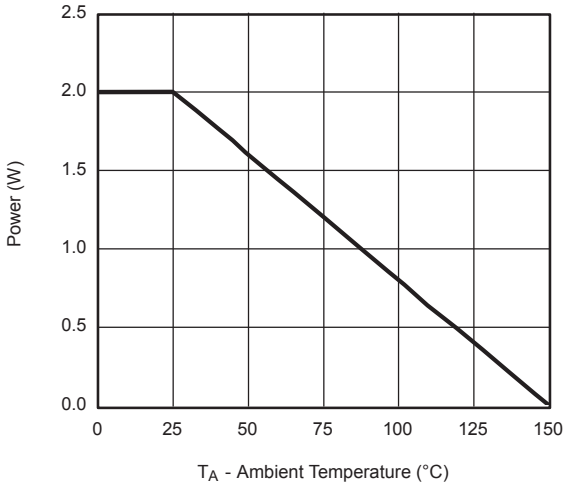
P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Current Derating*



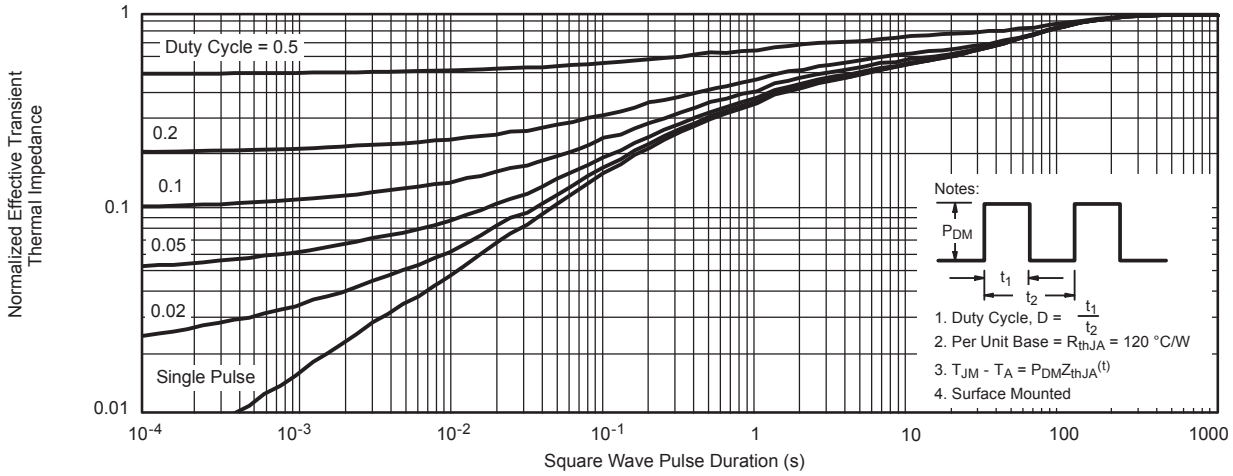
Power Derating, Junction-to-Foot



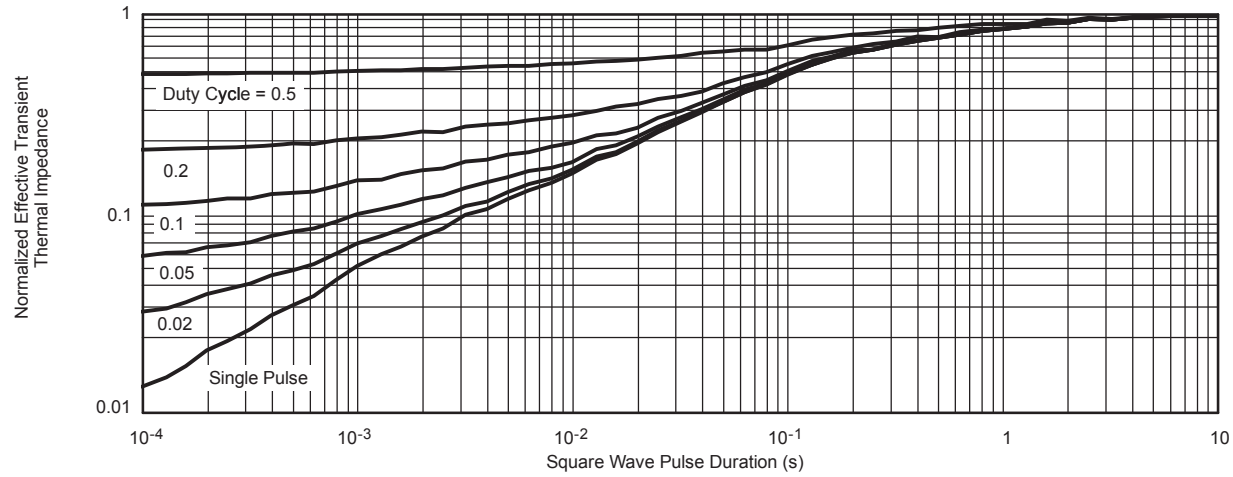
Power Derating, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Din-Tek Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Din-Tek"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Din-Tek makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Din-Tek disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Din-Tek's knowledge of typical requirements that are often placed on Din-Tek products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Din-Tek's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Din-Tek products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Din-Tek product could result in personal injury or death. Customers using or selling Din-Tek products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Din-Tek personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Din-Tek. Product names and markings noted herein may be trademarks of their respective owners.

Material Category Policy

Din-Tek Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Din-Tek documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Din-Tek Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Din-Tek documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.