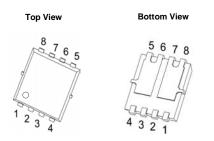
Dual N-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A)	Q _g (Typ.)		
60	11 at V _{GS} = 10 V	25	14 nC		
	15 at V _{GS} = 4.5 V	20	14110		

PDFN 3.3x3.3



FEATURES

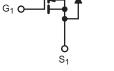
- **DT-Trench Power MOSFET** •
- 100 % Rg and UIS Tested

APPLICATIONS

- Power Management
- DC/DC Conversion
- Load Switching

D₁

• Motor Driving





G₂ (

 D_2

C

N-Channel MOSFET

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-source voltage	V _{DS}	60			
Gate-source voltage	V _{GS}	± 20	- V		
	T _C = 25 °C	00	25		
	T _C = 70 °C		17		
Continuous drain current ($T_J = 150 \ ^\circ C$)	T _A = 25 °C	I _D	5.3 ^{b, c}		
	T _A = 70 °C		1.9 ^{b, c}	_	
Pulsed drain current (t = 100 µs)		I _{DM}	100	— A	
	T _C = 25 °C		25		
Continuous source current (MOSFET diode conduction)	T _A = 25 °C	I _S	5.3 ^{b, c}		
Single pulse avalanche current L = 0.1 mH		I _{AS}	23		
L = C		E _{AS}	29	mJ	
	T _C = 25 °C		18		
Maximum neuror discinction	T _C = 70 °C		11.5	14/	
Maximum power dissipation	T _A = 25 °C	P _D	3.6 ^{b, c}	W	
	T _A = 70 °C		2.3 ^{b, c}		
Operating junction and storage temperature range	T _J , T _{stg}	-55 to +150			
Soldering recommendations (peak temperature)			260		

THERMAL RESISTANCE RATINGS		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient b, d	t ≤ 10 s	R _{thJA}	33	55	°C/W	
Maximum junction-to-case (drain)	Steady state	R _{thJC}	6	10	C/W	

Notes

a. $T_{C} = 25 \,^{\circ}C$ b. Surface mounted on 1" x 1" FR4 board

c. t = 10 s

d. Maximum under steady state conditions is 69 °C/W





SPECIFICATIONS ($T_J = 25 \ ^{\circ}C$,	unless othe	erwise noted)						
	CHANNEL-1 AND CHANNEL-2							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT		
Static	•	•	•					
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$	60	-	-	v		
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1	-	3	v		
Gate-source leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$		-	± 100	nA		
Zero gate voltage drain current		$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	1	μΑ		
	IDSS	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$	-	-	5			
On-state drain current ^a	I _{D(on)}	$V_{DS} \geq 5 \text{ V}, V_{GS} = 10 \text{ V}$	25	-	-	А		
Drain-source on-state resistance ^a		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}$	-	11	14			
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 15 \text{ A}$	-	15	20	mΩ		
Forward transconductance a	9 _{fs}	V _{DS} = 5 V, I _D = 20 A	-	86	-	S		
Dynamic ^b	•	•	•					
Input capacitance	C _{iss}		-	758	-	pF		
Output capacitance	C _{oss}		-	202	-			
Reverse transfer capacitance	C _{rss}	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	-	20	-			
Total gate charge	Qg		-	14	-	nC		
Gate-source charge	Q _{gs}	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$	-	2.4	-			
Gate-drain charge	Q _{gd}		-	0.67	-			
Gate resistance	Rg	f = 1 MHz	-	1.6	-	Ω		
Turn-on delay time	t _{d(on)}		-	5	-	ns		
Rise time	tr	V _{DD} = 15 V, R _L = 1.2 Ω, I _D ≅ 20 A,	-	6	-			
Turn-off delay time	t _{d(off)}	$V_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$	-	18	-			
Fall time	t _f		-	8	-			
Drain-source Body Diode Characterist	cs				•			
Continuous source-drain diode current	I _S	$T_{\rm C} = 25^{\circ}{\rm C}$	-	-	25	^		
Pulse diode forward current	I _{SM}		-	-	100	A		
Body diode voltage	V _{SD}	$I_{S} = 1 \text{ A}, V_{GS} = 0 \text{ V}$	-	0.7	1.0	V		
Body diode reverse recovery time	t _{rr}		-	20	-	ns		
Body diode reverse recovery charge	Q _{rr}	I _F = 20 A, di/dt = 100 A/μs,	-	10	-	nC		
Reverse recovery fall time	t _a	$T_{\rm J} = 25 \ ^{\circ}{\rm C}$	-	8	-	20		
Reverse recovery rise time	t _b			7	-	ns		

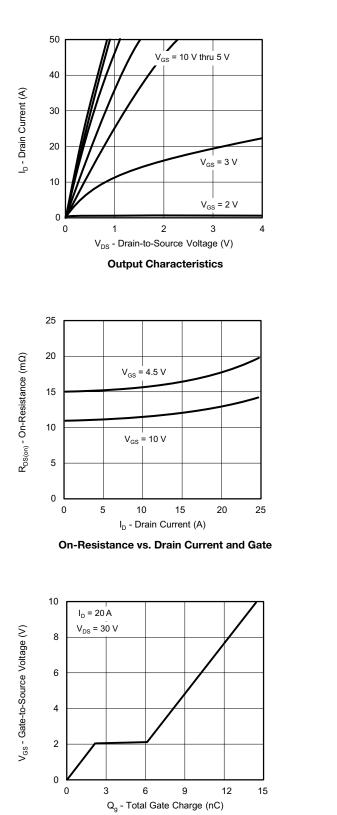
Notes

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 % b. Guaranteed by design, not subject to production testing

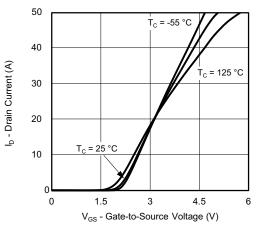
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



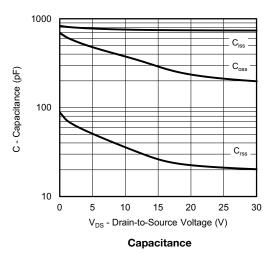
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

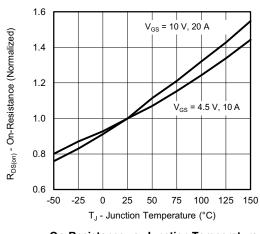


Gate Charge



Transfer Characteristics

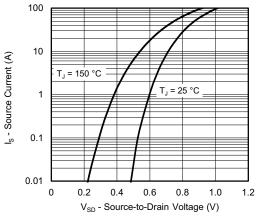




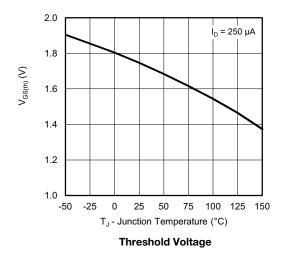
On-Resistance vs. Junction Temperature

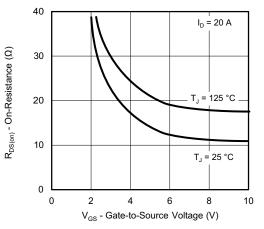


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

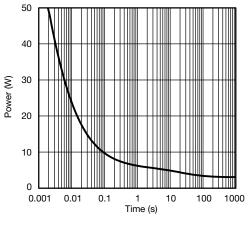


Source-Drain Diode Forward Voltage





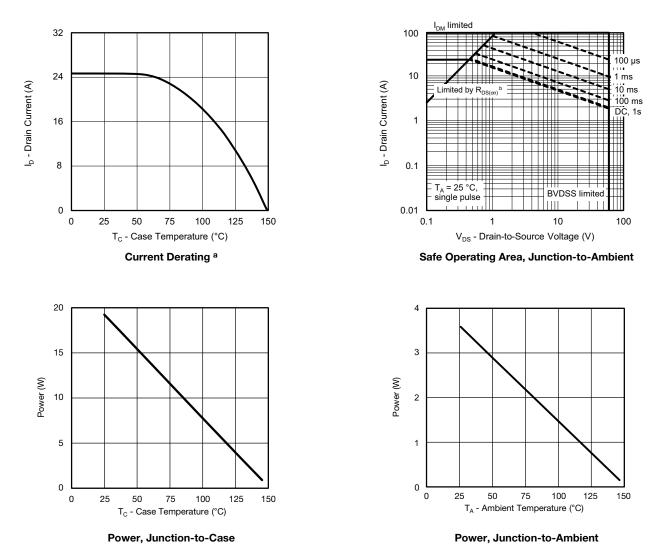
On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Notes

- a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-ambient thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit
- b. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified



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