

N-Channel 100 V (D-S) Super Junction Power MOSFET

| PRODUCT SUMMARY | | | | | |
|---------------------|----------------------------------|---------------------------------|-----------------------|--|--|
| V _{DS} (V) | $R_{DS(on)}$ (m Ω)(Typ.) | I _D (A) ^a | Q _g (Typ.) | | |
| 100 | 6 at V _{GS} = 10 V | 55 | 27 nC | | |
| | 10.5 at V _{GS} = 4.5 V | 40 | 27110 | | |

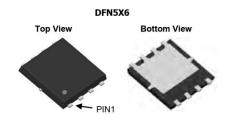
FEATURES

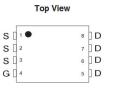
- DT-SJ Power MOSFET
- 100 % Rg and UIS Tested

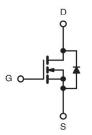


APPLICATIONS

- DC/DC Primary Side Switch
- Telecom/Server 48 V, Full/Half-Bridge DC/DC







N-Channel MOSFET

| ABSOLUTE MAXIMUM RATINGS | (T _A = 25 °C, unle | ess otherwise i | noted) | | |
|---|-----------------------------------|-----------------|----------------------|------|--|
| Parameter | | Symbol | Limit | Unit | |
| Drain-Source Voltage | | V _{DS} | 100 | V | |
| Gate-Source Voltage | V _{GS} | ± 20 | | | |
| | T _C = 25 °C | | 55 | | |
| Continuous Drain Current (T _{.I} = 150 °C) | T _C = 70 °C | I _D | 38 | | |
| Continuous Brain Guiterit (1) = 130 C) | T _A = 25 °C | | 15.6 ^{b, c} | | |
| | T _A = 70 °C | | 12.2 ^{b, c} | Α | |
| Pulsed Drain Current (t = 300 μs) | | I _{DM} | 200 | A | |
| Continuous Source-Drain Diode Current | T _C = 25 °C | I _S | 55 | | |
| Continuous Source-Diam Blode Current | T _A = 25 °C | 'S | 4.5 ^{b, c} | | |
| Single Pulse Avalanche Current | 1 0.1 ml l | I _{AS} | 45 | | |
| Single Pulse Avalanche Energy | L = 0.1 mH | E _{AS} | 70 | mJ | |
| | T _C = 25 °C | | 108 | W | |
| Maximum Power Dissipation | T _C = 70 °C | P _D | 69 | | |
| | T _A = 25 °C |] '' | 5.5 ^{b, c} | | |
| | T _A = 70 °C | | 3.5 ^{b, c} | | |
| Operating Junction and Storage Temperature Ra | T _J , T _{stg} | - 55 to 150 | °C | | |
| Soldering Recommendations (Peak Temperature | | 260 | C | | |

| THERMAL RESISTANCE RATINGS | | | | | | |
|---|--------------|-------------------|---------|---------|------|--|
| Parameter | | Symbol | Typical | Maximum | Unit | |
| Maximum Junction-to-Ambient ^{b, f} | t ≤ 10 s | R _{thJA} | 20 | 25 | °C/W | |
| Maximum Junction-to-Case (Drain) | Steady State | R_{thJC} | 2.1 | 2.8 | | |

- a. Based on T_C = 25 °C. b. Surface mounted on 1" x 1" FR4 board.
- d. The DFN5X6 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under steady state conditions is 70 °C/W.



| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
|---|-------------------------|--|------|-------|-------|-------|--|
| Static | | | | | | | |
| Drain-Source Breakdown Voltage | V _{DS} | $V_{GS} = 0, I_D = 250 \mu A$ | 100 | | | V | |
| V _{DS} Temperature Coefficient | $\Delta V_{DS}/T_{J}$ | I _D = 250 μA | | 64 | | mV/°C | |
| V _{GS(th)} Temperature Coefficient | $\Delta V_{GS(th)}/T_J$ | 10 = 200 μΛ | | - 5.8 | | | |
| Gate-Source Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_D = 250 \mu A$ | 1.5 | | 3.5 | V | |
| Gate-Source Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$ | | | ± 100 | nA | |
| Zana Oata Waltana Dusin Oamani | I _{DSS} | V _{DS} = 100 V, V _{GS} = 0 V | | | 1 | μA | |
| Zero Gate Voltage Drain Current | | V_{DS} = 64 V, V_{GS} = 0 V, T_{J} = 55 °C | | | 10 | | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$ | 55 | | | Α | |
| Drain-Source On-State Resistance ^a | D | $V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$ | | 6 | 7.5 | 0 | |
| | R _{DS(on)} | $V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$ | | 10.5 | 15.9 | mΩ | |
| Forward Transconductance ^a | 9 _{fs} | $V_{DS} = 10 \text{ V}, I_{D} = 15 \text{ A}$ | | 65 | | S | |
| Dynamic ^b | | | | | | | |
| Input Capacitance | C _{iss} | | | 1610 | | pF | |
| Output Capacitance | C _{oss} | $V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$ | | 359 | | | |
| Reverse Transfer Capacitance | C _{rss} | | | 40 | | | |
| Total Gate Charge | Qq | | | 27 | | | |
| Gate-Source Charge | Q _{gs} | $V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 10 \text{ A}$ | | 4.3 | | nC | |
| Gate-Drain Charge | Q _{gd} | | | 7.2 | | | |
| Gate Resistance | R _g | f = 1 MHz | 0.2 | 1.05 | 2.1 | Ω | |
| Turn-On Delay Time | t _{d(on)} | | | 19 | | | |
| Rise Time | t _r | $V_{DD} = 50 \text{ V}, R_L = 5 \Omega$ | | 11 | | ns | |
| Turn-Off Delay Time | t _{d(off)} | $I_D\cong$ 10 A, V_{GEN} = 10 V, R_g = 1 Ω | | 45 | | | |
| Fall Time | t _f | | | 8 | | | |
| Drain-Source Body Diode Characteristic | s | | | | | | |
| Continuous Source-Drain Diode Current | I _S | T _C = 25 °C | | | 55 | A | |
| Pulse Diode Forward Current ^a | I _{SM} | | | | 200 | | |
| Body Diode Voltage | V _{SD} | I _S = 1 A | | 0.7 | 1 | V | |
| Body Diode Reverse Recovery Time | t _{rr} | | | 56 | | ns | |
| ody Diode Reverse Recovery Charge Q | | | 38 | | nC | | |
| Reverse Recovery Fall Time | t _a | $I_F = 10 \text{ A}, \text{ dl/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$ | | 22 | | | |
| Reverse Recovery Rise Time | t _b | | | 15 | | ns | |

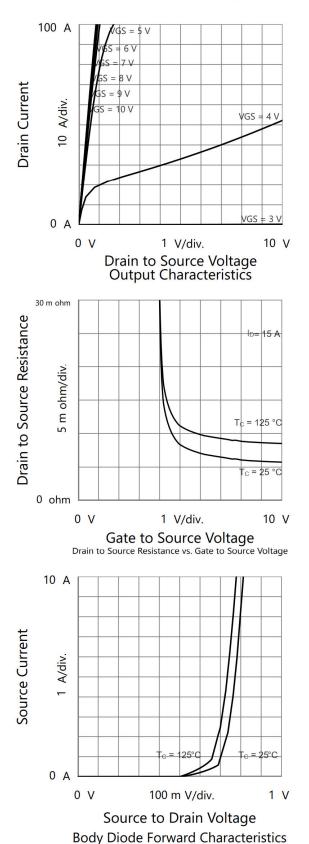
- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.

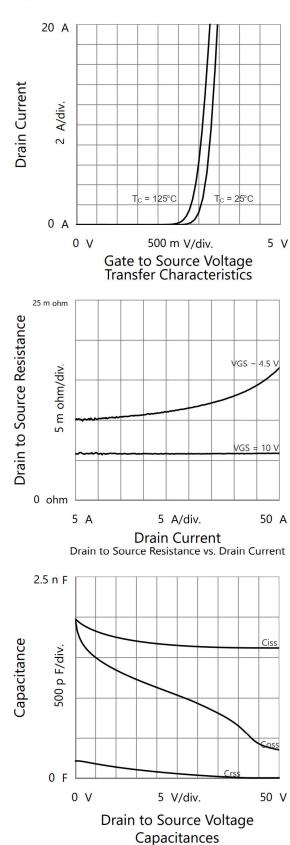
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



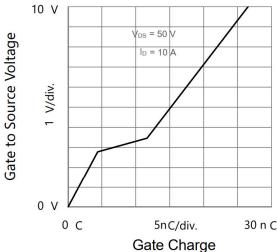
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

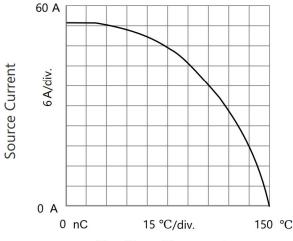




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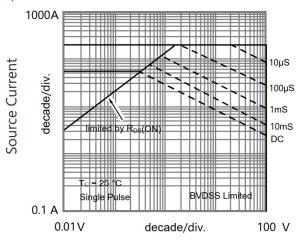


Gate to Source Voltage vs. GateCharge

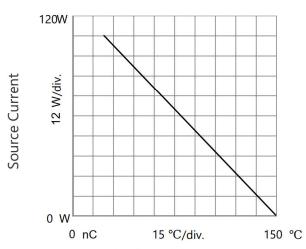


T_C - Case Temperature

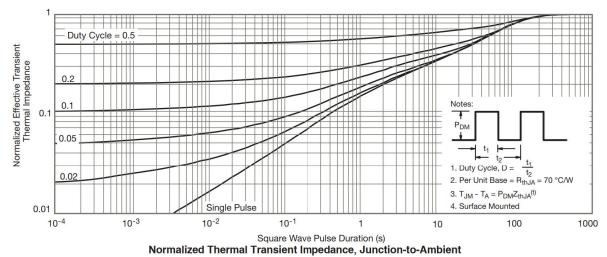
Current Derating



Source to Drain Voltage Safe Operating Area, Junction-to-Ambient



T_C - Case Temperature **Current Derating**







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