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P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^a	Q _g (Typ.)	
- 60	7 at V _{GS} = - 10 V	85	76 nC	
	8.5 at V _{GS} = - 4.5 V	- 65		

FEATURES

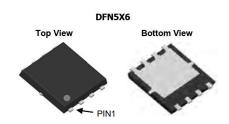
- DT-Trench Power MOSFET
- \bullet 100 % R $_{g}$ and UIS Tested

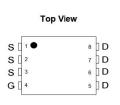
APPLICATIONS

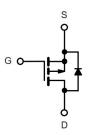
- PWM Applications
- Load Switch
- Power Management



ROHS







P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$(T_C = 25 ^{\circ}C, \text{ unles})$	s otherwise noted)		
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-Source Voltage		V _{DS}	- 60	V	
Gate-Source Voltage		V_{GS}	± 20	1	
Continuous Drain Current	T _C = 25 °C		- 85		
Continuous Drain Current	T _C = 100 °C	I _D	- 54		
Continuous Source Current (Diode Conduction)		Is	- 85	A	
Pulsed Drain Current ^a		I _{DM}	- 340		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	- 80		
Single Pulse Avalanche Energy	L = 0.1 MH	E _{AS}	230	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C	D	120	W	
Maximum Fower Dissipation	T _C = 100 °C	P_{D}	48	Į vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to + 150	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-Case (Drain) ^{c,d}	RthJC	1.04	°C/W	

Notes:

- a. Based on $T_C=25^{\circ}C$.
- b. Pulse test; pulse width 300 µs, duty cycle 2 %.
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.



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Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 60			V
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$			1	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
7 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	I _{DSS}	V _{DS} = -60 V, V _{GS} = 0 V			1	
Zero Gate Voltage Drain Current		V _{DS} = -60 V, V _{GS} = 0 V, T _J = 125 °C			1	μA
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 20 A		7	8	mΩ
		$V_{GS} = -4.5 \text{ V}, I_D = -20 \text{ A}$		8.5	10	
Forward Transconductance ^a	9 _{fs}	V _{DS} = -10 V,I _D = - 20 A		60		S
Dynamic ^b						
Input Capacitance	C _{iss}			12300		pF
Output Capacitance	C _{oss}	$V_{DS} = -30 \text{ V,V}_{GS} = 0 \text{ V, f} = 1 \text{ MHz}$		485		
Reverse Transfer Capacitance	C _{rss}			604		
Total Gate Charge	Q_g			76		nC
Gate-Source Charge	Q_{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -30 \text{ V}, I_{D} = -20 \text{ A}$		10		
Gate-Drain Charge	Q_{gd}			8		
Gate Resistance	R_{g}	f = 1 MHz		1		Ω
Turn-On Delay Time	t _{d(on)}			58		
Rise Time	t _r	V_{DS} = -30 V, R_L = 4 Ω		23		ns
Turn-Off Delay Time	t _{d(off)}	$I_D \simeq -20 \text{A}, V_{GS} = -10 \text{V}, R_g = 6 \Omega$		105		
Fall Time	t _f			21		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	Is	100			- 85	Α
Pulse Diode Forward Current ^a	I _{SM}				- 340	
Body Diode Voltage	V _{SD}	I _S = - 1 A			- 1.2	V
Body Diode Reverse Recovery Time	t _{rr}	1 00 A 11/11 400 A/ T 05 00		65		ns
Body Diode Reverse Recovery Charge	$I_F = -20 \text{ A, di/dt} = 100 \text{ A/µs, T}_J = 25 ^{\circ}\text{C}$			74		nC

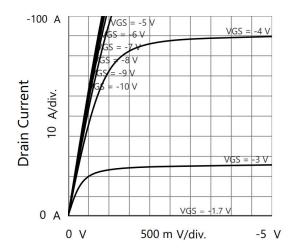
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

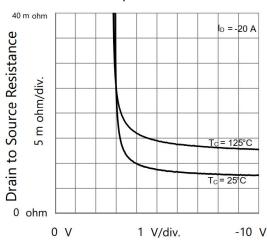
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)

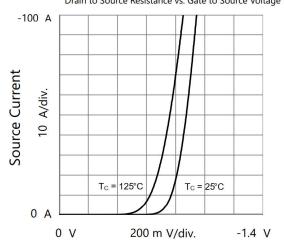
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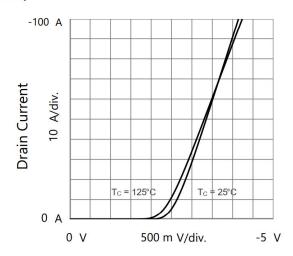
Drain to Source Voltage Output Characteristics



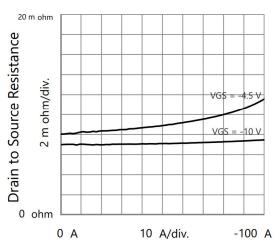
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



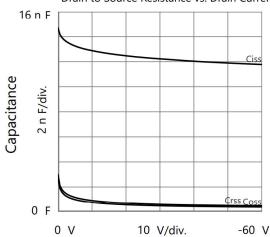
Source to Drain Voltage Body Diode Forward Characteristics



Gate to Source Voltage Transfer Characteristics



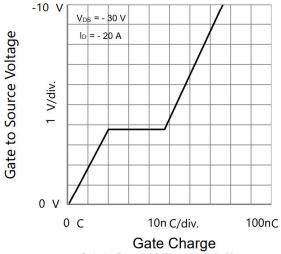
Drain Current
Drain to Source Resistance vs. Drain Current



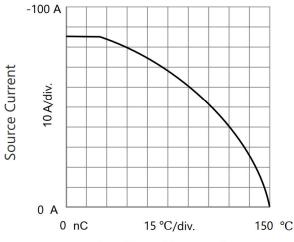
Drain to Source Voltage Capacitances

TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)

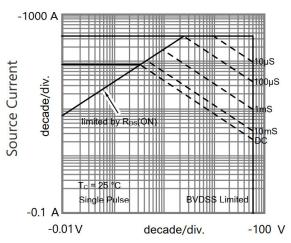
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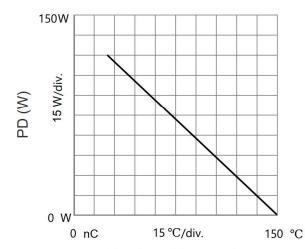
Gate to Source Voltage vs. GateCharge



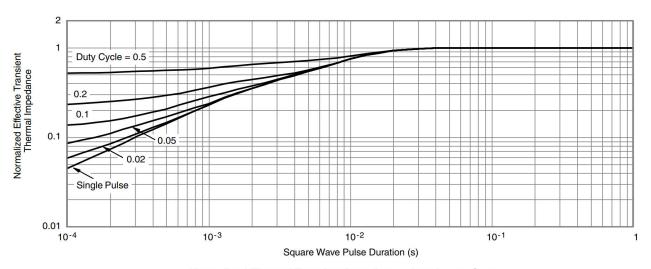
T_C - Case Temperature **Current Derating**



Source to Drain Voltage Safe Operating Area, Junction-to-Ambient



 T_{C} - Case Temperature $_{\text{Power Derating}}$



Normalized Thermal Transient Impedance, Junction-to-Case





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