

# N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (mΩ)(Typ.)	I <sub>D</sub> (A) <sup>a, e</sup>	Q <sub>g</sub> (Typ.)		
30	2.5 at V <sub>GS</sub> = 10 V	95	54 nC		
	4.6 at V <sub>GS</sub> = 4.5 V	70	34 NC		

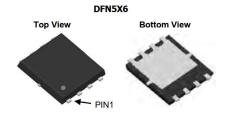
#### **FEATURES**

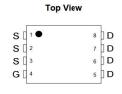
- · DT-Trench Power MOSFET
- 100 %  $R_{\rm g}$  and UIS Tested

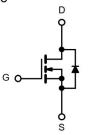
# COMPLIANT

### **APPLICATIONS**

- OR-ing
- Server
- Notebook PC Core







N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)					
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	30	V		
Gate-Source Voltage	V <sub>GS</sub>	± 20	•		
	T <sub>C</sub> = 25 °C		95 <sup>a, e</sup>		
Continuous Drain Current (T <sub>.I</sub> = 175 °C)	T <sub>C</sub> = 70 °C	l <sub>D</sub>	80 <sup>e</sup>		
Continuous Diain Current (1,1 = 175 C)	T <sub>A</sub> = 25 °C		30 <sup>b, c</sup>	A	
	T <sub>A</sub> = 70 °C		21 <sup>b, c</sup>		
Pulsed Drain Current		I <sub>DM</sub>	360		
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	86		
Single Pulse Avalanche Energy	L = 0.1 IIII1	E <sub>AS</sub>	59	mJ	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	I <sub>S</sub>	95 <sup>a, e</sup>	Α	
Continuous Source-Drain Blode Current	T <sub>A</sub> = 25 °C	'S	3.1 <sup>b, c</sup>		
	T <sub>C</sub> = 25 °C		240 <sup>a</sup>		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	168	w	
	T <sub>A</sub> = 25 °C		3.65 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		2.56 <sup>b, c</sup>		
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	35	41	°C/W	
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.4	0.625		

#### Notes:

- Notes:
  a. Based on T<sub>C</sub> = 25 °C.
  b. Surface mounted on 1" x 1" FR4 board.
  c. t = 10 s.
  d. Maximum under steady state conditions is 90 °C/W.
- e. Calculated based on maximum junction temperature.



Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		35		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	10 - 200 μΛ		- 7.5			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1		3	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
7	I <sub>DSS</sub>	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	110			Α	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		2.5	3	mΩ	
		$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		4.6	5.5		
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A		155		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			2440		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		302			
Reverse Transfer Capacitance	C <sub>rss</sub>			275			
Total Gate Charge	Qg			54		nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 24 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 30 \text{ A}$		4.5			
Gate-Drain Charge	Q <sub>gd</sub>			12.5			
Gate Resistance	$R_{g}$	f = 1 MHz		2.5		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			23			
Rise Time	t <sub>r</sub>	$V_{DD} = 24 \text{ V}, R_{L} = 0.555 \Omega$		11		-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_{D} \cong 30 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		60			
Fall Time	t <sub>f</sub>			13			
Turn-On Delay Time	t <sub>d(on)</sub>			45		ns	
Rise Time	t <sub>r</sub>	$V_{DD} = 15 \text{ V}, R_{L} = 0.625 \Omega$		160		-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 20 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		44			
Fall Time	t <sub>f</sub>			8			
<b>Drain-Source Body Diode Characteristic</b>	s						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			95		
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				360	A	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 1 A		0.6	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			50	82	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	L = 20 A di/dt = 100 A/us T = 25 °C		70	98	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C		20			
Reverse Recovery Rise Time	t <sub>b</sub>			28		ns	

#### Notes:

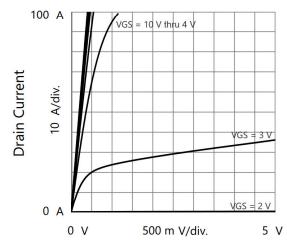
- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

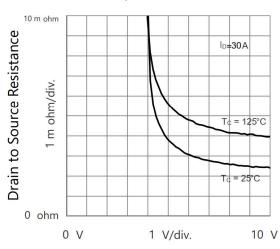




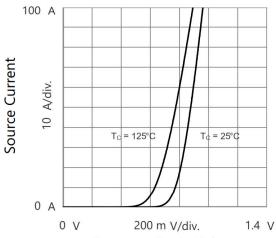
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



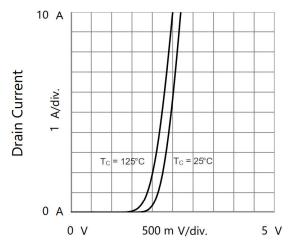
Drain to Source Voltage Output Characteristics



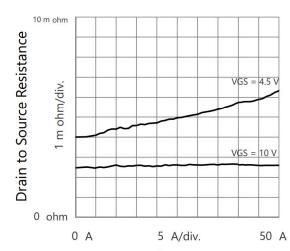
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



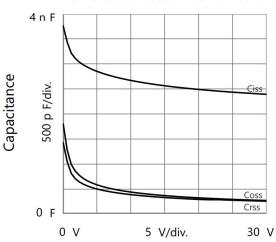
Source to Drain Voltage Body Diode Forward Characteristics



Gate to Source Voltage Transfer Characteristics



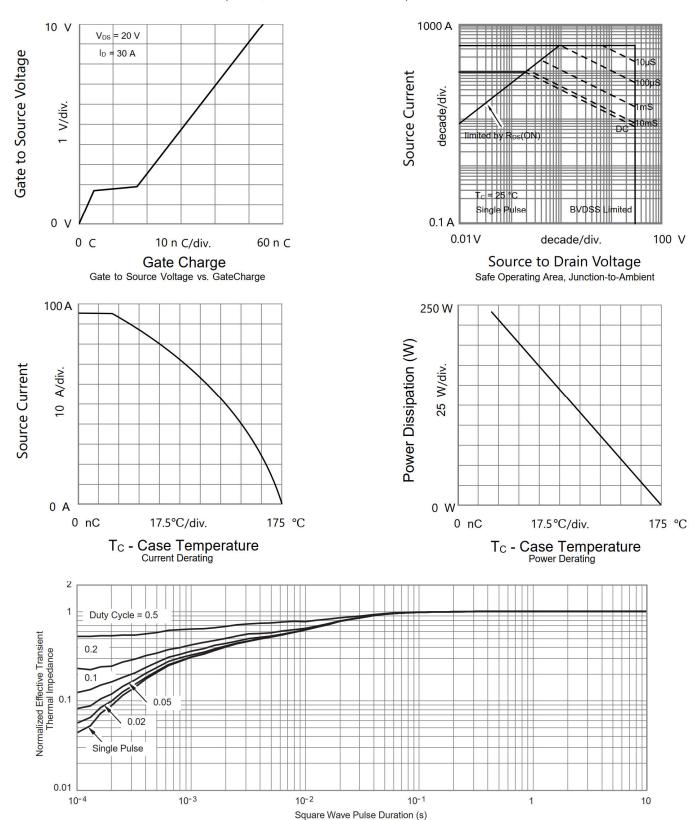
Drain Current
Drain to Source Resistance vs. Drain Current



Drain to Source Voltage Capacitances



## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case





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