

# N-Channel 40 V (D-S) MOSFET

	PRODUCT SUMMARY				
Ī	V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (mΩ)(Typ.)	I <sub>D</sub> (A) <sup>a, e</sup>	Q <sub>g</sub> (Typ.)	
Ī	40	4.7 at V <sub>GS</sub> = 10 V	70	70 nC	
		6 at V <sub>GS</sub> = 4.5 V	65	70110	

### **FEATURES**

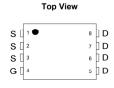
- DT-Trench Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested

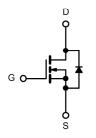


## **APPLICATIONS**

- · Notebook PC Core
- VRM/POL







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage	V <sub>DS</sub>	40	_ V			
Gate-Source Voltage		V <sub>GS</sub>			± 20	
	T <sub>C</sub> = 25 °C	I <sub>D</sub>	70 <sup>a, e</sup>			
Continuous Drain Current (T <sub>J</sub> = 175 °C)	T <sub>C</sub> = 70 °C		60 <sup>e</sup>			
Continuous Brain Current (1) = 175 C)	T <sub>A</sub> = 25 °C		19 <sup>b, c</sup>	A		
	T <sub>A</sub> = 70 °C		18.6 <sup>b, c</sup>	] ^		
Pulsed Drain Current	·	I <sub>DM</sub>	280			
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	59			
Single Pulse Avalanche Energy	L = 0.111111	E <sub>AS</sub>	210	mJ		
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	l <sub>S</sub>	70 <sup>a, e</sup>	А		
	T <sub>A</sub> = 25 °C	''s	3.5 <sup>b, c</sup>	] ^		
	T <sub>C</sub> = 25 °C	- P <sub>D</sub>	105 <sup>a</sup>	W		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C		55			
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C		6.15 <sup>b, c</sup>	] **		
	T <sub>A</sub> = 70 °C		3.05 <sup>b, c</sup>			
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C			

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	15	25	°C/W
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	1.0	1.5	5/77

### Notes:

- a. Based on T<sub>C</sub> = 25 °C.
  b. Surface mounted on 1" x 1" FR4 board.

- c. t = 10 s.
  d. Maximum under steady state conditions is 90 °C/W.
  e. Calculated based on maximum junction temperature.



Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	40			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I_ = 250 HA		35		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = 250 \mu\text{A}$		- 5.5			
Gate-Source Threshold Voltage			1.2		2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zoro Coto Voltago Drain Current	1	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V			1	1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	70			Α	
D : 0	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		4.7	5.2	mΩ	
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 20 A		6	7		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A		110		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			3470		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ = 20 V, $V_{GS}$ = 0 V, f = 1 MHz		315			
Reverse Transfer Capacitance	C <sub>rss</sub>			300			
Total Gate Charge	Q <sub>q</sub>			70		nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 20 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$		32			
Gate-Drain Charge	Q <sub>gd</sub>			27			
Gate Resistance	R <sub>g</sub>	f = 1 MHz		2.2		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			18			
Rise Time	$t_r$ $V_{DD} = 15 \text{ V, R}_L = 0.555 \Omega$			11			
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 30 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		70			
Fall Time	t <sub>f</sub>			10			
Turn-On Delay Time	t <sub>d(on)</sub>			55		ns	
Rise Time	$t_r$ $V_{DD} = 15 \text{ V, R}_L = 0.625 \Omega$			180			
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 20 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		55			
Fall Time	t <sub>f</sub>			12		1	
<b>Drain-Source Body Diode Characteristic</b>	s						
Continuous Source-Drain Diode Current	Is	T <sub>C</sub> = 25 °C			70	_	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				280	A	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 1 A		0.6	1	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			52	78	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	L = 30 A di/dt = 100 A/v = T = 25 °C		70.2	105	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 30 \text{ A, di/dt} = 100 \text{ A/}\mu\text{s, T}_J = 25 ^{\circ}\text{C}$		27		ns	
Reverse Recovery Rise Time	t <sub>b</sub>			25			

### Notes:

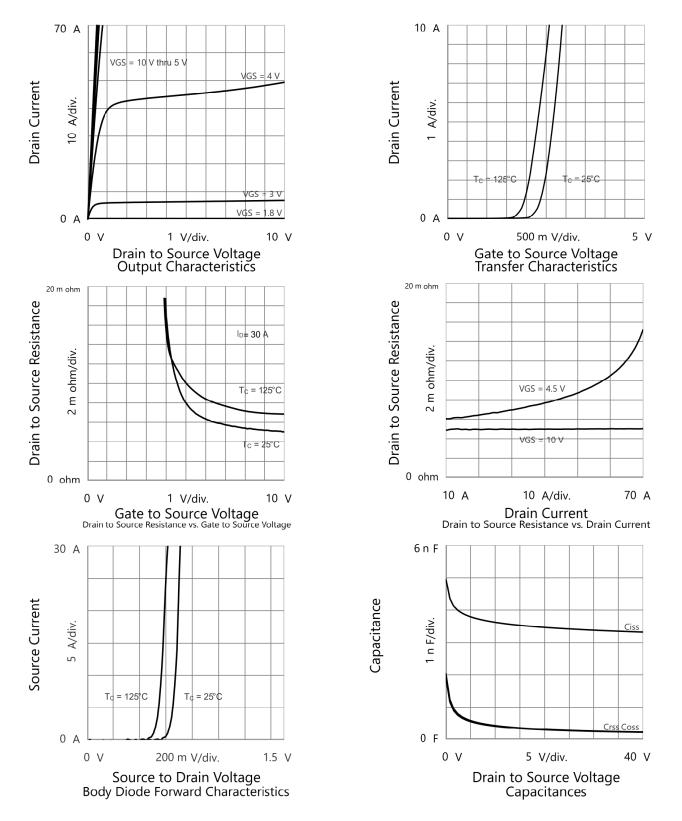
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq 300~\mu\text{s},$  duty cycle  $\leq 2~\%.$ 

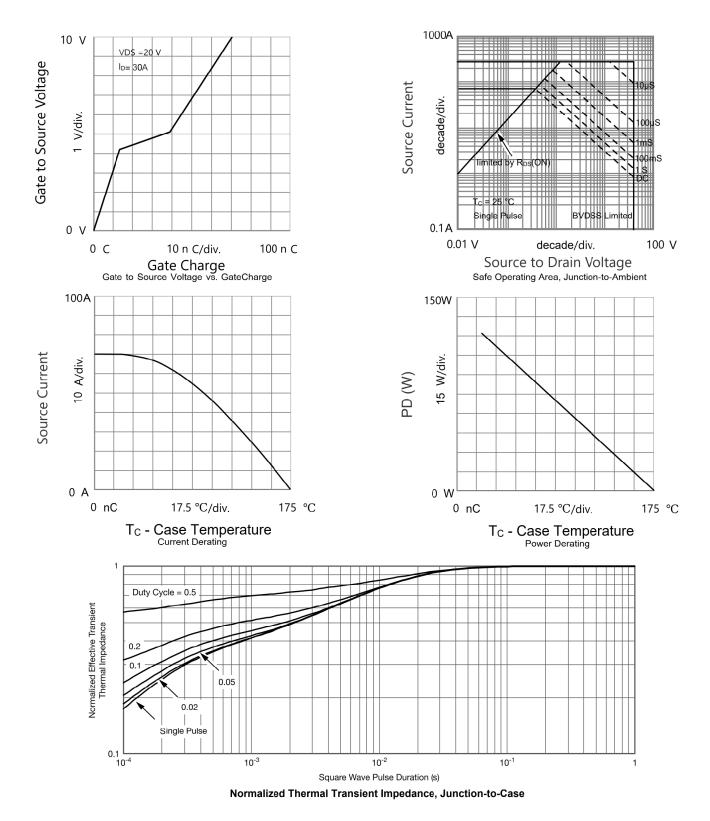
b. Guaranteed by design, not subject to production testing.



## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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