N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ.)
60	0.0048 at V_{GS} = 10 V	72[a]	13.5 nC
	0.0080 at V_{GS} = 4.5 V	50[a]	

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested

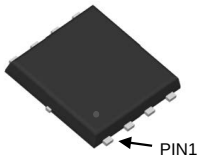
RoHS COMPLIANT

APPLICATIONS

- Primary Side Switching
- Synchronous Rectification
- DC/DC Converters
- Boost Converters
- DC/AC Inverters

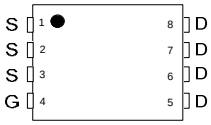
DFN5X6

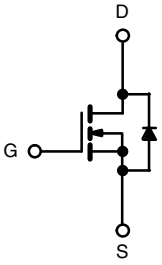
Top View



Bottom View

Top View





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)				
Parameter		**Symbol**	**Limit**	**Unit**
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 150 °C)	T_C = 25 °C	I_D	72[a]	A
	T_C = 70 °C		48[a]	
	T_A = 25 °C		24[b, c]	
	T_A = 70 °C		19.2[b, c]	
Pulsed Drain Current (t = 100 µs)		I_{DM}	240	
Continuous Source-Drain Diode Current	T_C = 25 °C	I_S	70[a]	
	T_A = 25 °C		4.5[b, c]	
Single Pulse Avalanche Current	L = 0.1 mH	I_{AS}	45	
Single Pulse Avalanche Energy		E_{AS}	205	mJ
Maximum Power Dissipation	T_C = 25 °C	P_D	106	W
	T_C = 70 °C		73	
	T_A = 25 °C		4.5[b, c]	
	T_A = 70 °C		2.8[b, c]	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 175	°C
Soldering Recommendations (Peak Temperature)			260	

THERMAL RESISTANCE RATINGS					
Parameter		**Symbol**	**Typical**	**Maximum**	**Unit**
Maximum Junction-to-Ambient[b]	t ≤ 10 s	R_{thJA}	20	25	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	1.0	2.0	

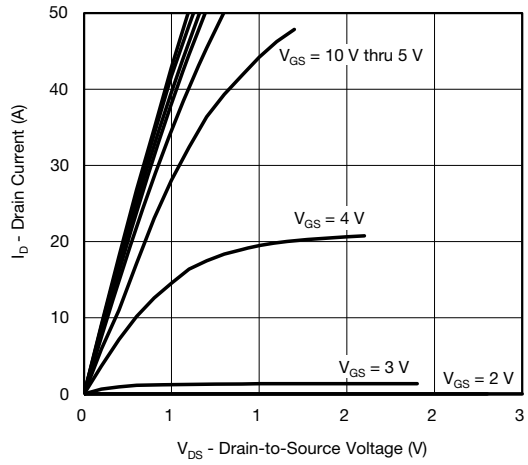
Notes:
a. Package limited.
b. Surface mounted on 1" x 1" FR4 board.
c. t = 10 s.

SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
Parameter	**Symbol**	**Test Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	60			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	I_D = 250 µA		86		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 5.5		
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	1		3	V
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 48 V, V_{GS} = 0 V			1	µA
		V_{DS} = 48 V, V_{GS} = 0 V, T_J = 55 °C			10	
On-State Drain Current[a]	$I_{D(on)}$	V_{DS} ≥ 5 V, V_{GS} = 10 V	72			A
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 20 A		0.0048	0.0065	Ω
		V_{GS} = 6 V, I_D = 15 A		0.0065	0.0078	
		V_{GS} = 4.5 V, I_D = 10 A		0.0080	0.0096	
Forward Transconductance[a]	g_{fs}	V_{DS} = 48 V, I_D = 20 A		60		S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{DS} = 48 V, V_{GS} = 0 V, f = 1 MHz		1950		pF
Output Capacitance	C_{oss}			850		
Reverse Transfer Capacitance	C_{rss}			50		
Total Gate Charge	Q_g	V_{DS} = 48 V, V_{GS} = 10 V, I_D = 20 A		13.5	18	nC
		V_{DS} = 48 V, V_{GS} = 6 V, I_D = 15 A		11.5	16	
		V_{DS} = 48 V, V_{GS} = 4.5 V, I_D = 10 A		9	15	
Gate-Source Charge	Q_{gs}			7		
Gate-Drain Charge	Q_{gd}			4		
Output Charge	Q_{oss}	V_{DS} = 48 V, V_{GS} = 0 V		23.5	46	
Gate Resistance	R_g	f = 1 MHz	0.5	1.8	3	Ω
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 48 V, R_L = 3 Ω I_D ≅ 10 A, V_{GEN} = 10 V, R_g = 1 Ω		19	38	ns
Rise Time	t_r			8	16	
Turn-Off Delay Time	$t_{d(off)}$			31	60	
Fall Time	t_f			7	14	
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 48 V, R_L = 3 Ω I_D ≅ 10 A, V_{GEN} = 4.5 V, R_g = 1 Ω		42	80	
Rise Time	t_r			81	150	
Turn-Off Delay Time	$t_{d(off)}$			28	50	
Fall Time	t_f			8	16	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	T_C = 25 °C			70	A
Pulse Diode Forward Current (t_p = 100 µs)	I_{SM}				240	
Body Diode Voltage	V_{SD}	I_S = 5 A		0.7	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = 10 A, dI/dt = 100 A/µs, T_J = 25 °C		40	80	ns
Body Diode Reverse Recovery Charge	Q_{rr}			37	74	nC
Reverse Recovery Fall Time	t_a			18		ns
Reverse Recovery Rise Time	t_b			22		

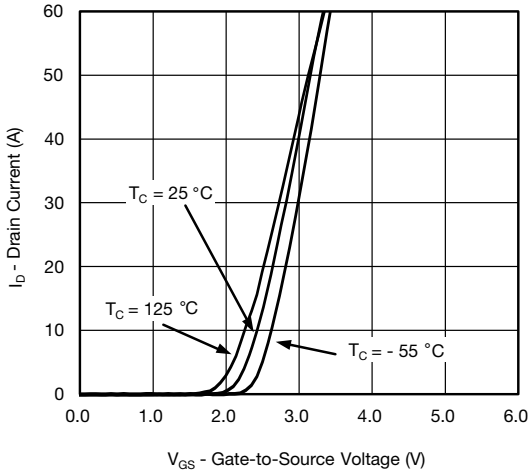
Notes:
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

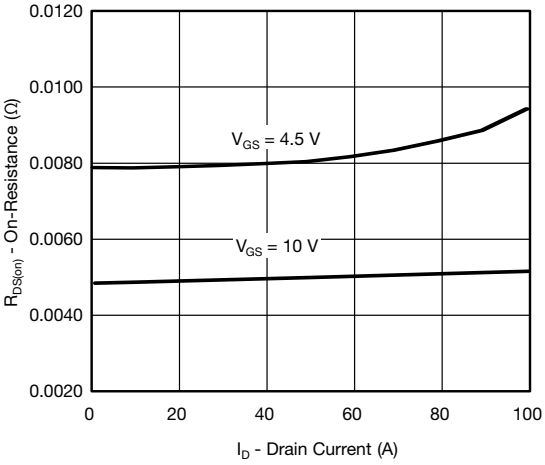
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



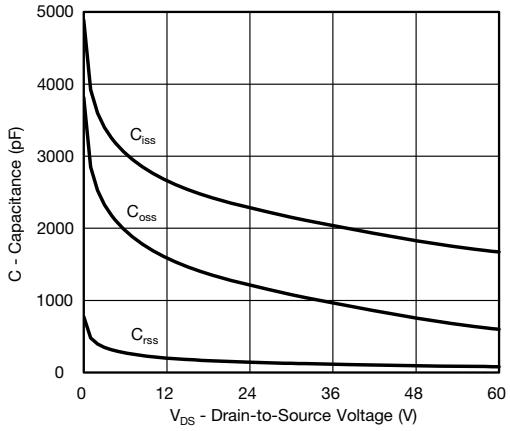
Output Characteristics



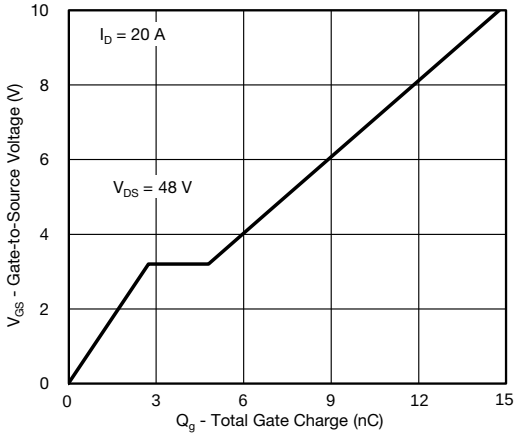
Transfer Characteristics



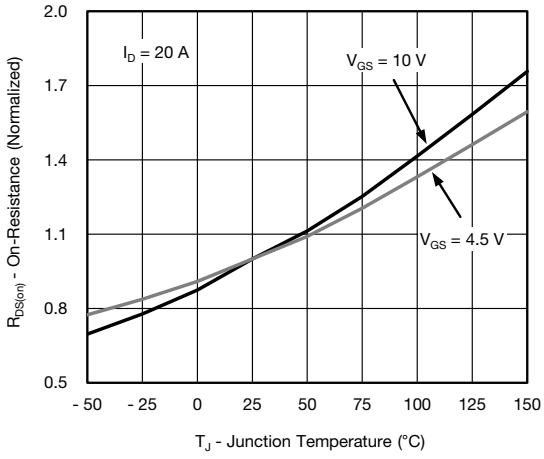
On-Resistance vs. Drain Current



Capacitance

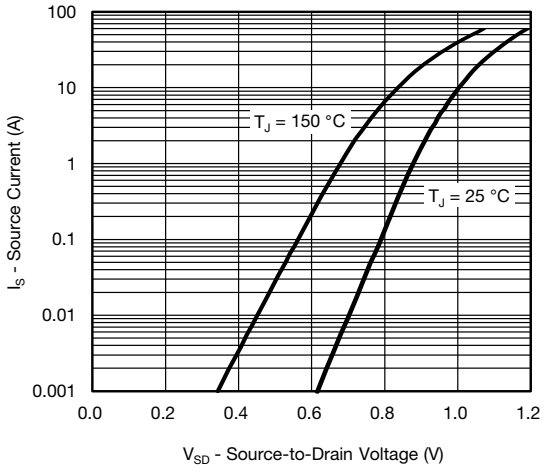


Gate Charge

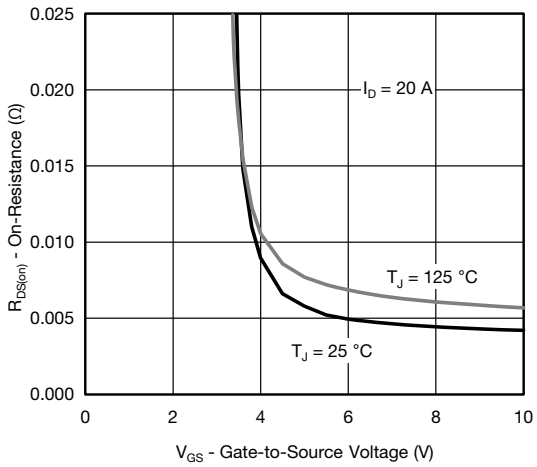


On-Resistance vs. Junction Temperature

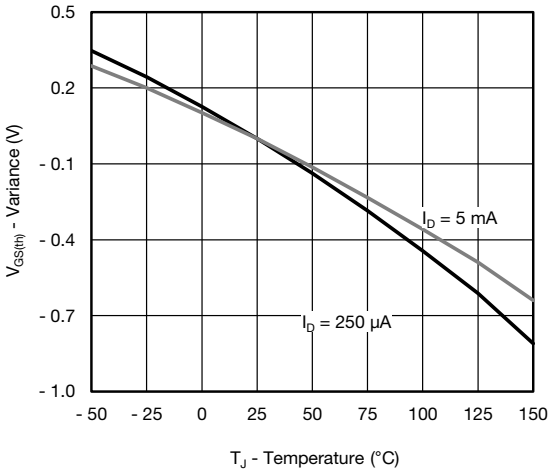
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



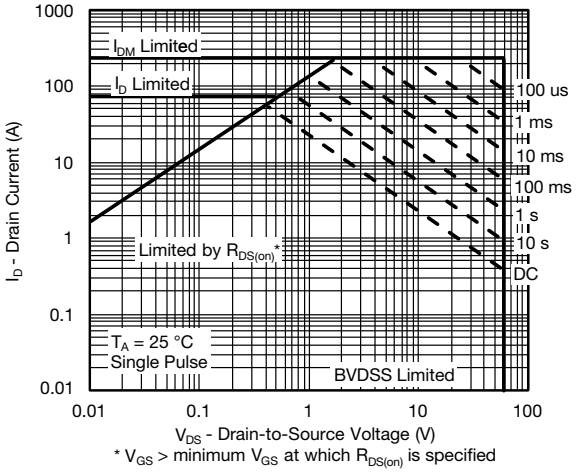
Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage

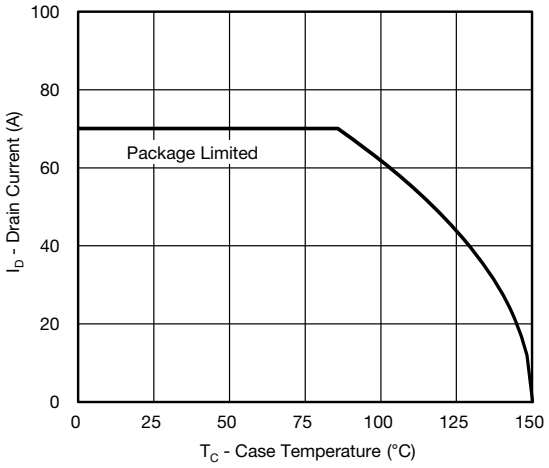


Threshold Voltage

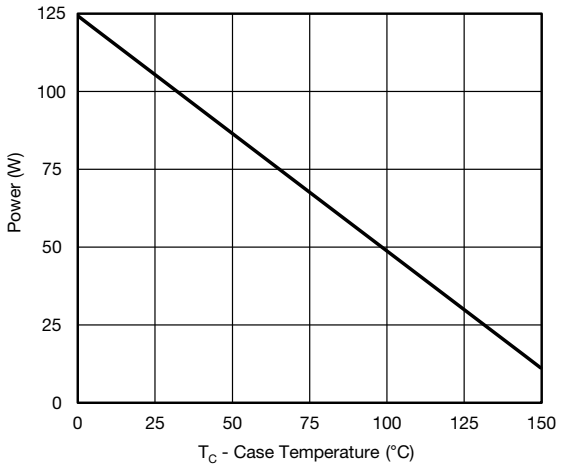


Safe Operating Area, Junction-to-Ambient

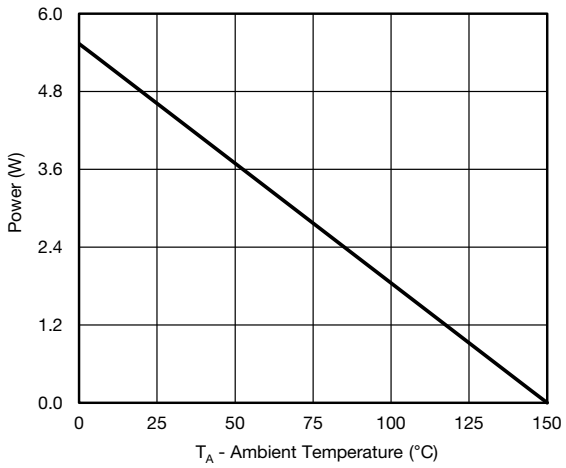
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating*



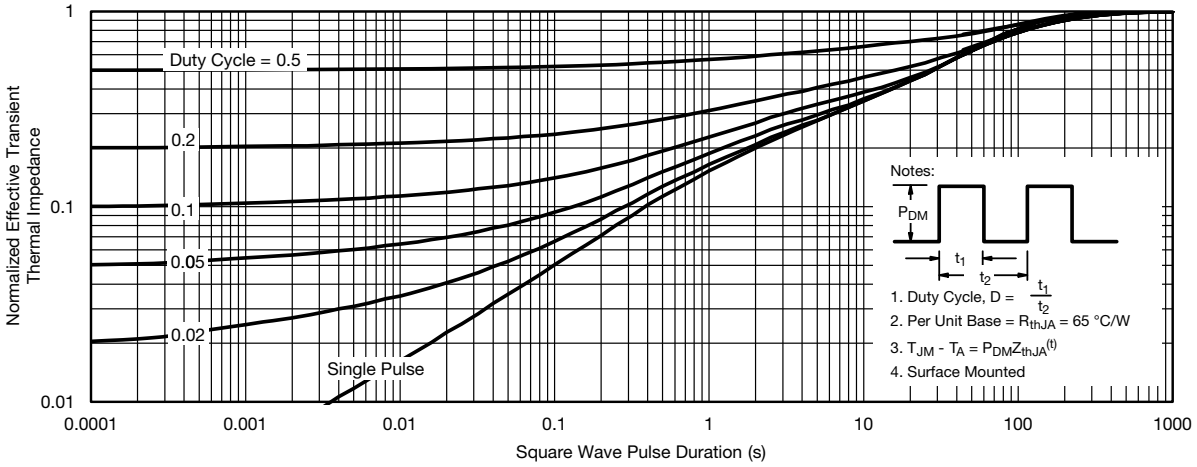
Power, Junction-to-Case



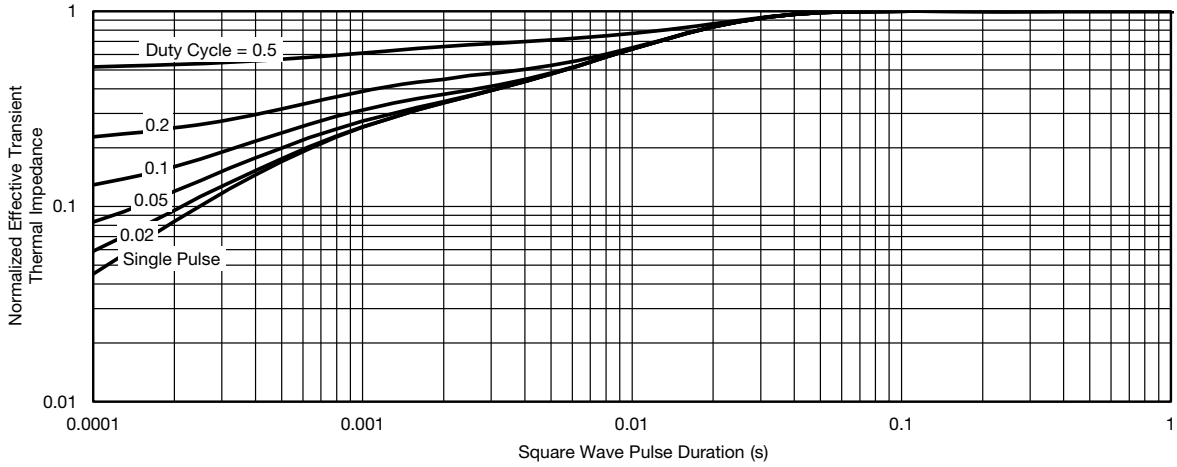
Power, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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