

P-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ.)
- 100	0.450 at $V_{GS} = - 10$ V	-1.5	8.1
	0.557 at $V_{GS} = - 4.5$ V	-1.2	

FEATURES

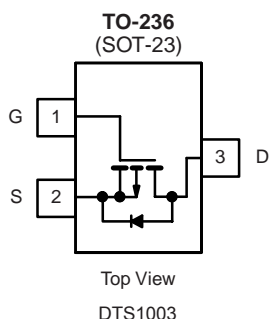
- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET® Power MOSFET
- Ultra Low On-Resistance
- Small Size



RoHS
 COMPLIANT
 HALOGEN
FREE
 Available

APPLICATIONS

- Active Clamp Circuits in DC/DC Power Supplies



ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter	Symbol	LIMIT	Unit
Drain-Source Voltage	V_{DS}	- 100	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150$ °C) ^{a, b}	I_D	$T_A = 25$ °C	A
		$T_A = 70$ °C	
Pulsed Drain Current	I_{DM}	- 4.5	
Continuous Source Current (Diode Conduction) ^{a, b}	I_S	- 6.8	
Avalanche Current	$L = 1.0$ mH	I_{AS}	2.2
Avalanche Energy		E_{AS}	1.31
Maximum Power Dissipation ^{a, b}	P_D	$T_A = 25$ °C	W
		$T_A = 70$ °C	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	R_{thJA}	85	150	°C/W
		140	186	
Maximum Junction-to-Foot (Drain)	R_{thJF}	45	60	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. Pulse width limited by maximum junction temperature.

SPECIFICATIONS $T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Limits			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{ V}$, $I_D = -250\text{ }\mu\text{A}$	- 100			V
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = -250\text{ }\mu\text{A}$	- 1.2		- 2.5	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}$, $V_{GS} = \pm 20\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -80\text{ V}$, $V_{GS} = 0\text{ V}$			- 1	μA
		$V_{DS} = -80\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 55\text{ }^{\circ}\text{C}$			- 10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \leq -15\text{ V}$, $V_{GS} = 10\text{ V}$	- 2.6			A
Drain-Source On-Resistance ^a	$R_{DS(on)}$	$V_{GS} = -10\text{ V}$, $I_D = -0.5\text{ A}$		0.45	0.60	Ω
		$V_{GS} = -4.5\text{ V}$, $I_D = -0.5\text{ A}$		0.557	0.75	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15\text{ V}$, $I_D = -0.5\text{ A}$		2.1		S
Diode Forward Voltage	V_{SD}	$I_S = -1.0\text{ A}$, $V_{GS} = 0\text{ V}$		0.5	- 1.2	V
Dynamic ^b						
Total Gate Charge	Q_g	$V_{DS} = -80\text{ V}$, $V_{GS} = 10\text{ V}$, $I_D \cong -0.5\text{ A}$		8.1	13	nC
Gate-Source Charge	Q_{gs}			1.6		
Gate-Drain Charge	Q_{gd}			2.4		
Gate Resistance	R_g	$f = 1.0\text{ MHz}$		10		Ω
Input Capacitance	C_{iss}	$V_{DS} = -25\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$		570	689	pF
Output Capacitance	C_{oss}			136		
Reverse Transfer Capacitance	C_{rss}			27		
Switching ^c						
Turn-On Time	$t_{d(on)}$	$V_{DD} = -75\text{ V}$, $R_L = 75\text{ }\Omega$ $I_D \cong -1.0\text{ A}$, $V_{GEN} = -10\text{ V}$ $R_g = 6\text{ }\Omega$		8		ns
	t_r			14		
Turn-Off Time	$t_{d(off)}$			17		
	t_f			11		
Body Diode Reverse Recovery Charge	Q_{rr}	$I_F = 0.5\text{ A}$, $di/dt = 100\text{ A}/\mu\text{s}$		90		nC

Notes:

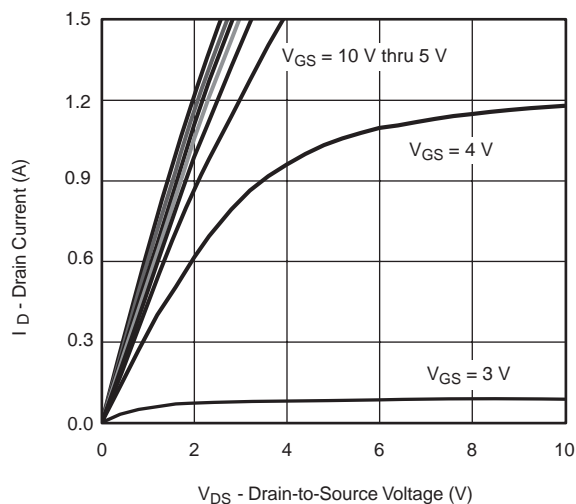
 a. Pulse test: $PW \leq 300\text{ }\mu\text{s}$ duty cycle $\leq 2\%$.

b. For DESIGN AID ONLY, not subject to production testing.

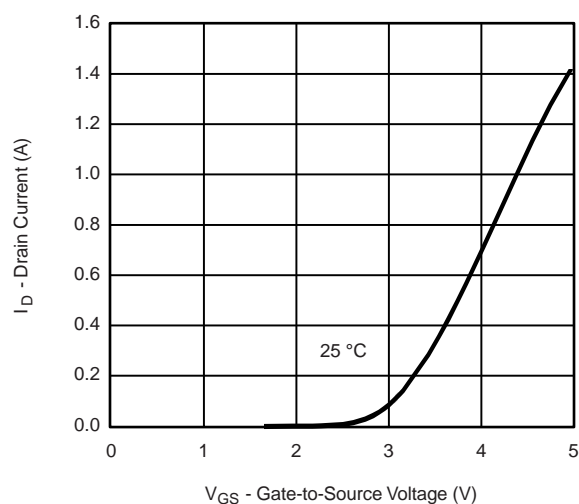
c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

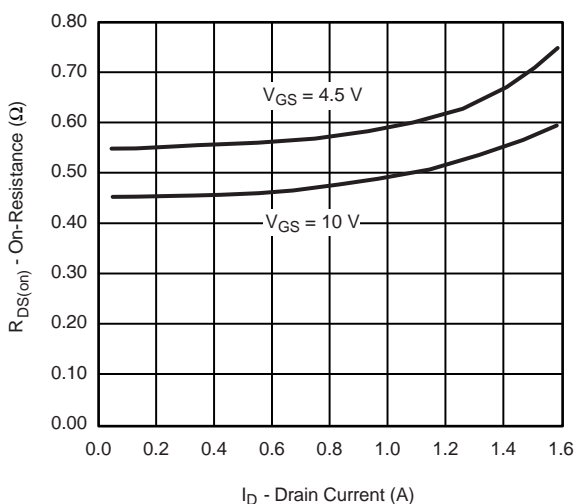
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



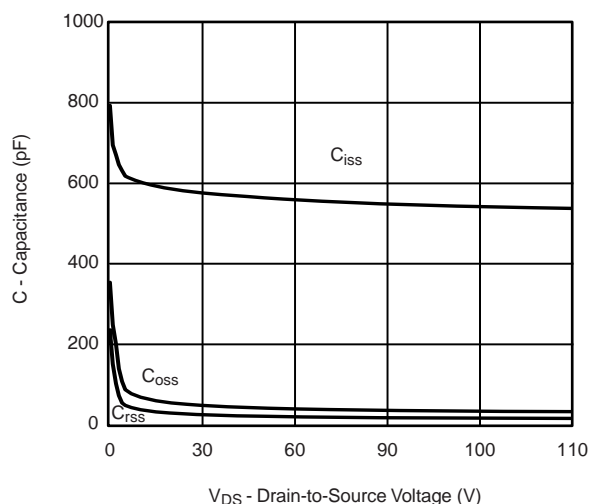
Output Characteristics



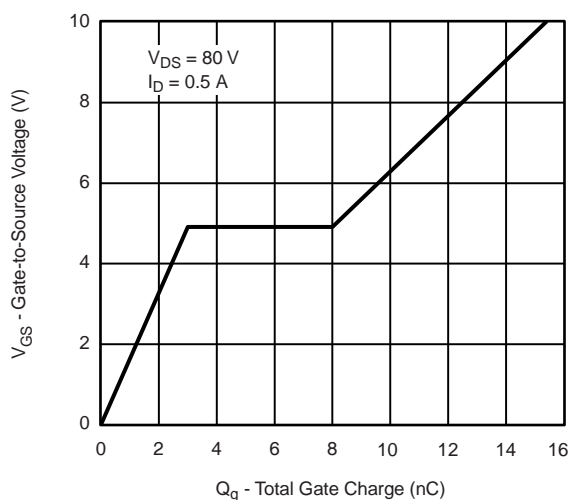
Transfer Characteristics



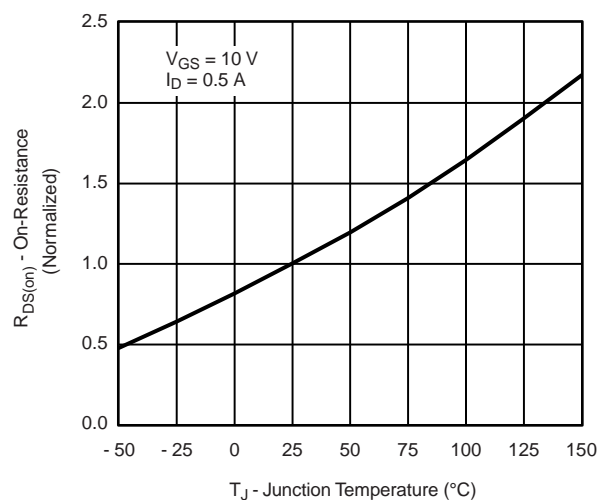
On-Resistance vs. Drain Current



Capacitance

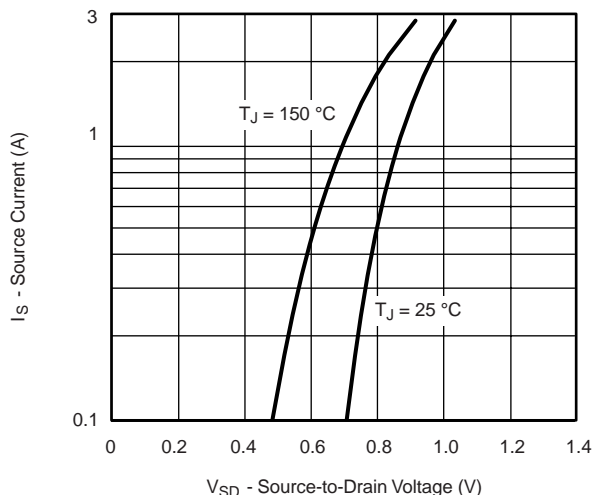


Gate Charge

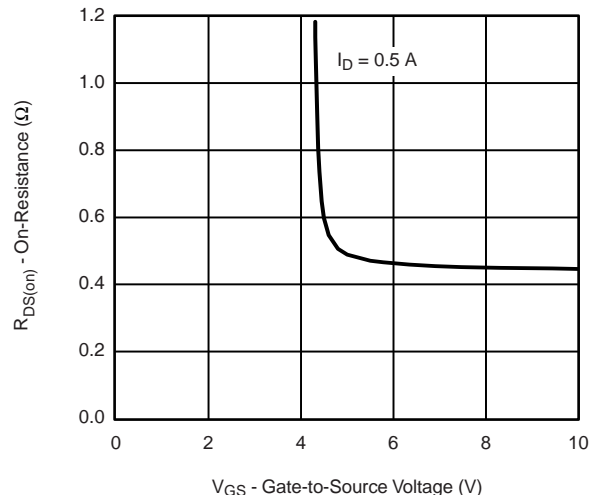


On-Resistance vs. Junction Temperature

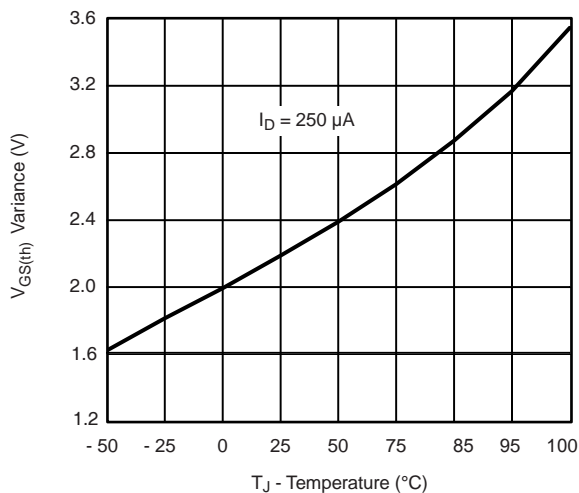
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



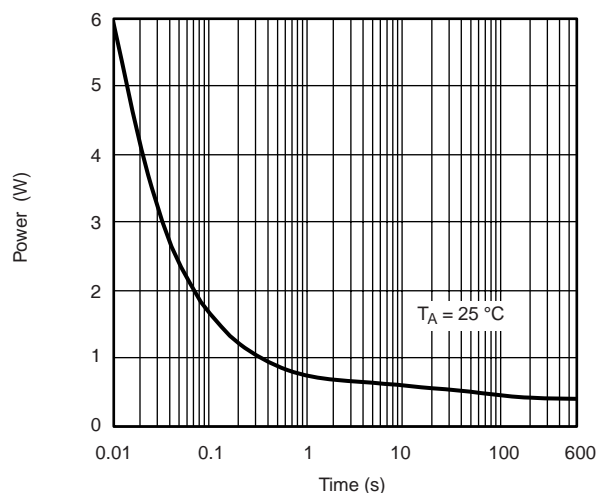
Source-Drain Diode Forward Voltage



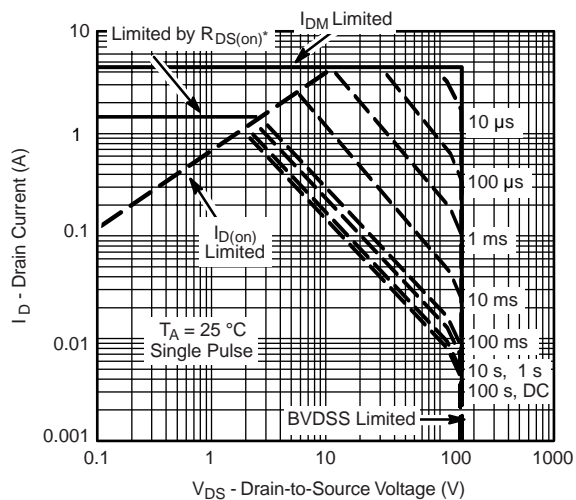
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



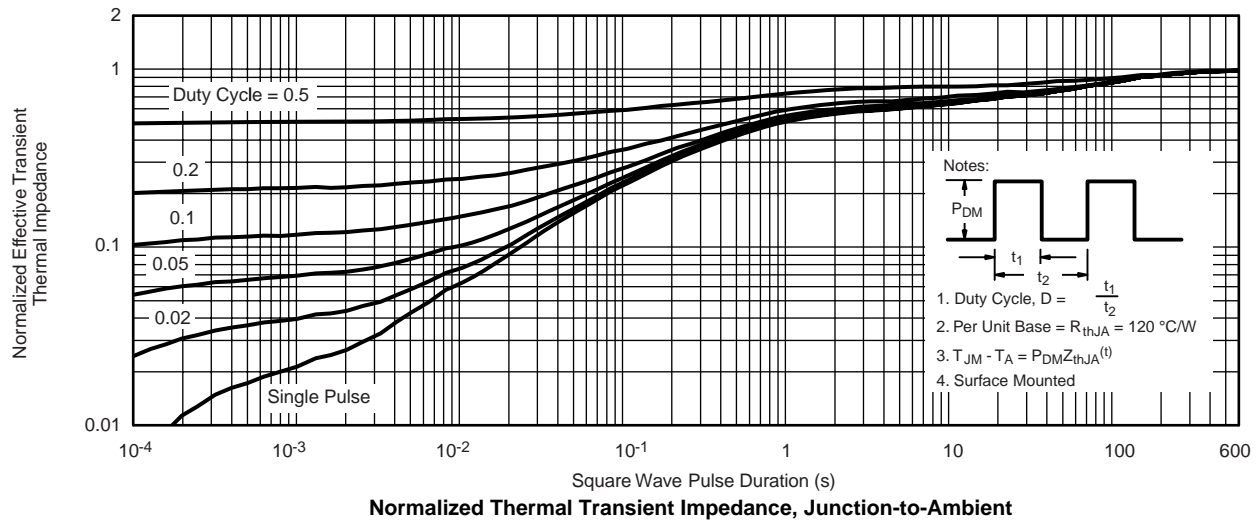
Single Pulse Power



* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area

THERMAL RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



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