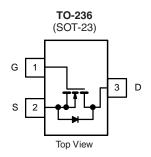




# N-Channel 150 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)		
150	0.580 at V <sub>GS</sub> = 10 V	0.9	6.5		
	0.815 at V <sub>GS</sub> = 4.5 V	0.7	0.5		



#### **FEATURES**

- DT-Trench Power MOSFET
- Small Size
- 100% R<sub>g</sub> and UIS Tested



#### **APPLICATIONS**

• Active Clamp Circuits in DC/DC Power Supplies

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	150	V
Gate-Source Voltage		$V_{GS}$	± 20	v
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 25 °C	I-	0.9	
Continuous Brain Current (1) = 100 O)	T <sub>C</sub> = 70 °C	l <sub>D</sub>	0.8	Α
Pulsed Drain Current (t = 300 μs)		I <sub>DM</sub>	3.5	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	L	0.9	
Continuous Source-Diam Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	0.7 <sup>b, c</sup>	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	0.8	
Single Pulse Avalanche Energy	L = 0.111111	E <sub>AS</sub>	1.05	mJ
Maximum Dawar Dissination	T <sub>C</sub> = 25 °C	D.	0.7	W
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	0.4	VV
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manifestore Investigate Application	t ≤ 5 s	$R_{thJA}$	80	100	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		125	166	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	42	50	

#### Notae

- a. Surface Mounted on 1" x 1" FR4 board.
- b. Pulse width limited by maximum junction temperature.





Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	•					
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	150			V
Gate-Source Thresho d Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2		4	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
	I <sub>DSS</sub>	V <sub>DS</sub> = 120 V, V <sub>GS</sub> = 0 V		- 1		^
Zero Gate Voltage Drain Current		V <sub>DS</sub> = 120 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10	μΑ
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	0.9			Α
	R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_D = 0.5 \text{ A}$		0.580	0.690	Ω
Drain-Source On-State Resistance <sup>a</sup>		$V_{GS} = 4.5 \text{ V}, I_D = 0.5 \text{ A}$		0.815	1.150	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 20 \text{ V}, I_{D} = 0.8 \text{ A}$		2.0		S
Diode Forward Voltage	V <sub>SD</sub>	$I_S = 0.9 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V
Dynamic <sup>b</sup>	1			1		
Input Capacitance	C <sub>iss</sub>			190		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 120 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		22		
Reverse Transfer Capacitance	C <sub>rss</sub>			11		
Tatal Oats Observe	Qg	$V_{DS} = 120 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 0.5 \text{ A}$		5.2	10.4	nC
Total Gate Charge				2.9	5.8	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 120 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 0.5 \text{ A}$		1.75		
Gate-Drain Charge	$Q_{gd}$			1.4		
Gate Resistance	Rg	f = 1 MHz		6.0		Ω
Switching <sup>c</sup>						
Turn-On Time	t <sub>d(on)</sub>	$V_{DD} = 120 \text{ V, R}_{L} = 39 \Omega$ $I_{D} \cong 0.5 \text{ A, V}_{GEN} = 10 \text{ V}$		30	45	ns
Turn-Ori Tillie	t <sub>r</sub>			26	39	
Turn-Off Time	t <sub>d(off)</sub>	$R_{q} = 6 \Omega$		17	26	
Turn-On Time	t <sub>f</sub>	y — <b>3</b>		12	20	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 0.5 A, dI/dt = 100 A/μs		93	143	nC

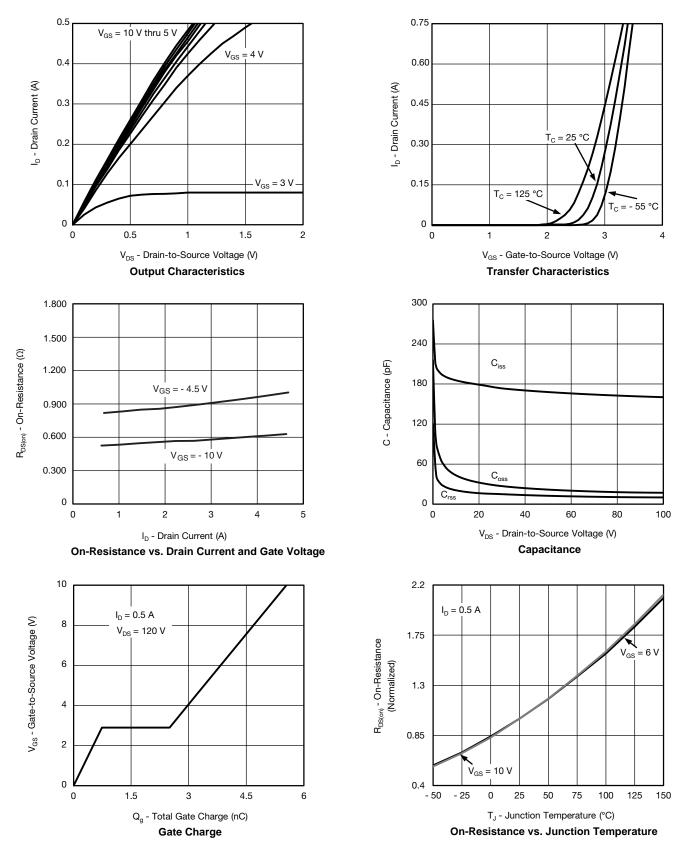
#### Notes:

- a. Pulse test: PW $\leq$  300  $\mu$ s duty cycle  $\leq$  2 %. b. For DESIGN AID ONLY, not subject to production testing. c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operationalsections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

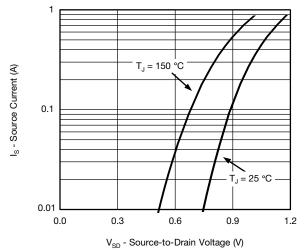


# TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

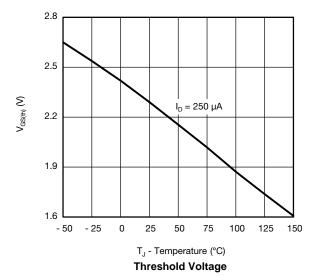


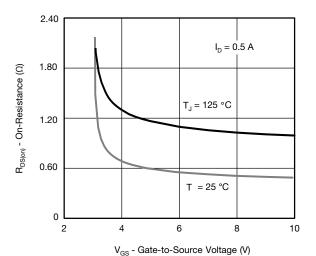


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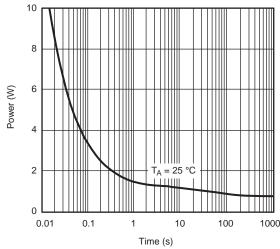


Source-Drain Diode Forward Voltage

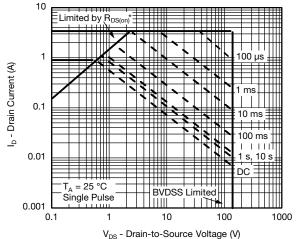




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power

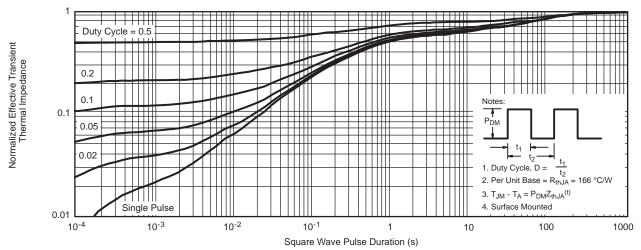


 $v_{DS}$  - Drain-to-Source voltage (v) \*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified



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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient





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