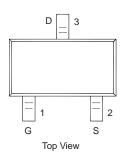


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N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)			
30	0.020 at V _{GS} = 10 V	6.2	4.5 nC			
	0.027 at V _{GS} = 4.5 V	4.8	4.5110			



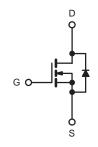


FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested

APPLICATIONS

- Load Switch
- Notebook Adaptor Switch
- DC/DC Converter



N-Channel MOSFET

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	± 20	V	
	T _C = 25 °C		6.2 ^a		
Continuous Drain Current (T _J = 150 °C)	T _C = 70 °C		4.4		
Continuous Drain Ourient (1) = 100 (0)	T _A = 25 °C	טי ד	6.0		
	T _A = 70 °C	1	4.7	A	
Pulsed Drain Current		I _{DM}	24		
Continuous Source-Drain Diode Current	T _C = 25 °C		6.2		
	T _A = 25 °C	Is	0.9 ^{b, c}		
	T _C = 25 °C		2.6		
Maximum Power Dissipation	T _C = 70 °C	P _D	2.0	w	
	T _A = 25 °C		1.9 ^{b, c}	vv	
	T _A = 70 °C	1	1.3 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	**	
Soldering Recommendations (Peak Temperature) ^{d, e}			260	°C	

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	75	105	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	thJF 40 55		0/11		

Notes:

a. Package limited

b. Surface Mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 130 $^{\circ}\text{C/W}.$



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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_{D} = 250 \mu A$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	1 050 14		31		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1.2		2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA	
		V _{DS} = 24 V, V _{GS} = 0 V, T _J = 55 °C			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	6			А	
	R _{DS(on)}	V _{GS} = 10 V, I _D = 3 A		0.020	0.025	Ω	
Drain-Source On-State Resistance ^a		$V_{GS} = 4.5 \text{ V}, I_{D} = 2 \text{ A}$		0.027	0.035		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 3 A		15		S	
Dynamic ^b							
Input Capacitance	C _{iss}			540			
Output Capacitance	C _{oss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		105		pF	
Reverse Transfer Capacitance	C _{rss}			18			
-	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 3 A		4.5	6.0	nC	
Total Gate Charge		$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 2 \text{ A}$		2.0	3.0		
Gate-Source Charge	Q _{gs}			0.75			
Gate-Drain Charge	Q _{gd}			0.35			
Gate Resistance	Rg	f = 1 MHz	0.8	4.5	8.8	Ω	
Turn-On Delay Time	t _{d(on)}			15		-	
Rise Time	t _r	V_{DD} = 15 V, R_{L} = 5.6 Ω		55		- ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 2.7 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		12			
Fall Time	t _f			22			
Turn-On Delay Time	t _{d(on)}			5			
Rise Time	t _r	V_{DD} = 15 V, R _L = 5.6 Ω		12			
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 2.7 \text{ A}, V_{GEN} = 10 \text{ V}, \text{ R}_g = 1 \Omega$		10			
Fall Time	t _f			5			
Drain-Source Body Diode Characteristic	cs				1	-	
Continuous Source-Drain Diode Current	ا _S	T _C = 25 °C			6.2	•	
Pulse Diode Forward Current	I _{SM}				18	A	
Body Diode Voltage	V _{SD}	$I_{S} = 2.7 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			12	20	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	L = 2.7 A d/dt = 100 A/m T 05 C		5	10	nC	
Reverse Recovery Fall Time	ta	$I_F = 2.7 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 ^\circ\text{C}$		6		1	
Reverse Recovery Rise Time	t _b	-		4		ns	

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %

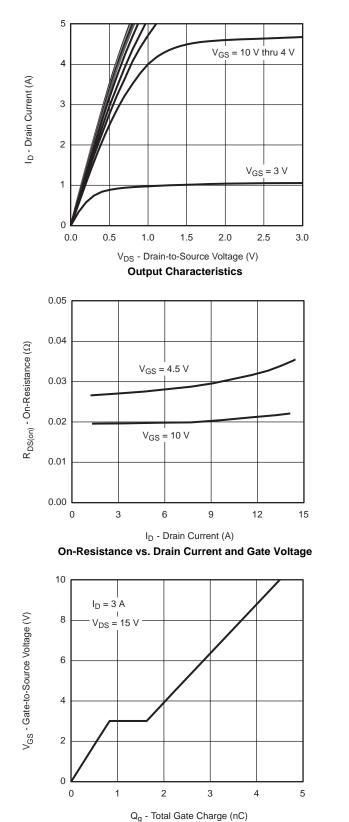
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

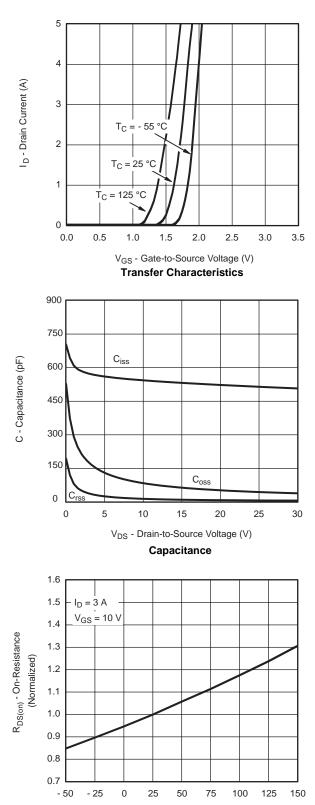


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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Gate Charge

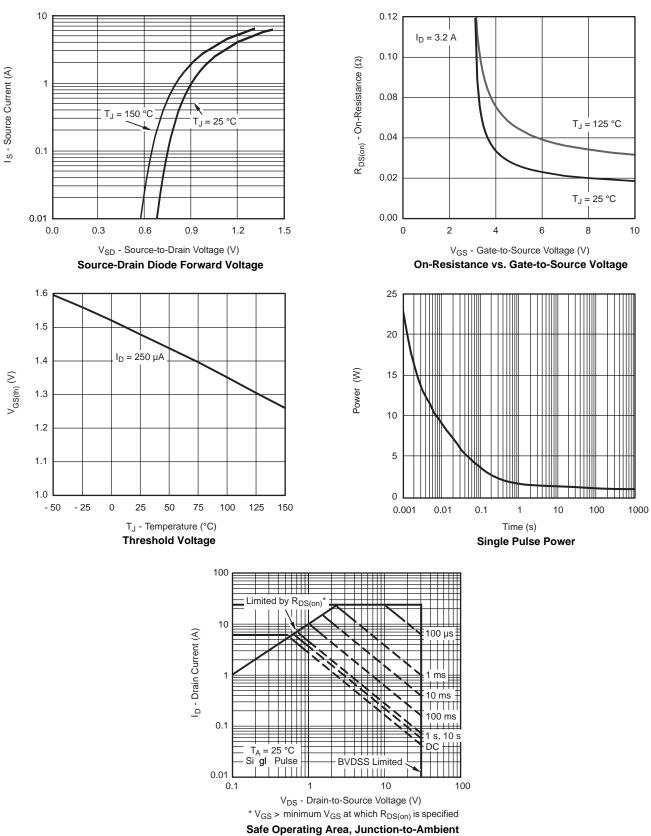


T_J - Junction Temperature (°C) On-Resistance vs. Junction Temperature

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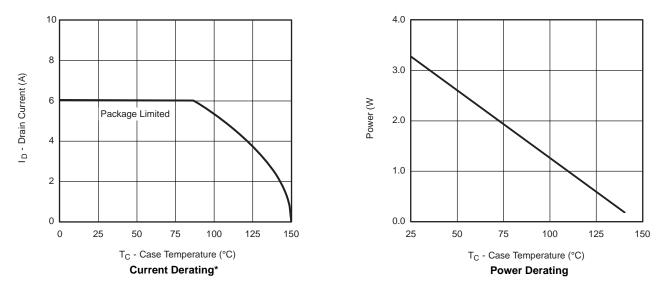
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

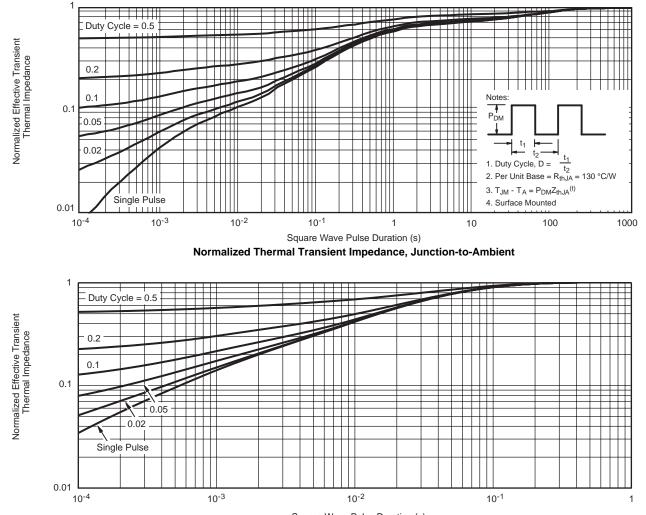


* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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Square Wave Pulse Duration (s) Normalized Thermal Transient Impedance, Junction-to-Foot



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