

N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^a	Q _g (Typ.)			
30	0.018 at V _{GS} = 10 V	6.5	6.5 nC			
	0.026 at V _{GS} = 4.5 V	5.2				

FEATURES

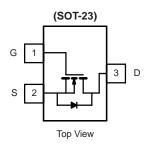
- DT-Trench Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC

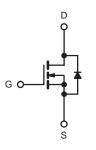


RoHS

APPLICATIONS

DC/DC Converter





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V_{DS}	30	V	
Gate-Source Voltage		V_{GS}	± 20	1	
	T _C = 25 °C	- I _D -	6.5 ^a		
Continuous Drain Current (T _{.1} = 150 °C)	$T_C = 70 ^{\circ}C$		5.3		
Continuous Brain Carrent (1) = 100 °C)	$T_A = 25 ^{\circ}C$		3.8		
	T _A = 70 °C		2.8	Α	
Pulsed Drain Current		I _{DM}	24		
	T _C = 25 °C		6.5		
Continuous Source-Drain Diode Current	T _A = 25 °C	IS	1.0 ^{b, c}		
Maximum Power Dissipation	T _A = 25 °C	P _D 1.4 ^{b, c} 0.9 ^{b, c}	1.4 ^{b, c}	w	
	$T_A = 70 ^{\circ}C$		0.9 ^{b, c}	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	
Soldering Recommendations (Peak Temperature) ^{d, e}			260		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R_{thJA}	90	119	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	60	78		

Notes:

- a. Package limited
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 130 °C/W.



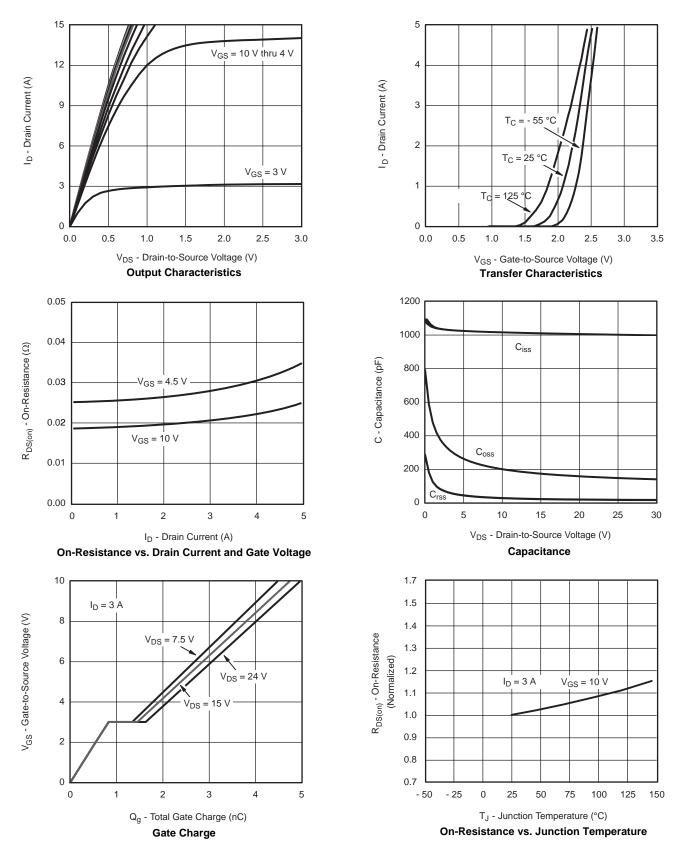
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		31		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	1D = 200 μΑ		- 5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1		3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	1		1		
		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	6.5			Α	
Drain-Source On-State Resistance ^a	D	V _{GS} = 10 V, I _D = 3 A		0.018	0.025		
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 2 \text{ A}$		0.026	0.035	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 3 A		12		S	
Dynamic ^b							
Input Capacitance	C _{iss}			1056		pF	
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		98			
Reverse Transfer Capacitance	C _{rss}			22			
Total Gate Charge	Qg			4.5	6.7	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 3 \text{ A}$		2			
Gate-Drain Charge	Q _{gd}	25 05 2		1.5			
Gate Resistance	Rg	f = 1 MHz		5.5		Ω	
Turn-On Delay Time	t _{d(on)}			6			
Rise Time	t _r	V_{DD} = 15 V, R_L = 5.6 Ω		18		ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 3 \text{ A, V}_{GEN} = 10 \text{ V, R}_g = 1 \Omega$		12			
Fall Time	t _f			6			
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			6.5	۸	
Pulse Diode Forward Current	I _{SM}				24	A	
Body Diode Voltage	V _{SD}	I _S = 3 A, V _{GS} = 0 V		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			10		ns	
Body Diode Reverse Recovery Charge		I _F = 3 A, dI/dt = 100 A/μs, T _{.I} = 25 °C		7		nC	
Reverse Recovery Fall Time	ta	i _F = 3 A, αί/αι = 100 Α/μ5, 1 _J = 25 °C		9		ns	
Reverse Recovery Rise Time	t _b			5			

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 % b. Guaranteed by design, not subject to production testing.

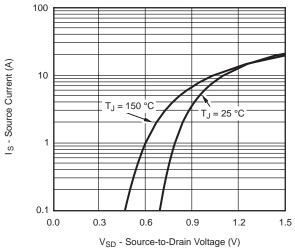


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

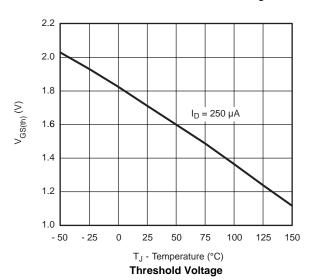


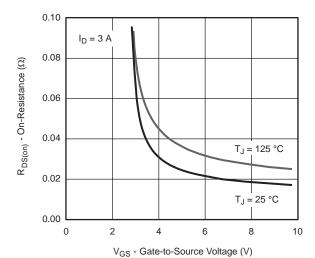


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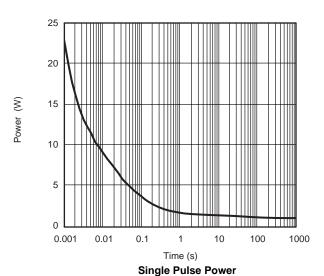


Source-Drain Diode Forward Voltage





On-Resistance vs. Gate-to-Source Voltage

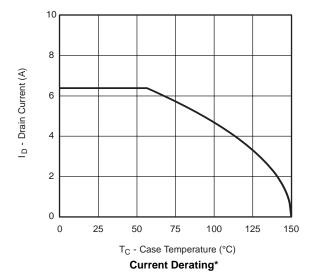


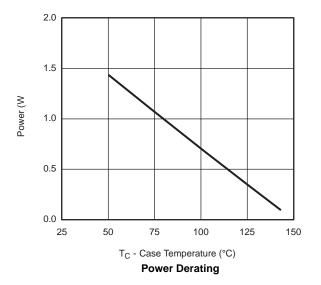
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

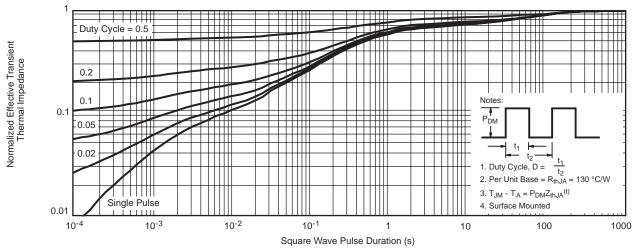




^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot





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