

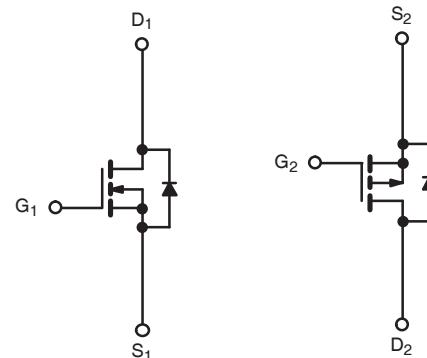
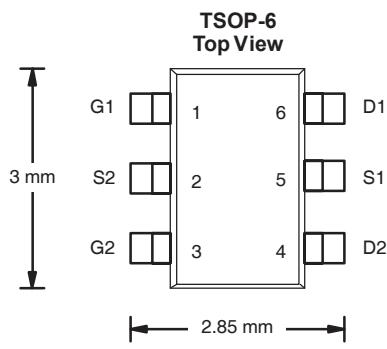
N- and P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY

	V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)
N-Channel	30	0.020 at V _{GS} = 10 V	3.7
		0.022 at V _{GS} = 4.5 V	3.0
P-Channel	- 30	0.060 at V _{GS} = - 10 V	- 3.0
		0.079 at V _{GS} = - 4.5 V	- 2.2

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC



ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V _{DS}	30	- 30	V
Gate-Source Voltage	V _{GS}	± 20	± 20	
Continuous Drain Current (T _J = 150 °C) ^{a, b}	I _D	3.7	- 3.0	A
		3.0	- 2.2	
Pulsed Drain Current	I _{DM}	8	- 7	A
Continuous Source Current (Diode Conduction) ^{a, b}	I _S	1.05	- 1.05	
Maximum Power Dissipation ^{a, b}	P _D	1.15		W
		0.73		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	R _{thJA}	93	110	°C/W
		130	150	
Maximum Junction-to-Lead	R _{thJL}	75	90	

Notes:

- a. Surface Mounted on FR4 board.
b. t ≤ 5 s.

SPECIFICATIONS $T_J = 25^\circ\text{C}$, unless otherwise noted									
Parameter	Symbol	Test Conditions			Min.	Typ.	Max.	Unit	
Static									
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	N-Ch	0.6		1.5		V	
		$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	P-Ch	-0.6		-1.5			
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	N-Ch P-Ch			± 100	± 100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$	N-Ch			1		μA	
		$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}$	P-Ch			-1			
		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^\circ\text{C}$	N-Ch			5			
		$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^\circ\text{C}$	P-Ch			-5			
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	N-Ch	3.7				A	
		$V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$	P-Ch	-3					
Drain-Source On-State Resistance ^a	$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}, I_D = 2.5 \text{ A}$	N-Ch		0.020	0.024		Ω	
		$V_{GS} = -10 \text{ V}, I_D = -1.8 \text{ A}$	P-Ch		0.060	0.069			
		$V_{GS} = 4.5 \text{ V}, I_D = 2.0 \text{ A}$	N-Ch		0.022	0.028			
		$V_{GS} = -4.5 \text{ V}, I_D = -1.2 \text{ A}$	P-Ch		0.079	0.083			
Forward Transconductance ^a	g_{fs}	$V_{DS} = 10 \text{ V}, I_D = 2.5 \text{ A}$	N-Ch		4.3			S	
		$V_{DS} = -15 \text{ V}, I_D = -1.8 \text{ A}$	P-Ch		2.4				
Diode Forward Voltage ^a	V_{SD}	$I_S = 1.05 \text{ A}, V_{GS} = 0 \text{ V}$	N-Ch		0.81	1.10		V	
		$I_S = -1.05 \text{ A}, V_{GS} = 0 \text{ V}$	P-Ch		-0.83	-1.10			
Dynamic^b									
Total Gate Charge	Q_g	N-Channel $V_{DS} = 15 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 1.8 \text{ A}$ P-Channel $V_{DS} = -15 \text{ V}, V_{GS} = -5 \text{ V}, I_D = -1.8 \text{ A}$	N-Ch		2.1	3.2		nC	
Gate-Source Charge	Q_{gs}		P-Ch		2.4	3.6			
Gate-Drain Charge	Q_{gd}		N-Ch		0.7				
Gate Resistance	R_g		P-Ch		0.9				
Turn-On Delay Time	$t_{d(\text{on})}$	N-Channel $V_{DD} = 15 \text{ V}, R_L = 15 \Omega$ $I_D \geq 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$ P-Channel $V_{DD} = -15 \text{ V}, R_L = 15 \Omega$ $I_D \geq -1 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 6 \Omega$	N-Ch	0.5		2.4		ns	
Rise Time	t_r		P-Ch	3		11			
Turn-Off Delay Time	$t_{d(\text{off})}$		N-Ch		7	11			
Fall Time	t_f		P-Ch		8	12			
Source-Drain Reverse Recovery Time	t_{rr}		N-Ch		9	14			
			P-Ch		12	18			
		$I_F = 1.05 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}$	N-Ch		13	20			
		$I_F = -1.05 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}$	P-Ch		12	18			

Notes:

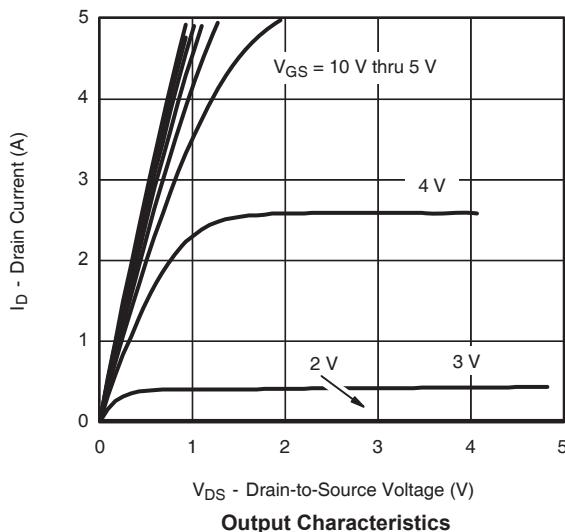
a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.

b. Guaranteed by design, not subject to production testing.

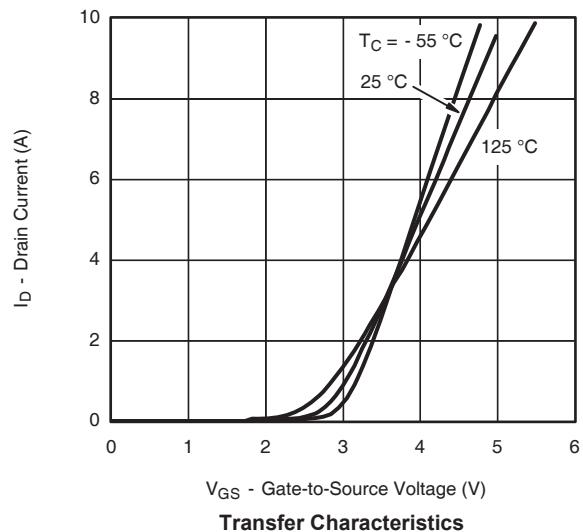
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

N-CHANNEL TYPICAL CHARACTERISTICS

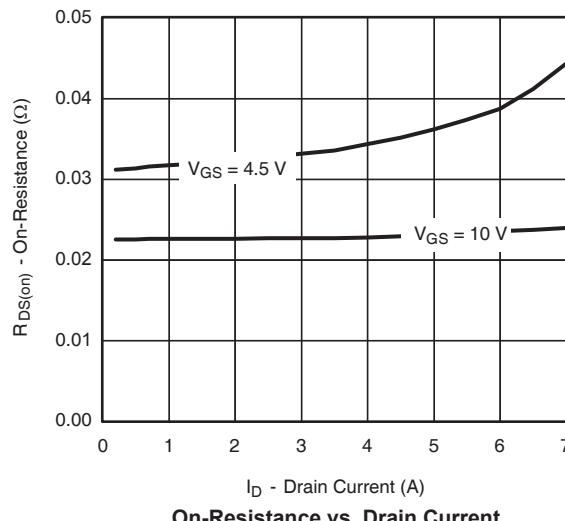
25 °C, unless otherwise noted



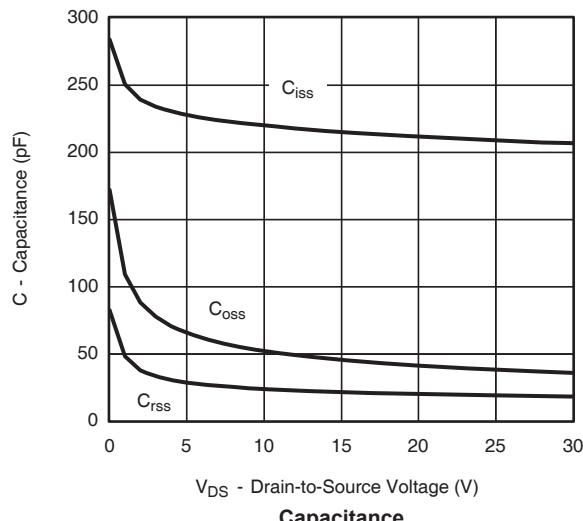
Output Characteristics



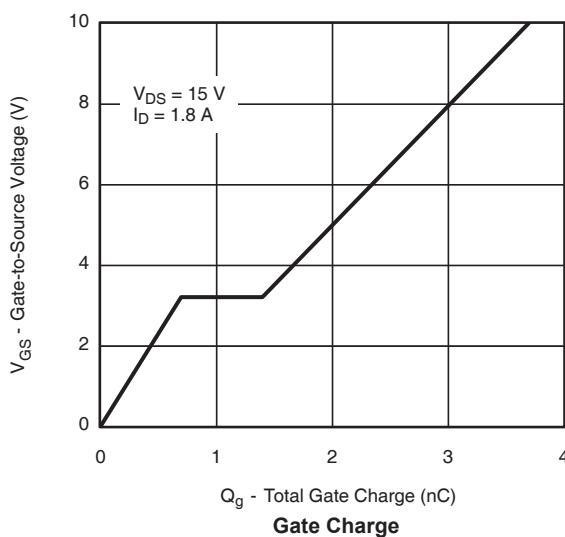
Transfer Characteristics



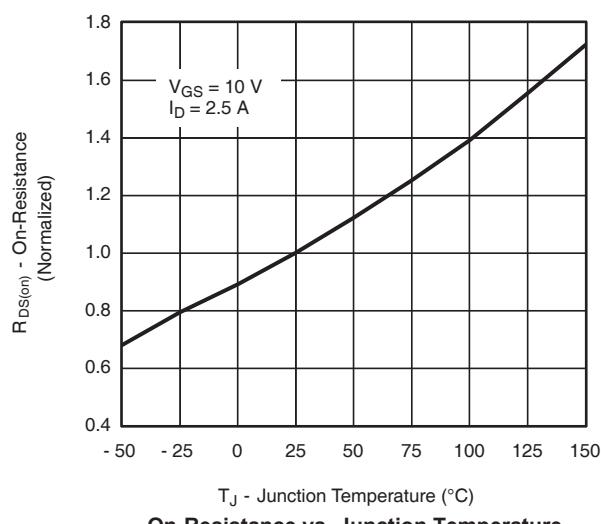
On-Resistance vs. Drain Current



Capacitance

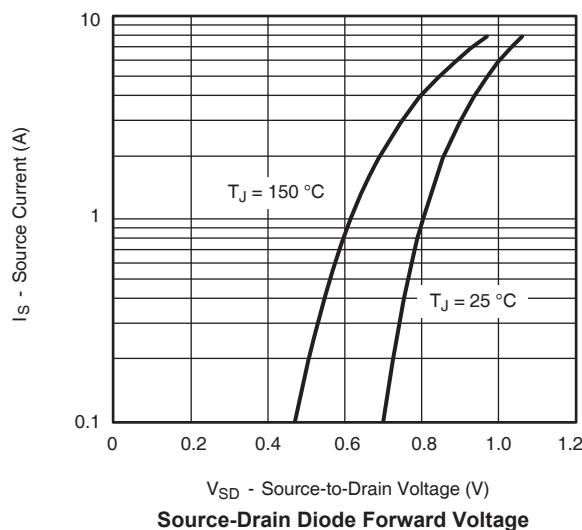


Gate Charge

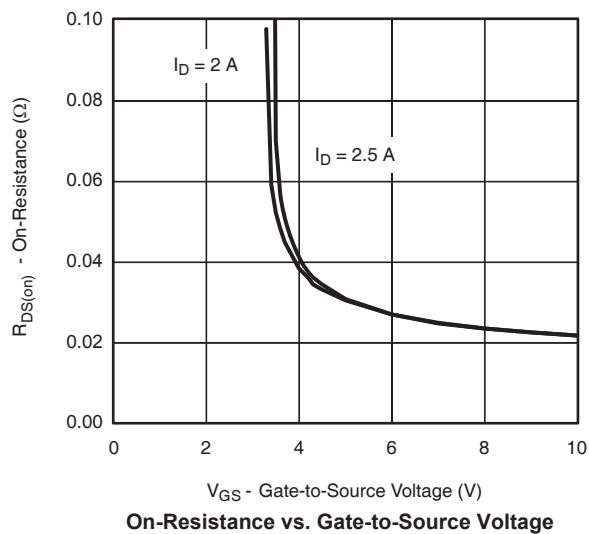


On-Resistance vs. Junction Temperature

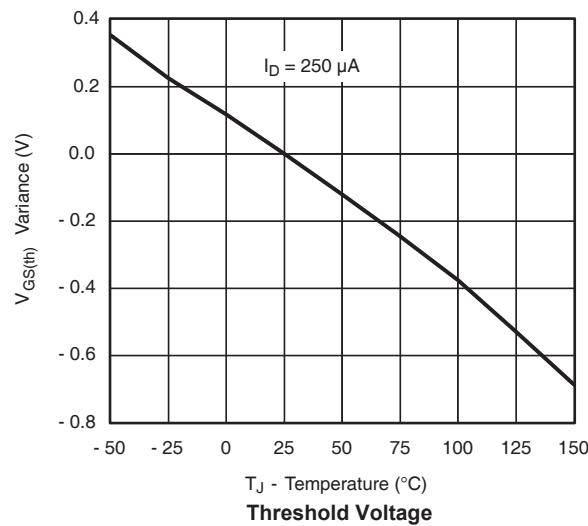
N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



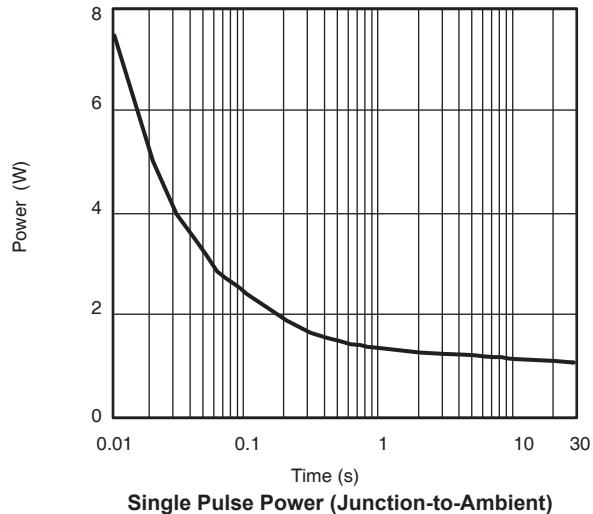
Source-Drain Diode Forward Voltage



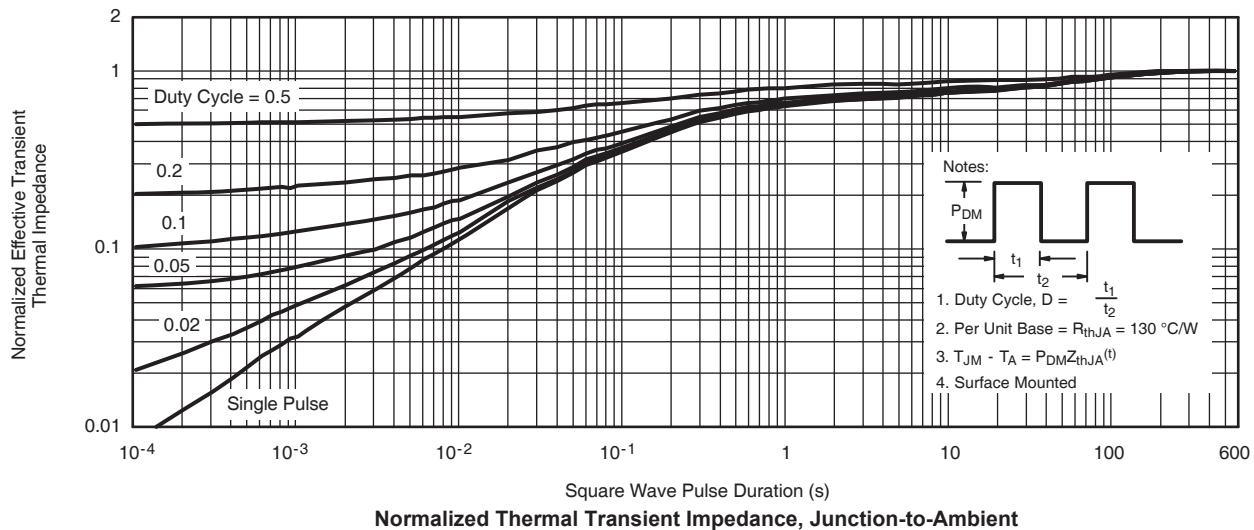
On-Resistance vs. Gate-to-Source Voltage



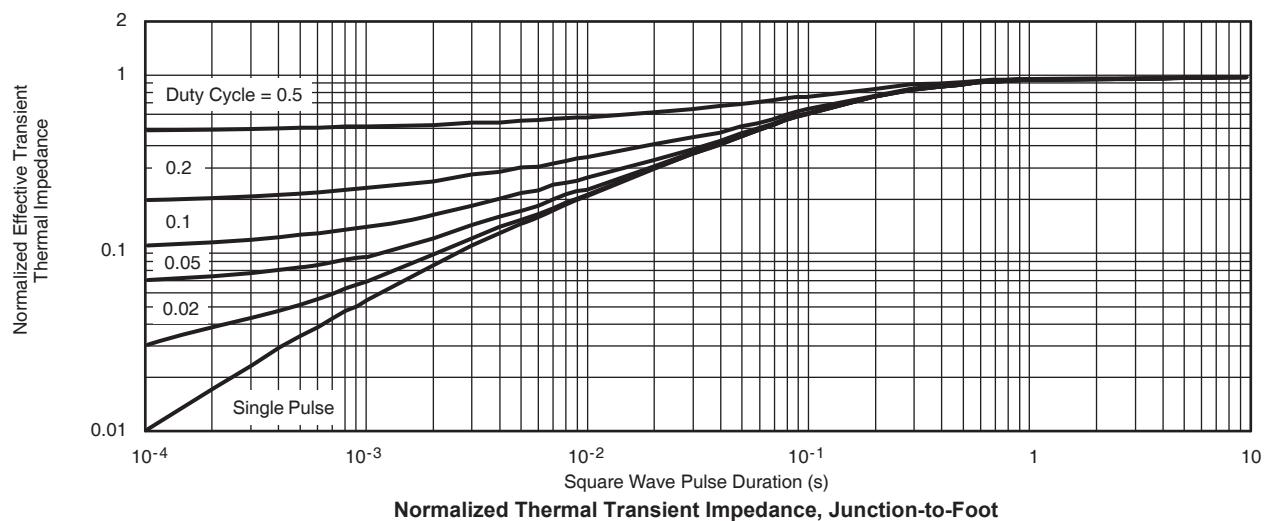
Threshold Voltage



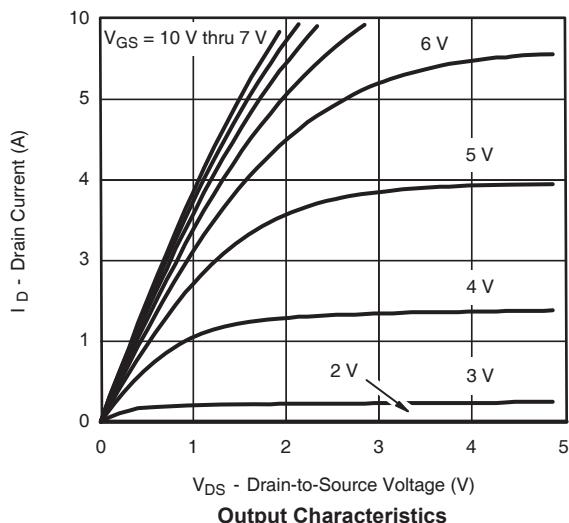
Single Pulse Power (Junction-to-Ambient)



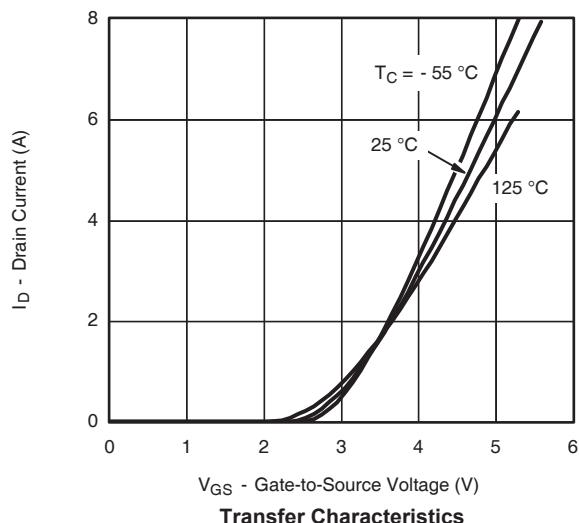
N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



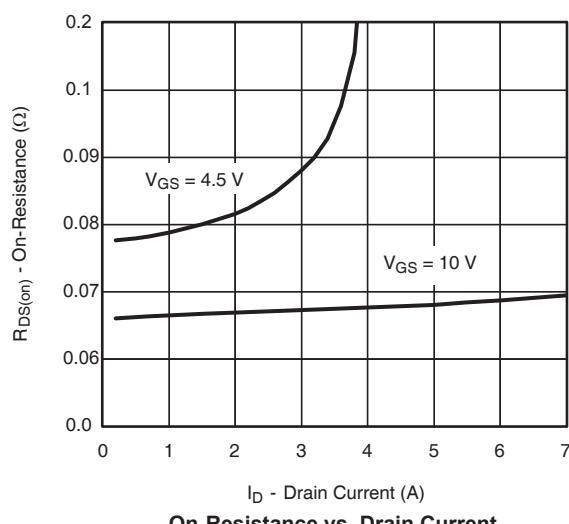
P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



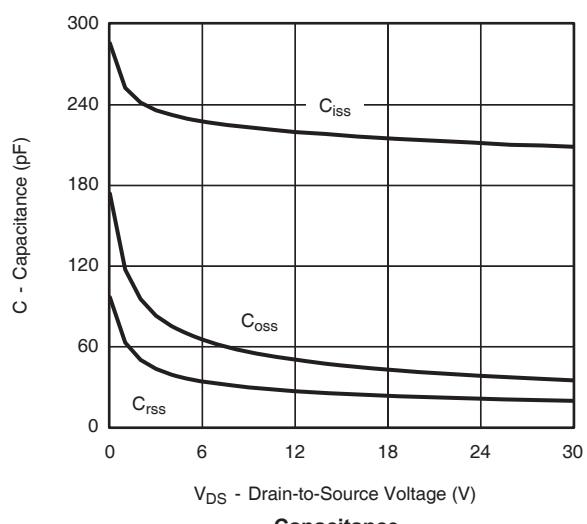
Output Characteristics



Transfer Characteristics

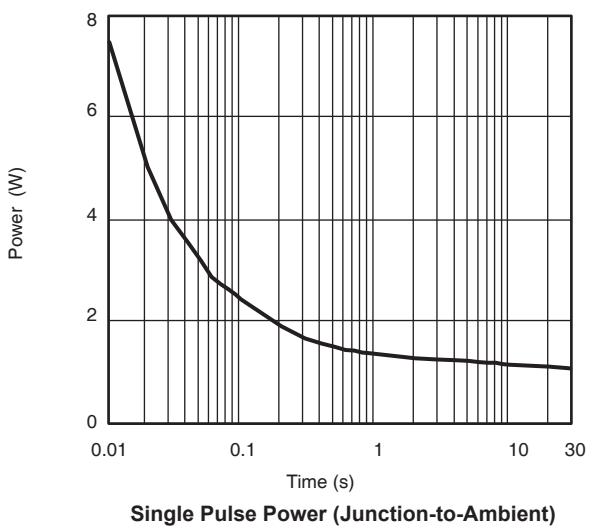
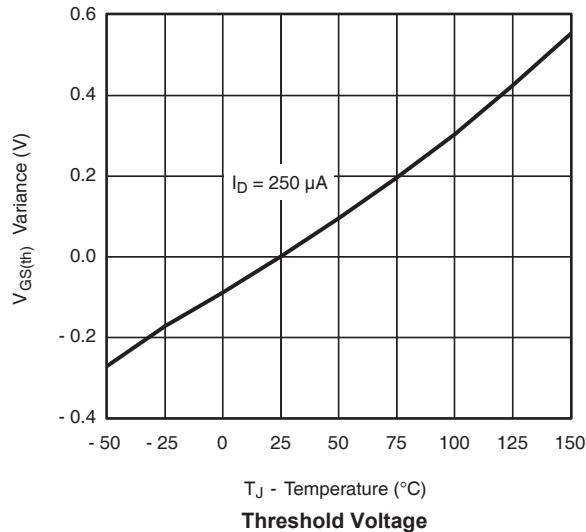
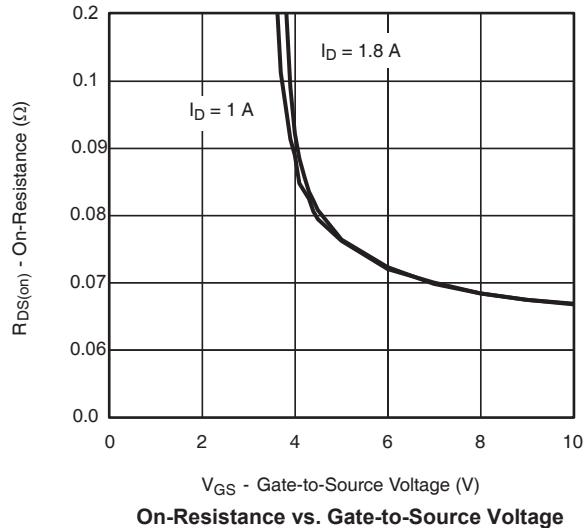
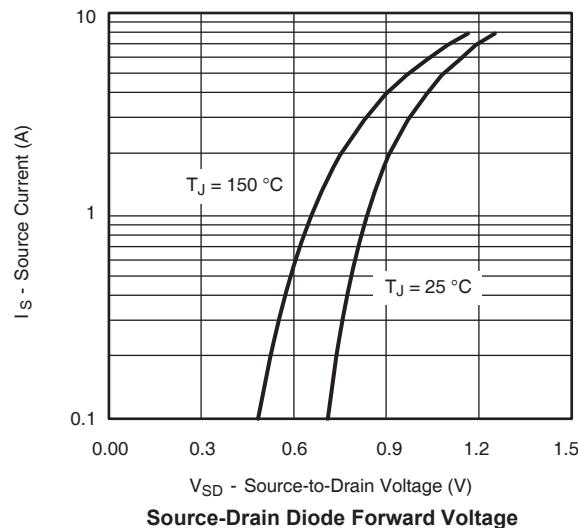
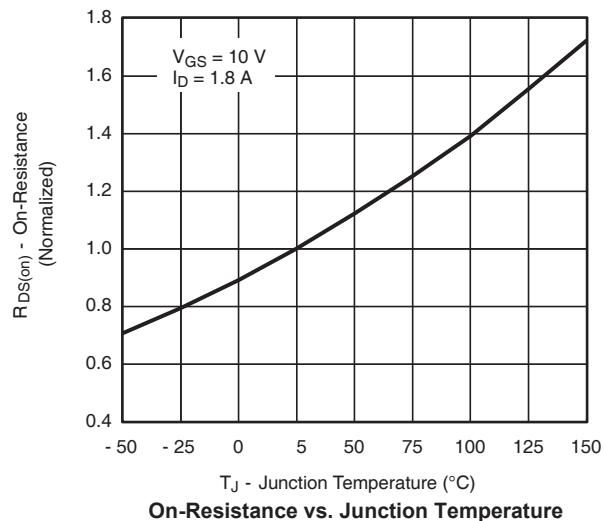
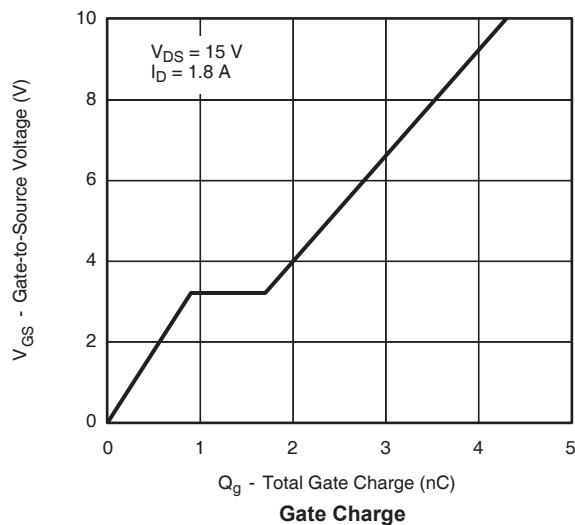


On-Resistance vs. Drain Current

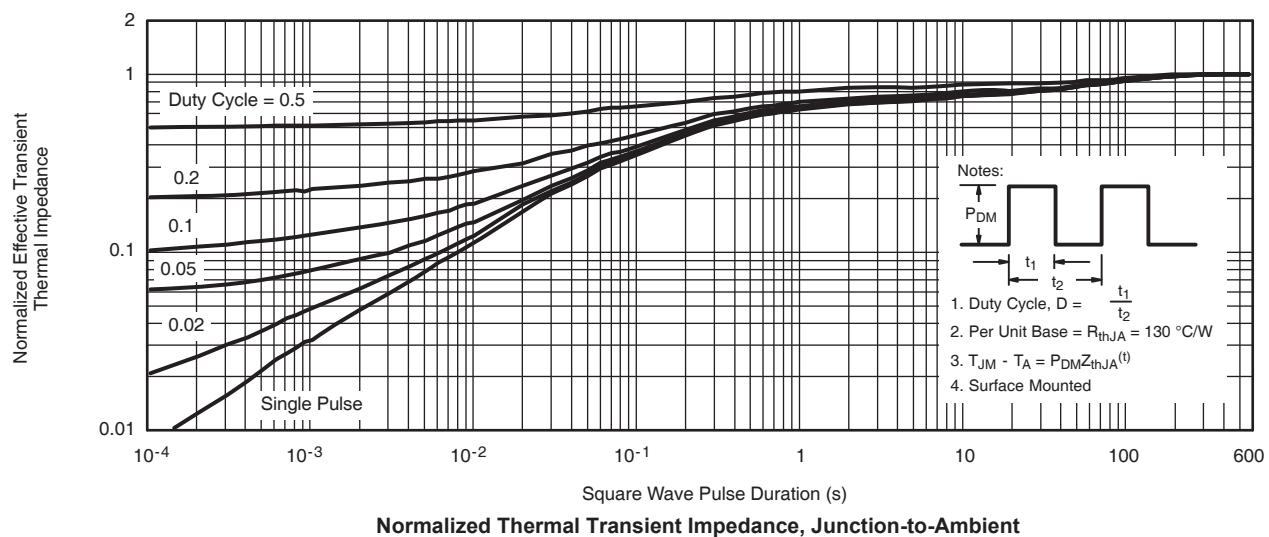


Capacitance

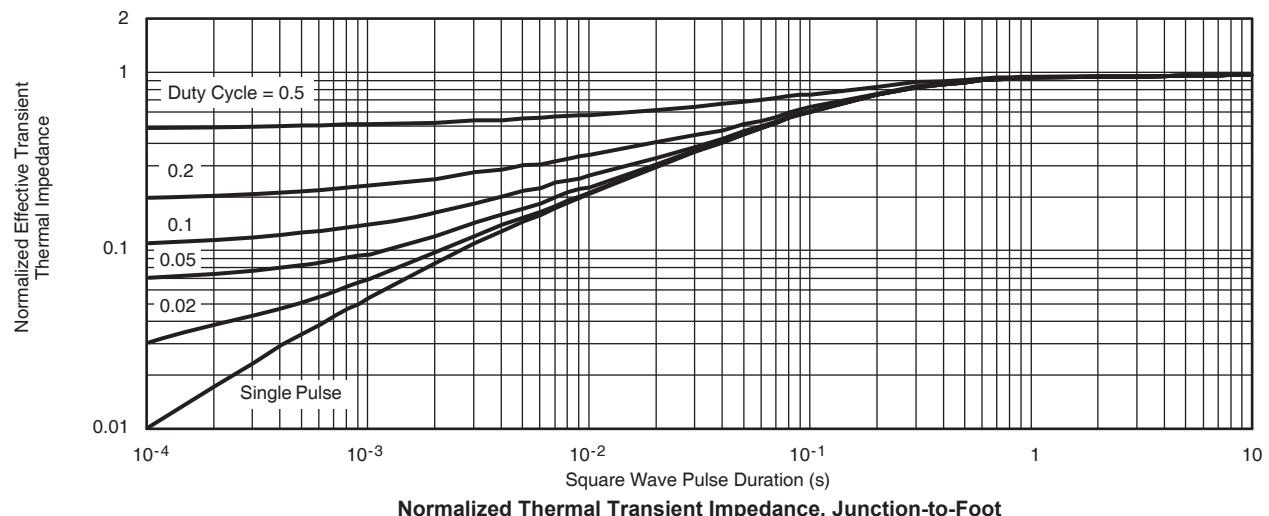
P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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