

P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}$ (m Ω)(Typ.)	I _D (A) ^a	Q _g (Typ.)		
- 20	33 at V _{GS} = - 4.5 V	- 6	8.5 nC		
	45 at V _{GS} = - 2.5 V	-0			

FEATURES

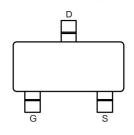
- DT-Trench Power MOSFET
- 100 % $\rm R_{\rm g}$ and UIS Tested



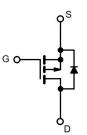
APPLICATIONS

· Load Switch for Portable Devices

SOT-23 Pin Configuration



Top View



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage		V _{DS}	- 20	V		
Gate-Source Voltage		V _{GS}	± 12	_ v		
Continuous Drain Current (T ₁ = 150 °C)	T _C = 25 °C		- 6			
Continuous Brain Carrent (1) = 100 C)	T _C = 70 °C		- 4.6	Α		
Pulsed Drain Current		I _{DM}	- 24			
Single-Pulse Avalanche Energy L = 0.1 mH		E _{AS}	6	mJ		
	T _C = 25 °C		2.4			
Maximum Power Dissipation	T _C = 70 °C	P _D	1.53	W		
Maximum Fower Dissipation	T _A = 25 °C] 'D	1.3 ^{b,c}			
	T _A = 70 °C	1	0.83 ^{b,c}			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	85	96	°C/W	
Maximum Junction-to-Case	Steady State	R _{thJC}	45	52]	

Notes:

- a. $T_C = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s
- d. Maximum under steady state conditions is 100C/W.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	V _{GS} = 0, I _D = - 250 μA	- 20			V	
Gate-Source Threshold Voltage	V _G S(th)	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.4		- 1.2	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA	
Zoro Coto Voltogo Proin Current	1	V _{DS} = - 20 V, V _{GS} = 0 V			- 1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 16 V, V _{GS} = 0 V, T _J = 55 °C			- 50		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -4.5 \text{ V}, V_{GS} = -5 \text{ V}$	- 6			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5V, I _D = - 3 A		33	40	mΩ	
		V _{GS} = - 2.5 V, I _D = - 3 A		40	50		
Forward Transconductance ^a	g _{fs}	V _{DS} = - 5 V, I _D = - 3 A		15		S	
Dynamic ^b							
Input Capacitance	C _{iss}			577		pF	
Output Capacitance	C _{oss}	V _{DS} = - 10 V, V _{GS} = 0 V, f = 1 MHz		70			
Reverse Transfer Capacitance	C _{rss}	1		56			
Total Gate Charge	Qq			8.5		nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -3 \text{ A}$		1.3			
Gate-Drain Charge	Q _{gd}	1		2			
Gate Resistance	R_{g}	f = 1 MHz		40		Ω	
Turn-On Delay Time	t _{d(on)}			15			
Rise Time	t _r	V _{DD} = - 10 V, R _L = 1.5 Ω		16		1	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong$ - 3 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		25		- ns -	
Fall Time	t _f	1		11			
Drain-Source Body Diode Characterist	ics		<u> </u>	2.3			
Continous Source-Drain Diode Current	I _S	T _C = 25 °C			- 6	Α	
Pulse Diode Forward Current	I _{SM}				- 24	1 ^	
Body Diode Voltage	V_{SD}	I _S = - 1 A			- 1	V	
Body Diode Reverse Recovery Time				15		ns	
Body Diode Reverse Recovery Charge	harge Q _{rr} I ₃ A dl/dt = 100 A/us T ₂₅ °C			5		nC	
Reverse Recovery Fall Time	ta	I _F = - 3 A, dl/dt = 100 A/μs, T _J = 25 °C		9		ns	
Reverse Recovery Rise Time	t _b	7		15		1 115	

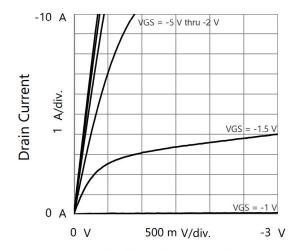
Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

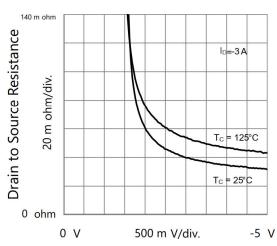
a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.



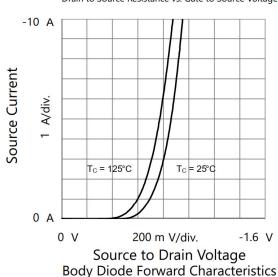
TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)

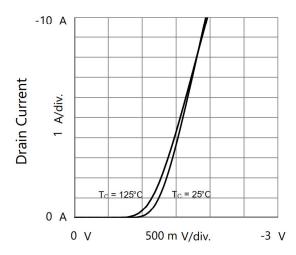


Drain to Source Voltage Output Characteristics

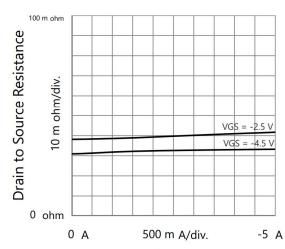


Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage

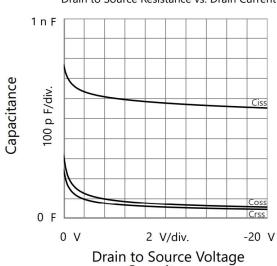




Gate to Source Voltage Transfer Characteristics

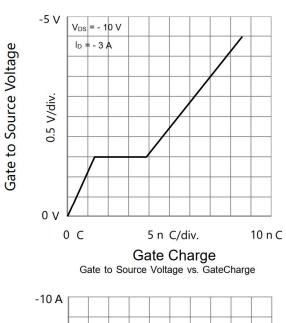


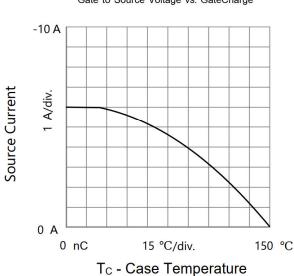
Drain Current
Drain to Source Resistance vs. Drain Current

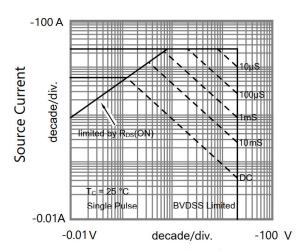




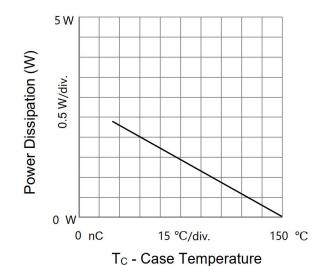
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

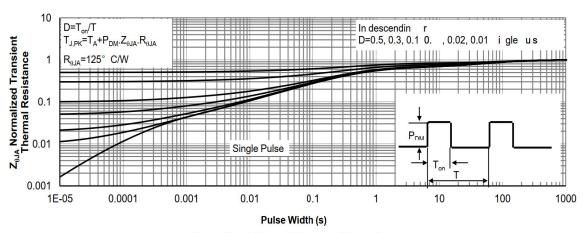






Source to Drain Voltage Safe Operating Area, Junction-to-Ambient





Normalized Thermal Transient Impedance



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