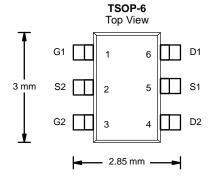


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> RoHS COMPLIANT

Dual P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	$R_{DS(on)}$ (Ω) I_{D} (A) ^a Q_{g} (
- 20	0.070 at V_{GS} = - 4.5V	- 4.0	2.7 nC			
- 20	0.096 at V _{GS} = - 2.5 V	- 3.2	2.7 110			

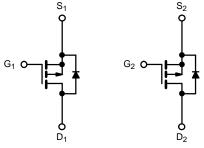


FEATURES

- DT-Trench Power MOSFET
- 100 % R_a Tested
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Load Switch for Portable Applications
- Battery Switch for Portable Devices
- Computers ٠
- Bus Switch
 - Load Switch



P-Channel MOSFET

P-Channel M	OSFET
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ABSOLUTE MAXIMUM RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	- 20	V	
Gate-Source Voltage		V _{GS}	± 12	v	
	T _C = 25 °C	- I _D	- 4.0		
Continuous Drain Current (T _J = 150 °C)	T _C = 70 °C		- 3.3		
Continuous Drain Current $(1) = 150$ C)	T _A = 25 °C		- 3.6 ^{b, c}		
	T _A = 70 °C		-3.1 ^{b, c}	А	
Pulsed Drain Current		I _{DM}	- 12		
	T _C = 25 °C		- 1.17		
Continuous Source-Drain Diode Current	T _A = 25 °C	۱ _S	- 0.95 ^{b, c}		
Maximum Power Dissipation	T _C = 25 °C	– P _D	1.4		
	T _C = 70 °C		0.9	w	
	T _A = 25 °C		1.14 ^{b, c}	vv	
	T _A = 70 °C		0.73 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	$t \le 5 s$	R _{thJA}	93	110	°C/W	
Maximum Junction-to-Foot	Steady State	R _{thJF}	75	90		

Notes:

c. t = 5 s.

d. Maximum under steady state conditions is 150 °C/W.

a. T_C = 25 °C.

b. Surface mounted on 1" x 1" FR4 board.

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static						•	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μA		- 17		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	i _D = - 250 μA		3.5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 0.6		- 1.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1		
		V _{DS} = - 20 V, V _{GS} = 0 V, T _J = 55 °C			10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le$ - 5 V, V_{GS} = - 4.5V	- 8			Α	
	D	V _{GS} = - 4.5V, I _D = - 2.5 A		0.070	0.077	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 1 A		0.096	0.105		
Forward Transconductance ^a	g _{fs}	V _{DS} = - 15 V, I _D = - 2.6 A		5		S	
Dynamic ^b							
Input Capacitance	C _{iss}			210		pF	
Output Capacitance	C _{oss}	V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz		45			
Reverse Transfer Capacitance	C _{rss}			33			
Total Gate Charge	Qg	V_{DS} = - 15 V, V_{GS} = - 4.5 V, I_{D} = - 2.6 A		5.2	8	- nC	
				2.7	4		
Gate-Source Charge	Q _{gs}	V_{DS} = - 15 V, V_{GS} = - 4.5 V, I_{D} = - 2.6 A		0.94			
Gate-Drain Charge	Q _{gd}			1.3			
Gate Resistance	Rg	f = 1 MHz	2	7	14	Ω	
Turn-On Delay Time	t _{d(on)}			39	59	- ns	
Rise Time	t _r	V_{DD} = - 15 V, R_L = 7.1 Ω		25	38		
Turn-Off Delay Time	t _{d(off)}	I_D \cong - 2.1 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		13	20		
Fall Time	t _f			9	18		
Turn-On Delay Time	t _{d(on)}			5	10		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 7.1 Ω		10	20		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 2.1 A, V_GEN = - 4.5 V, $\text{R}_\text{g}\text{=}$ 1 Ω		14	21		
Fall Time	t _f			7	14		
Drain-Source Body Diode Characteristic	cs			1	1		
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			1.17	^	
Pulse Diode Forward Current	I _{SM}				12	A	
Body Diode Voltage	V _{SD}	I _S = - 2.1 A, V _{GS} = 0 V		0.85	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			13	20	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	L = 2.1 A d/dt = 100 A/trace T = 05.90		6	12	nC	
Reverse Recovery Fall Time	ta	I _F = - 2.1 A, dl/dt = 100 A/μs, T _J = 25 °C		9			
Reverse Recovery Rise Time	t _b			4		ns	

Notes:

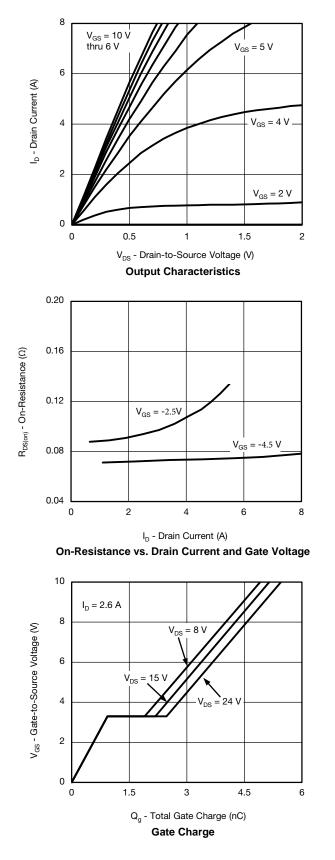
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %

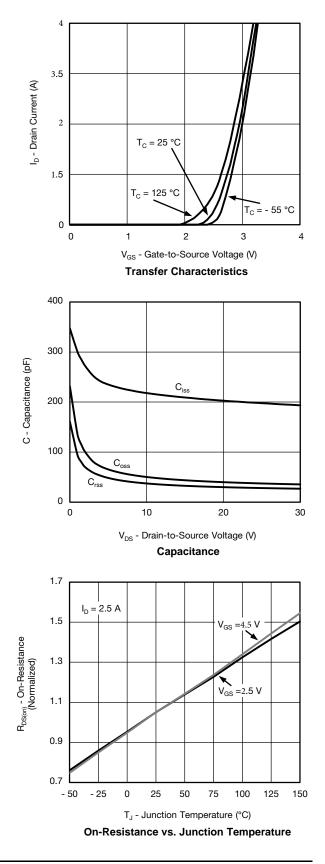
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

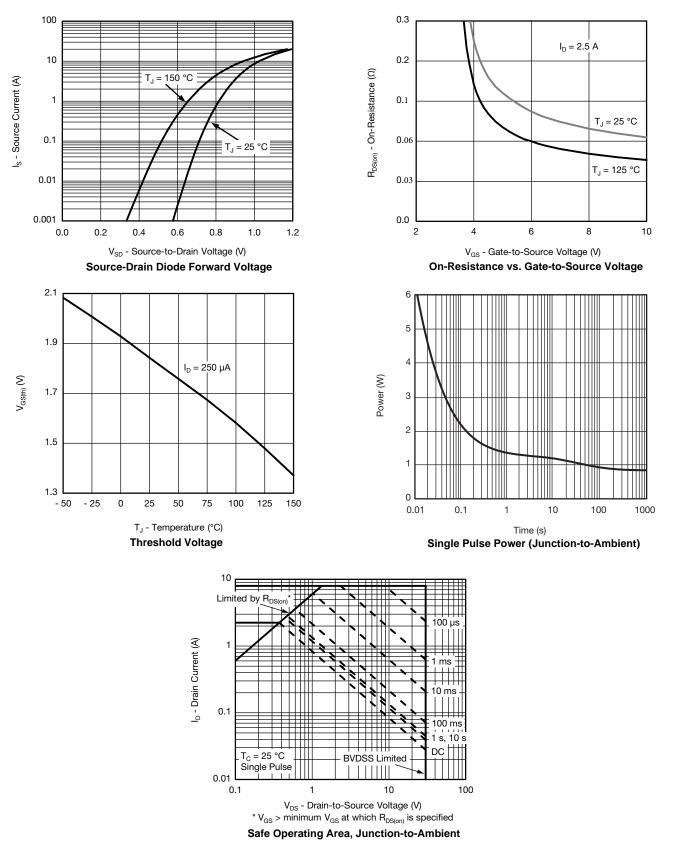






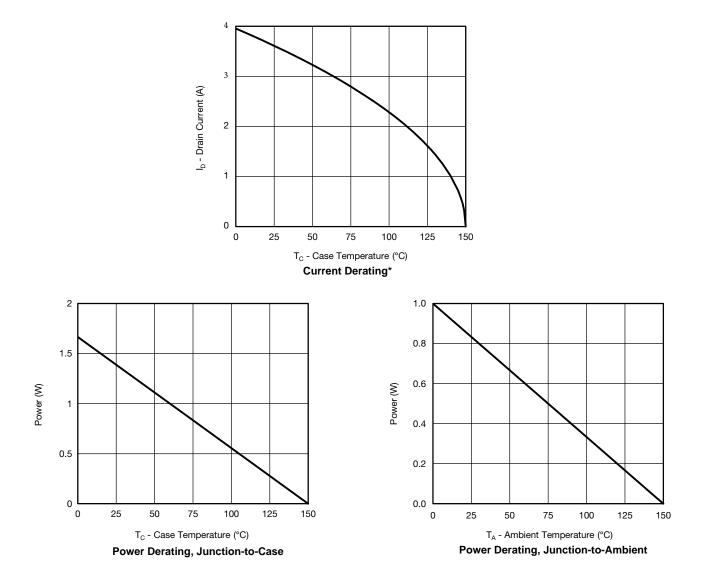
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





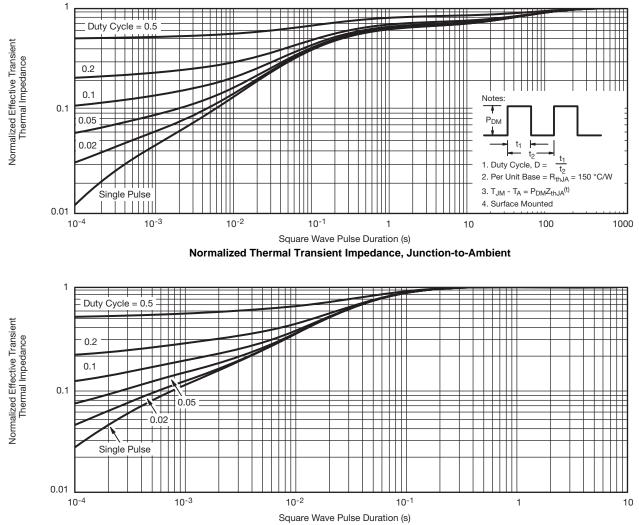
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot



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