

## Dual N-Channel 60 V (D-S) MOSFET

### PRODUCT SUMMARY

$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A) <sup>a</sup>	$Q_g$ (Typ.)
60	1.6 at $V_{GS} = 10$ V	0.45	1.3 nC
	1.9 at $V_{GS} = 4.5$ V	0.39	

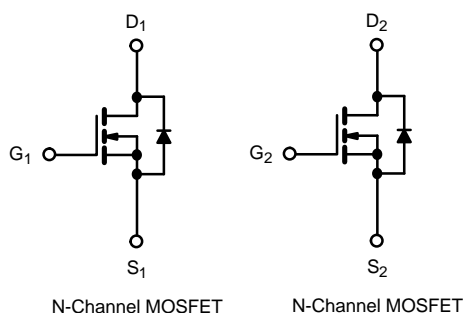
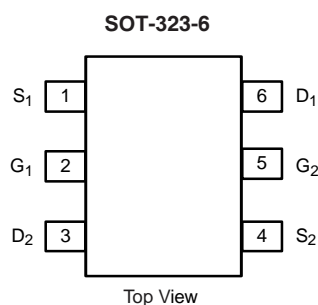
### FEATURES

- DT-Trench Power MOSFET
- Very Small Footprint
- 100 % Rg and UIS Tested


**RoHS**  
 COMPLIANT

### APPLICATIONS

- LED Inverter Circuits
- DC/DC Conversion Circuits
- Motor drives



### ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	$V_{DS}$	60	V
Gate-source voltage	$V_{GS}$	$\pm 20$	
Continuous drain current	$I_D$	0.45	A
		0.36	
Continuous source current (diode conduction) <sup>a</sup>	$I_S$	0.45	
Pulsed drain current <sup>b</sup>	$I_{DM}$	1.8	
Single pulse avalanche current	$I_{AS}$	0.43	mJ
Single pulse avalanche energy	$E_{AS}$	0.9	
Maximum power dissipation <sup>b</sup>	$P_D$	0.35	W
		0.182	
Operating junction and storage temperature range	$T_J, T_{stg}$	-55 to +150	°C

### THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-ambient	$R_{thJA}$	135	°C/W
Junction-to-foot (drain)	$R_{thJF}$	45	

Notes:

a. Surface mounted on FR4 board.

b. Pulse width limited by maximum junction temperature.

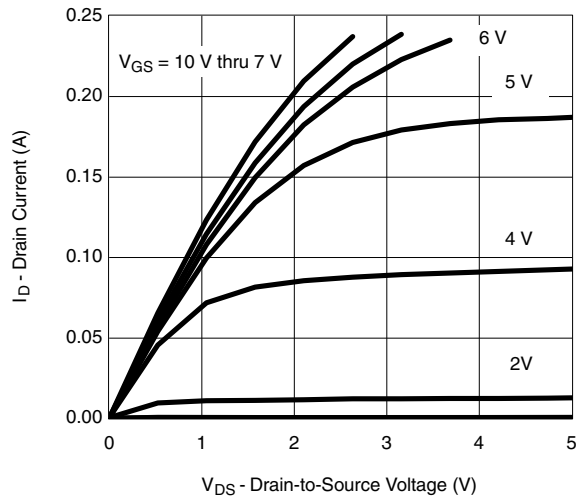
SPECIFICATIONS (T <sub>C</sub> = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA		60	-	-	V
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA		1.0		3.0	
Gate-source leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 12 V		-	-	± 5	μA
		V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V		-	-	± 5	mA
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 48 V	-	-	1	μA
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 48 V, T <sub>J</sub> = 125 °C	-	-	50	
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 48 V, T <sub>J</sub> = 150 °C	-	-	150	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	V <sub>DS</sub> ≤ 5 V	0.45	-	-	A
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 0.25 A	-	1.6	1.95	Ω
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 0.2 A, T <sub>J</sub> = 125 °C	-	1.85	2.4	
		V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 0.16 A	-	1.9	2.5	
Forward transconductance <sup>b</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.25 A		-	3	-	S
Dynamic <sup>b</sup>							
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 30 V, f = 1 MHz	-	60	-	pF
Output capacitance	C <sub>oss</sub>			-	17	-	
Reverse transfer capacitance	C <sub>rss</sub>			-	8	-	
Total gate charge <sup>c</sup>	Q <sub>g</sub>	V <sub>GS</sub> = 4.5 V	V <sub>DS</sub> = 30 V, I <sub>D</sub> = 0.16 A	-	1.3	-	nC
Gate-source charge <sup>c</sup>	Q <sub>gs</sub>			-	0.2	-	
Gate-drain charge <sup>c</sup>	Q <sub>gd</sub>			-	0.9	-	
Gate resistance	R <sub>g</sub>	f = 1 MHz		-	5.2	-	Ω
Turn-on delay time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 30 V, R <sub>L</sub> = 30 Ω I <sub>D</sub> ≅ 0.16 A, V <sub>GEN</sub> = 4.5 V, R <sub>g</sub> = 1 Ω		-	43	68	ns
Rise time <sup>c</sup>	t <sub>r</sub>			-	27	35	
Turn-off delay time <sup>c</sup>	t <sub>d(off)</sub>			-	12	20	
Fall time <sup>c</sup>	t <sub>f</sub>			-	9	15	
Source-Drain Diode Ratings and Characteristics <sup>b</sup>							
Pulsed current <sup>a</sup>	I <sub>SM</sub>			-	-	0.45	A
Forward voltage	V <sub>SD</sub>	I <sub>F</sub> = 0.45 A, V <sub>GS</sub> = 0 V		-	0.8	1.2	V

**Notes**

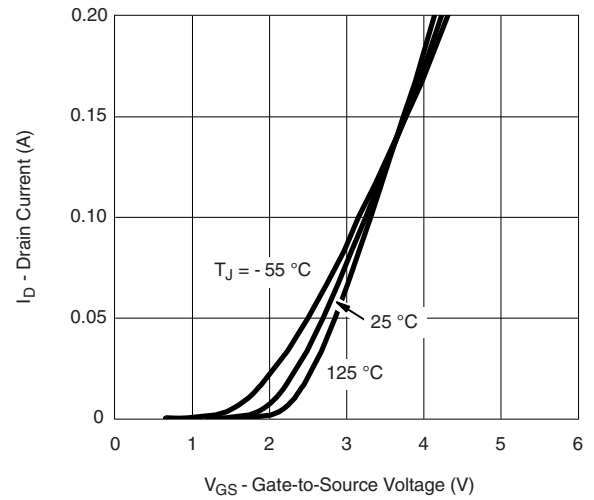
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$   
 b. Guaranteed by design, not subject to production testing  
 c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

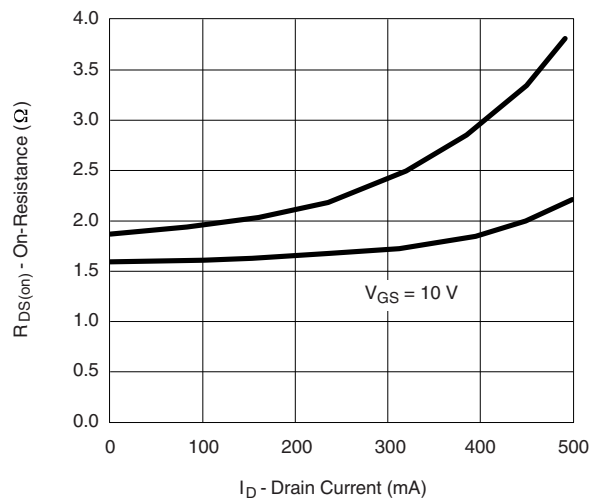
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



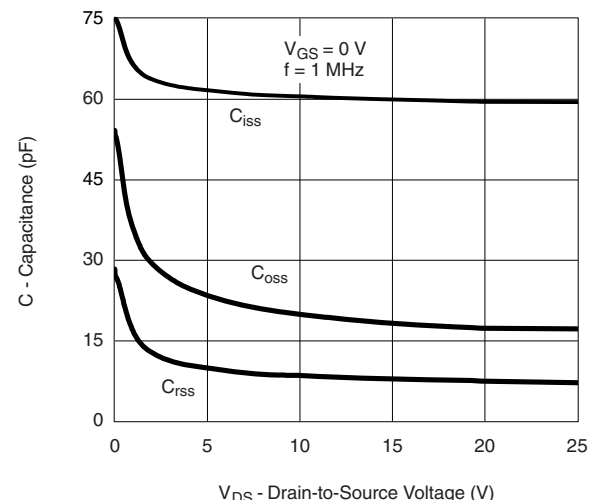
**Output Characteristics**



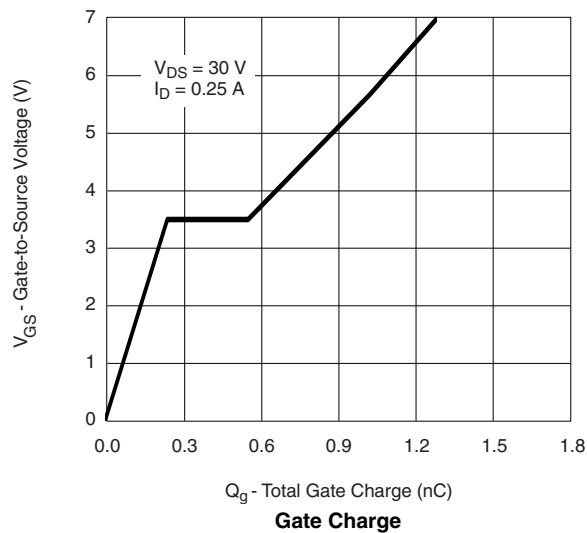
**Transfer Characteristics**



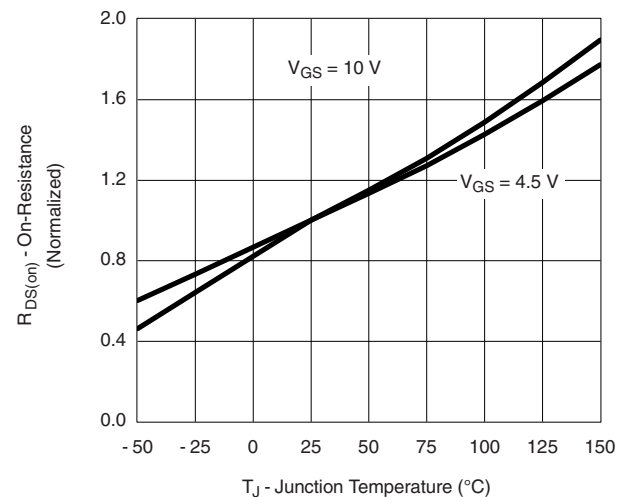
**On-Resistance vs. Drain Current**



**Capacitance**

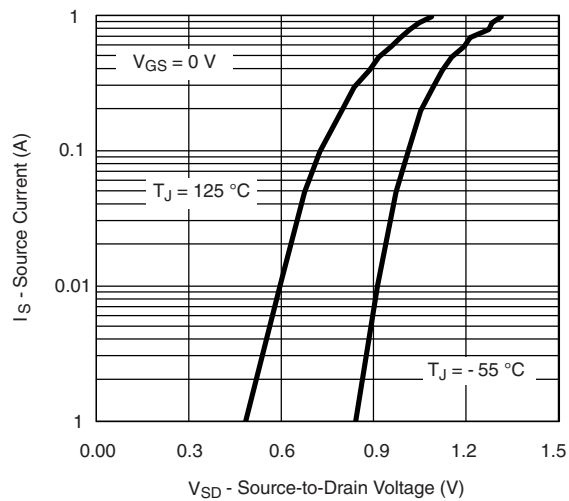


**Gate Charge**

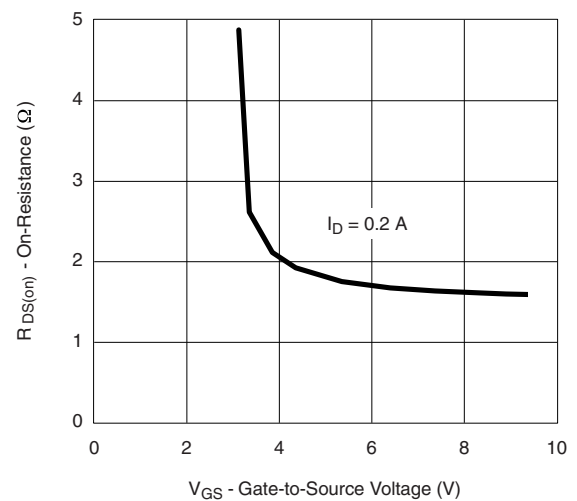


**On-Resistance vs. Junction Temperature**

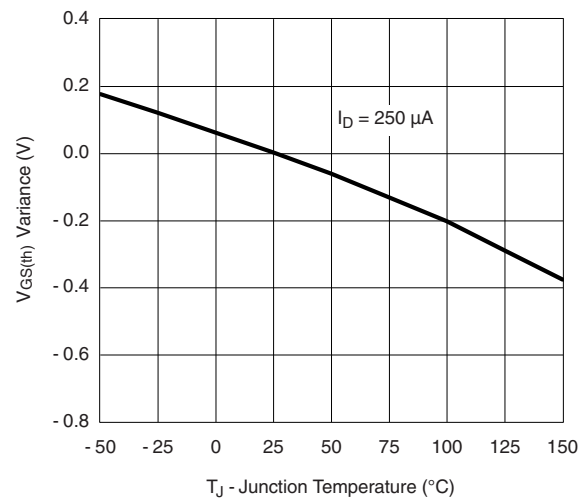
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



**Source-Drain Diode Forward Voltage**

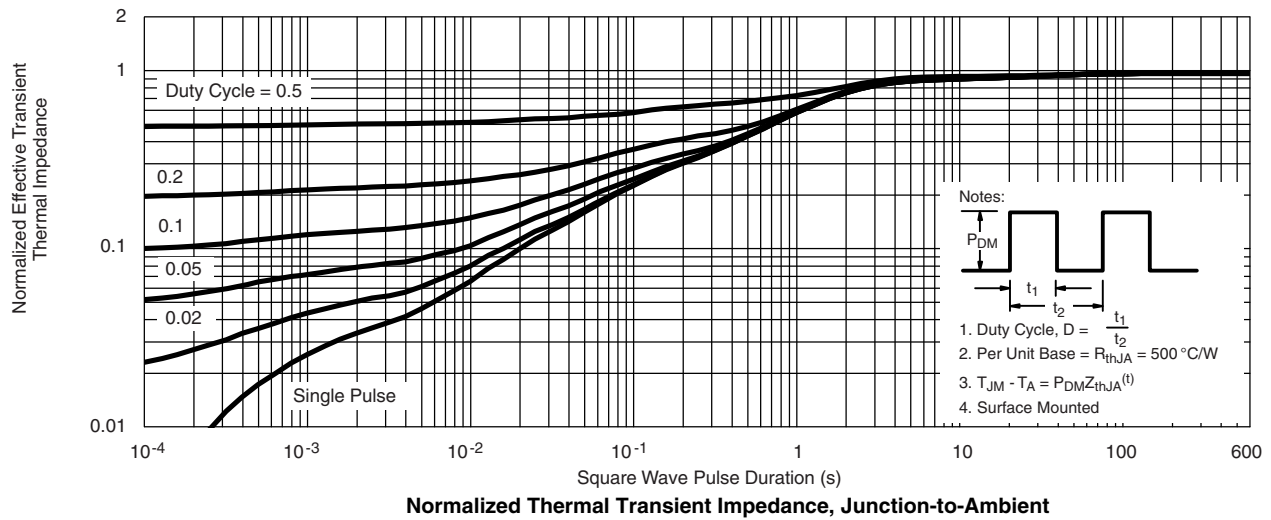


**On-Resistance vs. Gate-to-Source Voltage**



**Threshold Voltage Variance Over Temperature**

**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



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