N- and P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY			
	V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)
N-Channel	60	1.6 at V_{GS} = 10 V	0.45
		1.8 at V_{GS} = 4.5 V	0.4
P-Channel	- 60	4.5 at V_{GS} = - 10 V	- 0.25
		6.3 at V_{GS} = - 4.5 V	- 0.2

FEATURES

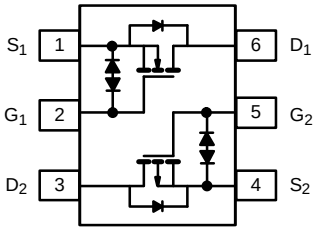
- DT-Trench Power MOSFET
- Very Small Footprint
- 100 % Rg and UIS Tested
- Gate-Source ESD Protected

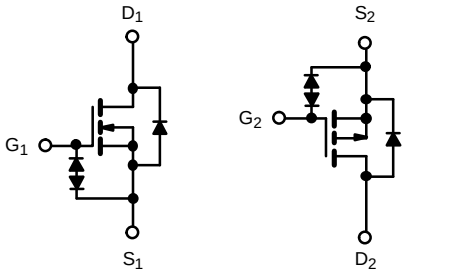
Pb-free
RoHS COMPLIANT

APPLICATIONS

- LED Inverter Circuits
- DC/DC Conversion Circuits
- Motor drives

SOT-323-6





N-Channel MOSFET P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)					
Parameter		Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage		V_{DS}	60	- 60	V
Gate-Source Voltage		V_{GS}	± 20		
Continuous Drain Current (T_J = 150 °C)[a]	T_A = 25 °C	I_D	0.45	- 0.25	A
	T_A = 85 °C		0.36	- 0.19	
Pulsed Drain Current[b]		I_{DM}	1.8	- 1	
Continuous Source Current (Diode Conduction)[a]		I_S	0.45	- 0.25	
Maximum Power Dissipation[a]	T_A = 25 °C	P_D	0.33	0.3	W
	T_A = 85 °C		0.17	0.16	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 150		°C

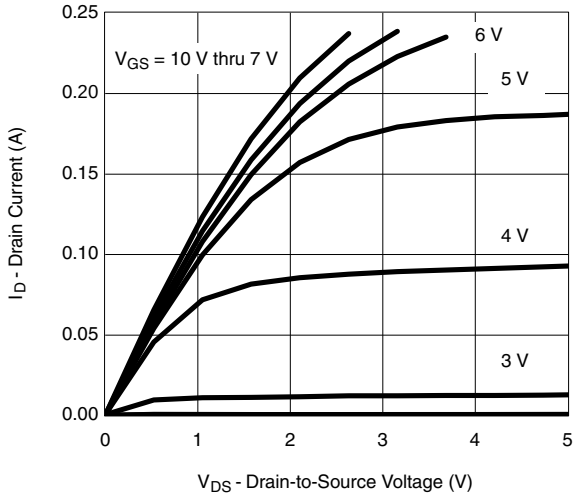
Notes:
a. Surface mounted on FR4 board.
b. Pulse width limited by maximum junction temperature.

SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)							
Parameter	**Symbol**	**Test Conditions**		**Min.**	**Typ.**	**Max.**	**Unit**
Static							
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 10 µA	N-Ch	60			V
		V_{GS} = 0 V, I_D = - 10 µA	P-Ch	- 60			
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	N-Ch	1		3.0	
		V_{DS} = V_{GS}, I_D = - 250 µA	P-Ch	- 1		- 3.0	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 5 V	N-Ch			± 50	nA
			P-Ch			± 100	
		V_{DS} = 0 V, V_{GS} = ± 10 V	N-Ch			± 150	
			P-Ch			± 200	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 50 V, V_{GS} = 0 V	N-Ch			10	
		V_{DS} = - 50 V, V_{GS} = 0 V	P-Ch			- 25	
		V_{DS} = 50 V, V_{GS} = 0 V, T_J = 85 °C	N-Ch			100	
		V_{DS} = - 50 V, V_{GS} = 0 V, T_J = 85 °C	P-Ch			- 250	
On-State Drain Current[a]	$I_{D(on)}$	V_{DS} = 10 V, V_{GS} = 4.5 V	N-Ch	1.5			A
		V_{DS} = - 10 V, V_{GS} = - 4.5 V	P-Ch	- 0.85			
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 4.5 V, I_D = 0.25 A	N-Ch		1.8	3.9	Ω
		V_{GS} = - 4.5 V, I_D = - 0.16 A	P-Ch		6.3	9.2	
		V_{GS} = 10 V, I_D = 0.3 A	N-Ch		1.6	2.2	
		V_{GS} = - 10 V, I_D = - 0.2 A	P-Ch		4.5	5.3	
Forward Transconductance[a]	g_{fs}	V_{DS} = 10 V, I_D = 0.2 A	N-Ch		12		ms
		V_{DS} = - 10 V, I_D = - 0.1A	P-Ch		10		
Diode Forward Voltage[a]	V_{SD}	I_S = 0.2 A, V_{GS} = 0 V	N-Ch			1.4	V
		I_S = - 0.2 A, V_{GS} = 0 V	P-Ch			- 1.4	
Dynamic[b]							
Total Gate Charge	Q_g	N-Channel V_{DS} = 30 V, V_{GS} = 4.5 V, I_D = 0.25 A P-Channel V_{DS} = - 30 V, V_{GS} = - 4.5 V, I_D = - 0.2 A	N-Ch		1.2		nC
			P-Ch		1.2		
Gate-Source Charge	Q_{gs}		N-Ch		0.2		
			P-Ch		0.4		
Gate-Drain Charge	Q_{gd}		N-Ch		0.8		
			P-Ch		0.5		
Input Capacitance	C_{iss}	N-Channel V_{DS} = 30 V, V_{GS} = 0 V, f = 1 MHz P-Channel V_{DS} = - 30 V, V_{GS} = 0 V, f = 1 MHz	N-Ch		60		pF
			P-Ch		83		
Output Capacitance	C_{oss}		N-Ch		18		
			P-Ch		15		
Reverse Transfer Capacitance	C_{rss}		N-Ch		8		
			P-Ch		9		
Turn-On Time[c]	t_{ON}	N-Channel V_{DD} = 30 V, R_L = 150 Ω I_D ≅ 0.2 A, V_{GEN} = 10 V, R_g = 10 Ω P-Channel V_{DD} = - 25 V, R_L = 150 Ω I_D ≅ - 0.2 A, V_{GEN} = - 10 V, R_g = 10 Ω	N-Ch		5		ns
			P-Ch		8		
Turn-Off Time[c]	t_{OFF}		N-Ch		15		
			P-Ch		13		

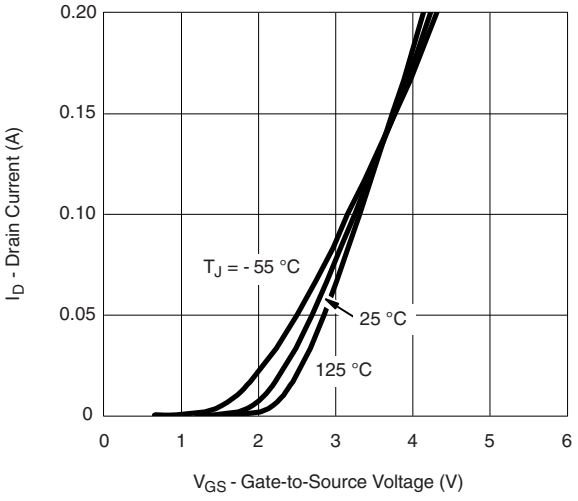
Notes:
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.
c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

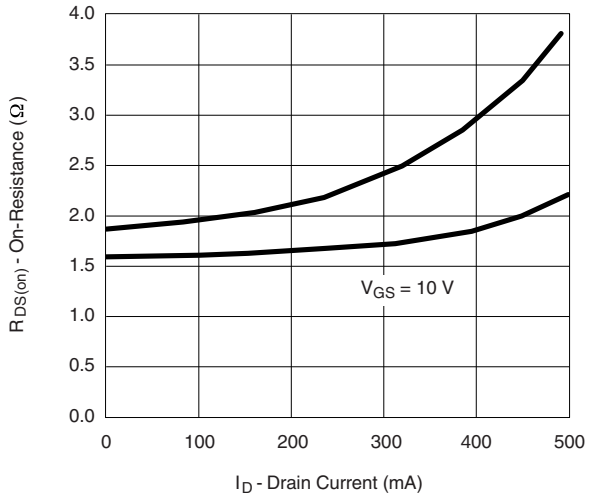
N-CHANNEL TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



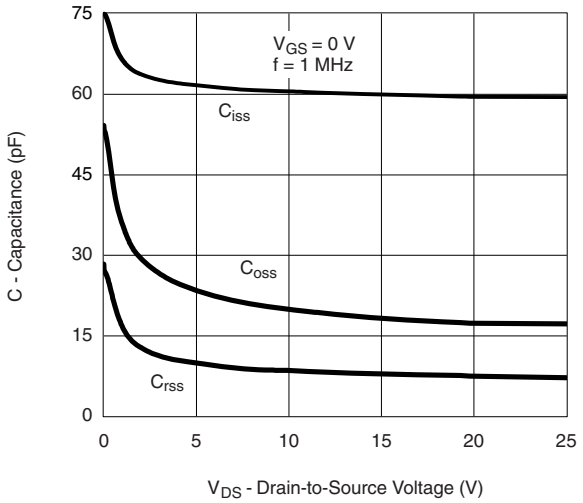
Output Characteristics



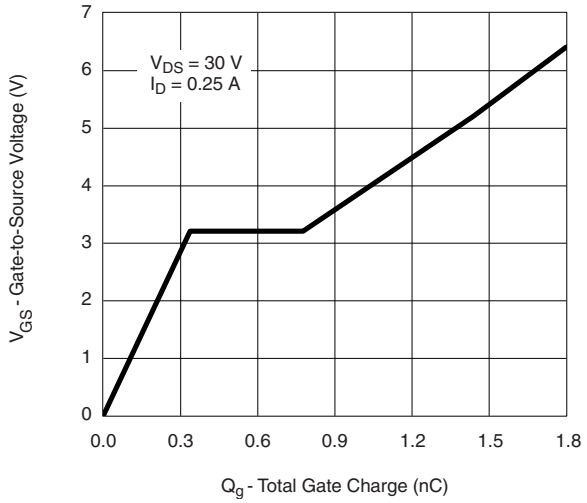
Transfer Characteristics



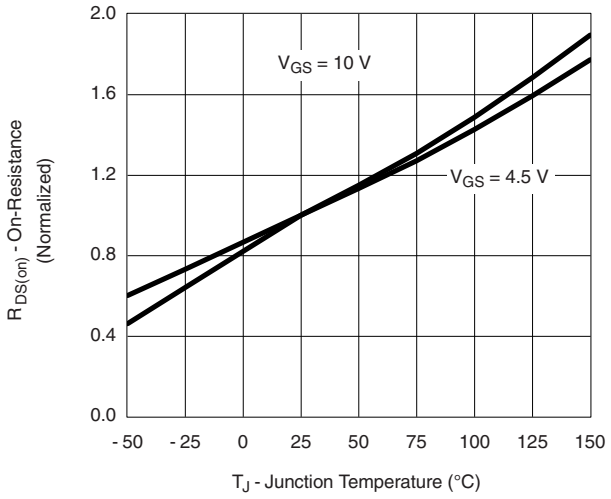
On-Resistance vs. Drain Current



Capacitance

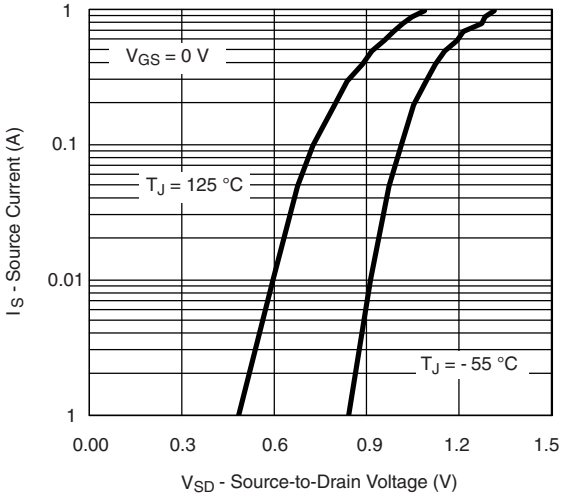


Gate Charge

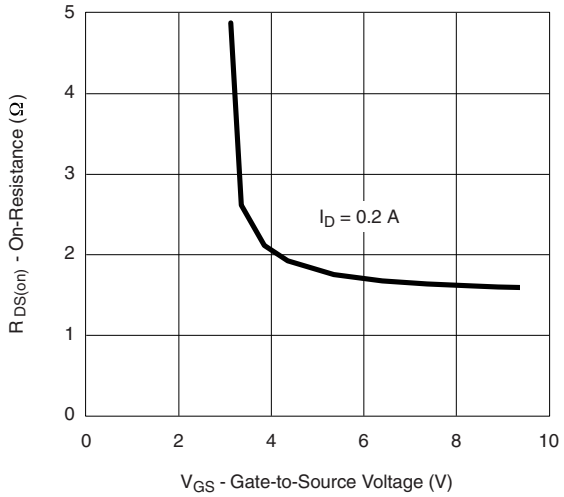


On-Resistance vs. Junction Temperature

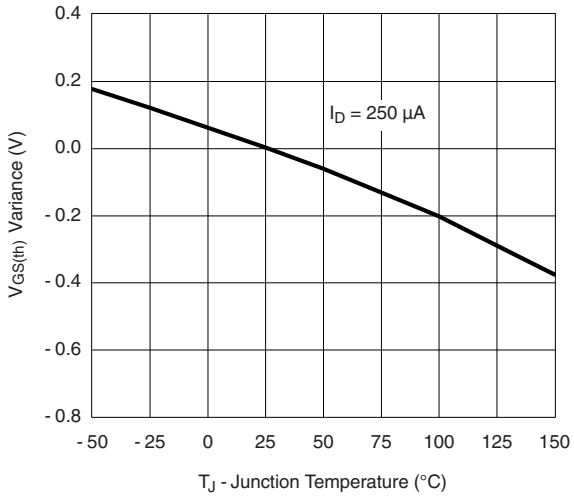
N-CHANNEL TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Source-Drain Diode Forward Voltage

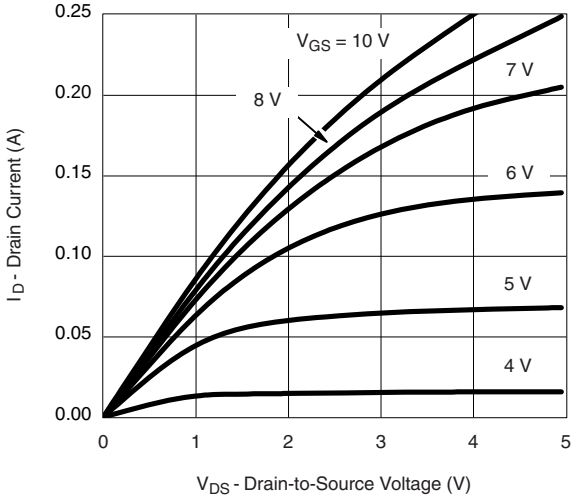


On-Resistance vs. Gate-to-Source Voltage

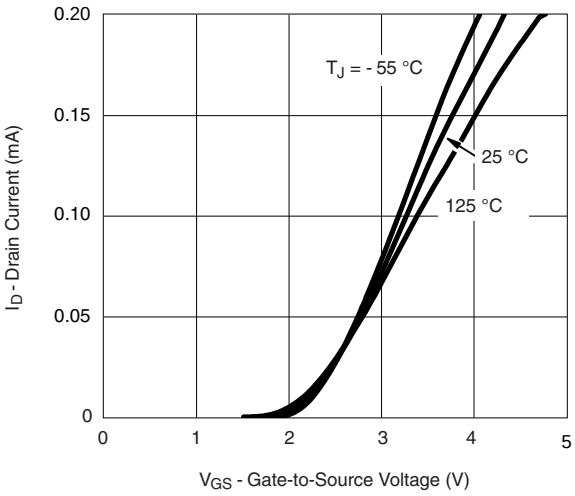


Threshold Voltage Variance Over Temperature

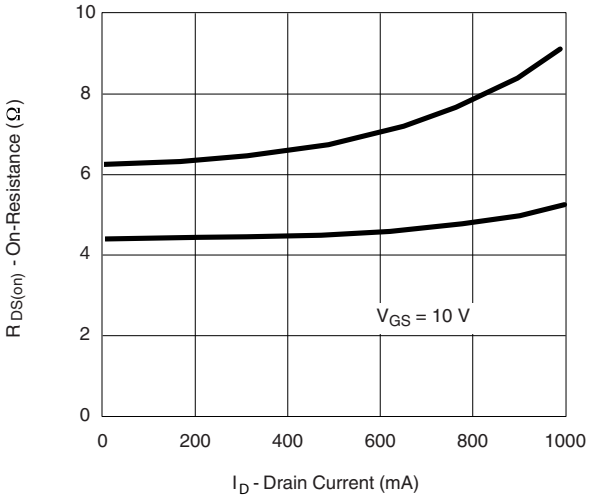
P-CHANNEL TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



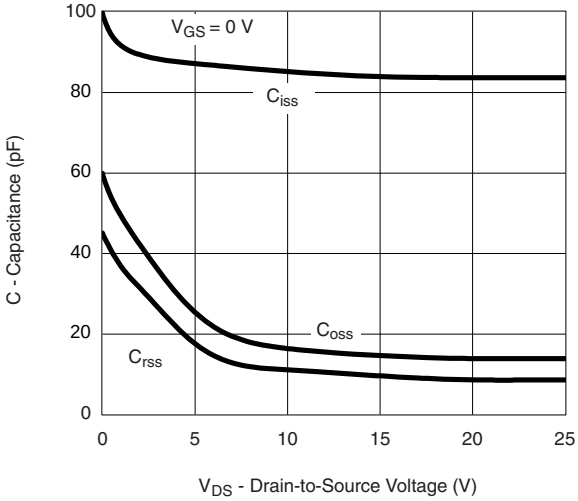
Output Characteristics



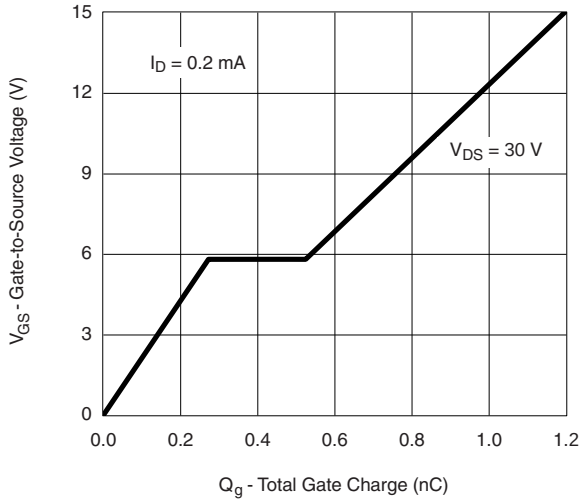
Transfer Characteristics



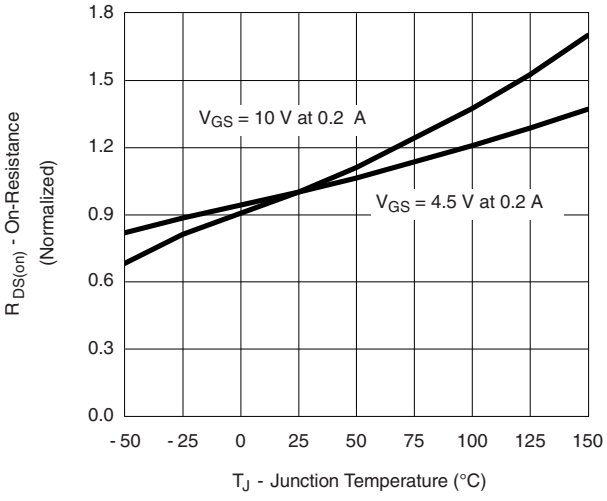
On-Resistance vs. Drain Current



Capacitance

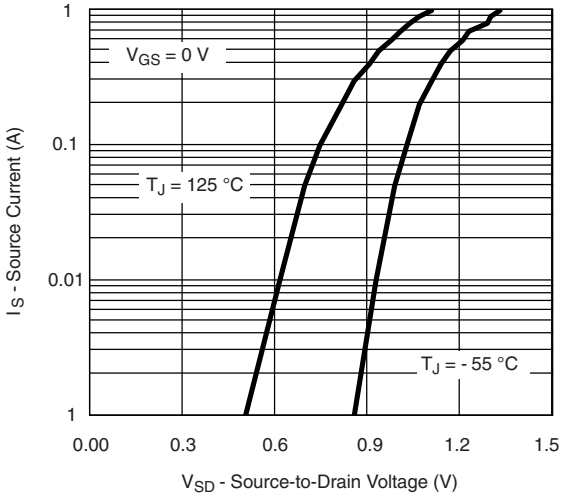


Gate Charge

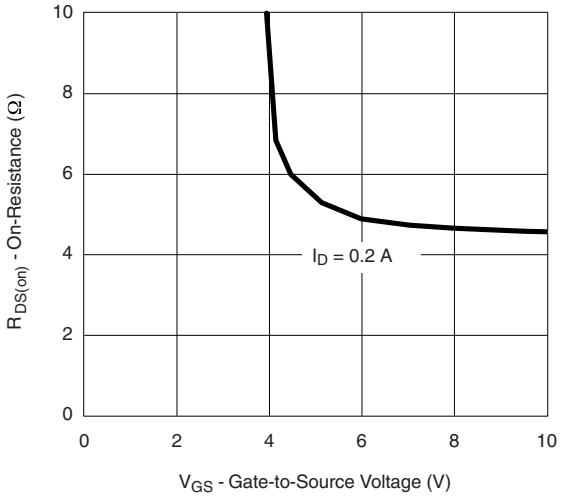


On-Resistance vs. Junction Temperature

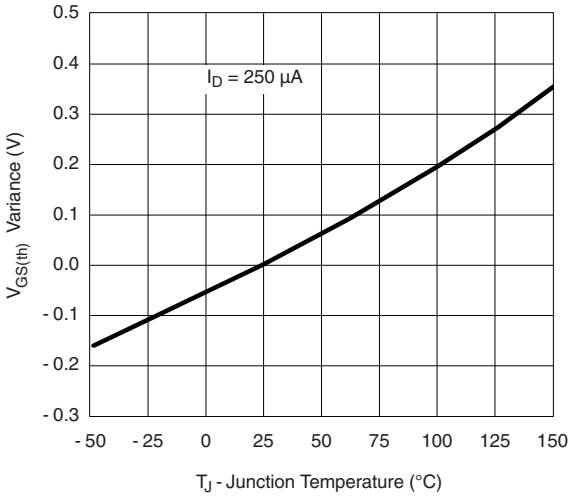
P-CHANNEL TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Source-Drain Diode Forward Voltage

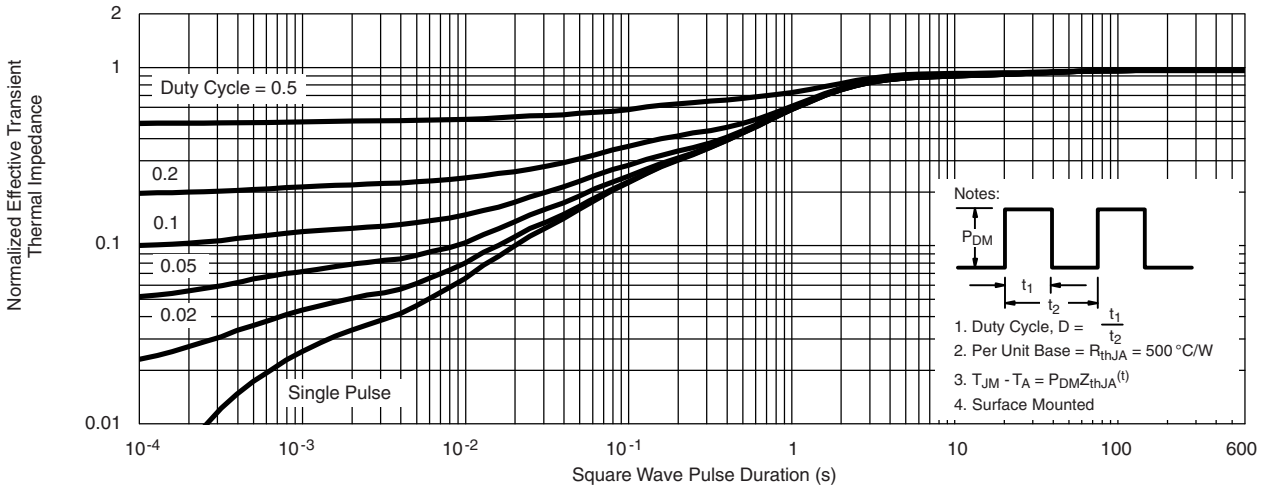


On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage Variance Over Temperature

N- OR P-CHANNEL TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient

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