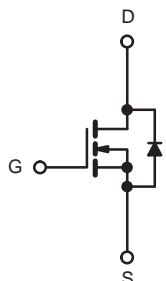
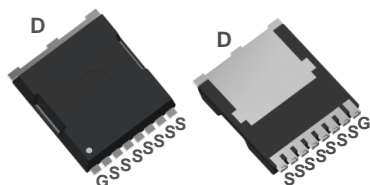


# N-Channel 30 V (D-S) MOSFET

## PRODUCT SUMMARY

V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (mΩ) TYP.	I <sub>D</sub> (A)	Q <sub>g</sub> (TYP.)
30	0.6 at V <sub>GS</sub> = 10 V	360	205 nC

## TOLL Pin Configuration



N-Channel MOSFET

## FEATURES

- DT-Trench Power MOSFET
- 100 % R<sub>g</sub> and UIS tested

## APPLICATIONS

- Power supplies:
  - Uninterruptible power supplies
  - AC/DC switch-mode power supplies
  - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- Battery management



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

## ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25 °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	± 20	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	I <sub>D</sub>	T <sub>C</sub> = 25 °C	A
		T <sub>C</sub> = 100 °C	
Pulsed Drain Current (t = 100 μs)	I <sub>DM</sub>	1440	
Avalanche Current <sup>d</sup>	I <sub>AS</sub>	125	
Single Avalanche Energy <sup>a</sup>	E <sub>AS</sub>	1258	mJ
Maximum Power Dissipation <sup>a</sup>	P <sub>D</sub>	T <sub>C</sub> = 25 °C	W
		T <sub>C</sub> = 100 °C	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C

## THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	15	°C/W
Junction-to-Case (Drain)	R <sub>thJC</sub>	0.5	

## Notes

- Duty cycle ≤ 1 %.
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR4 material).
- Single pulse width limited by junction temperature T<sub>J</sub>(MAX)=150°C.

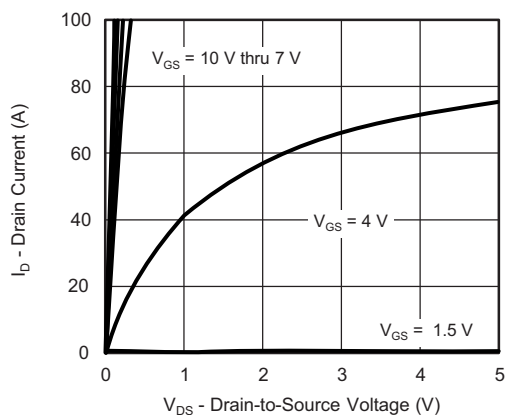
SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	30	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1.5	-	3.5	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	-	-	1	μA
		V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C	-	-	10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 10 V, V <sub>GS</sub> = 10 V	360	-	-	A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A	-	0.6	0.8	mΩ
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 50 A	-	0.98	1.3	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 50 A	-	123	-	S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 15 V, f = 1 MHz	-	14500	-	pF
Output Capacitance	C <sub>oss</sub>		-	2680	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	778	-	
Total Gate Charge <sup>c</sup>	Q <sub>g</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A	-	205	-	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>		-	43	-	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>		-	37	-	
Gate Resistance	R <sub>g</sub>	f = 1 MHz	-	2.8	-	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 15 V, I <sub>D</sub> = 50 A, R <sub>g</sub> = 3 Ω V <sub>GEN</sub> = 10 V	-	49	-	ns
Rise Time <sup>c</sup>	t <sub>r</sub>		-	56	-	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>		-	166	-	
Fall Time <sup>c</sup>	t <sub>f</sub>		-	72	-	
Drain-Source Body Diode Ratings and Characteristics <sup>b</sup> (T <sub>C</sub> = 25 °C)						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	-	-	360	A
Pulsed Current (t = 100 μs)	I <sub>SM</sub>		-	-	1440	A
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 1 A, V <sub>GS</sub> = 0 V	-	-	1	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 50 A, di/dt = 500 A/μs	-	58	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>		-	185	-	nC

**Notes**

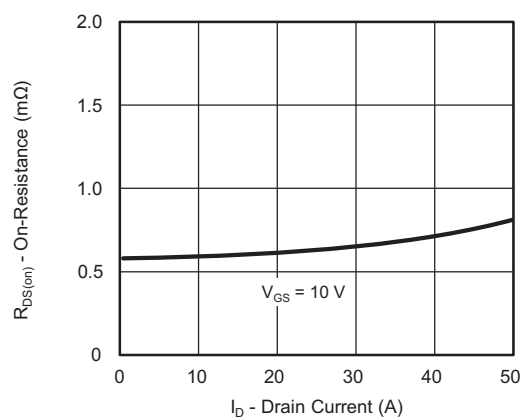
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.  
 c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

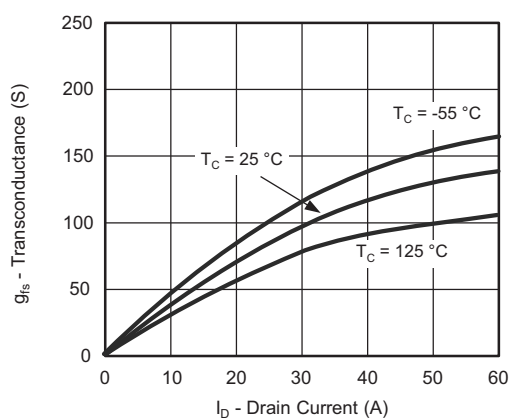
**TYPICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)



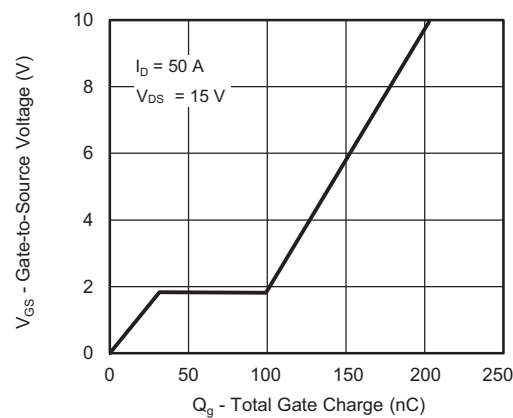
**Output Characteristics**



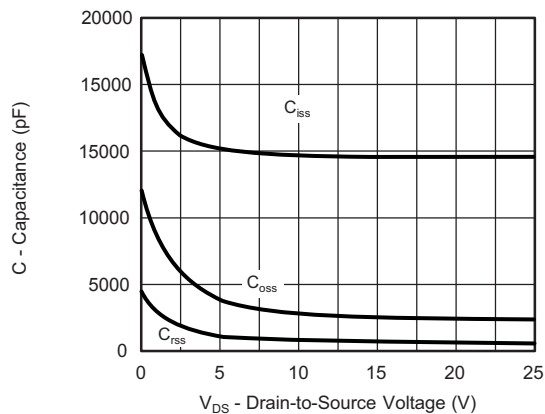
**On-Resistance vs. Drain Current**



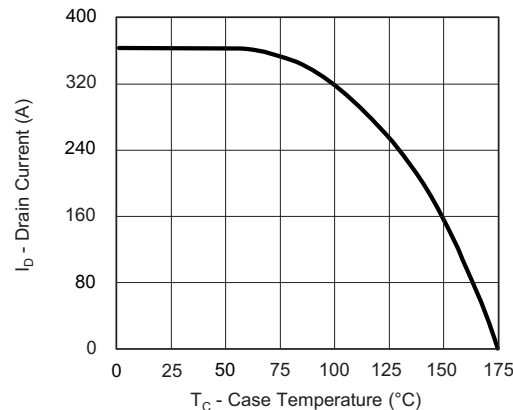
**Transconductance**



**Gate Charge**

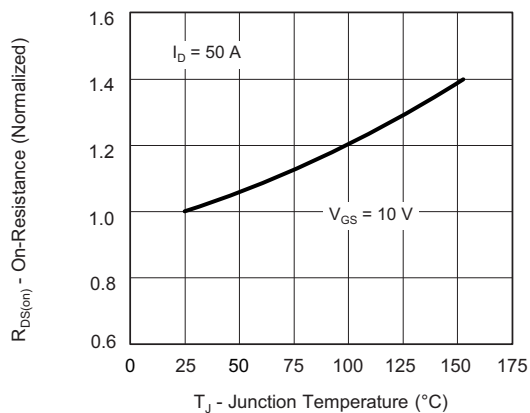


**Capacitance**

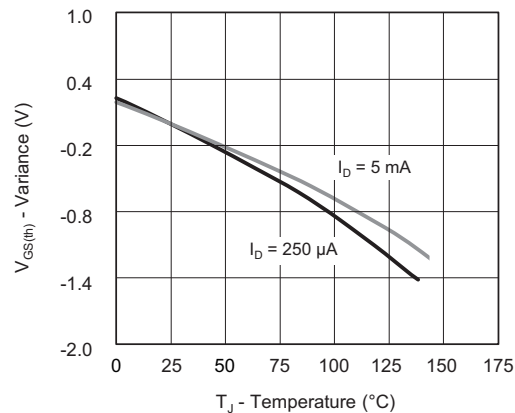


**Current De-Rating**

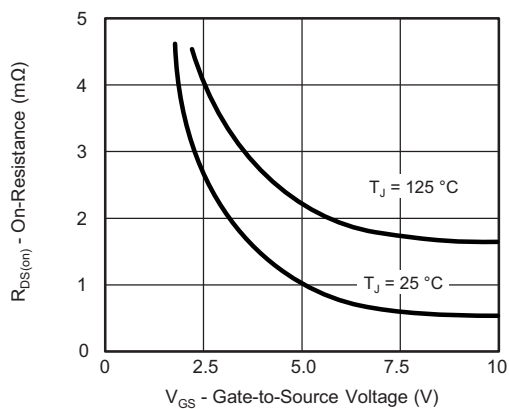
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



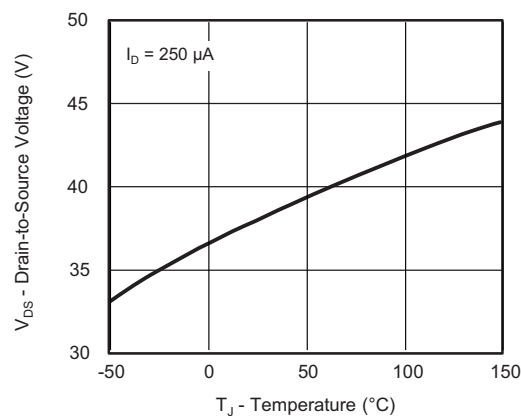
**On-Resistance vs. Junction Temperature**



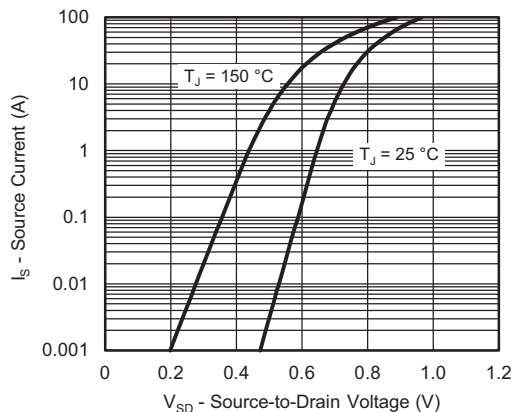
**Threshold Voltage**



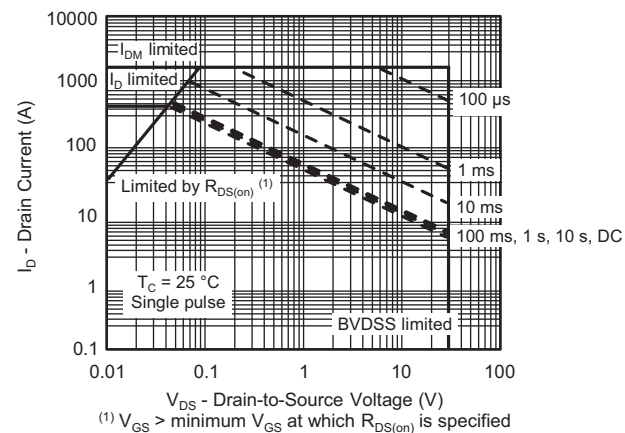
**On-Resistance vs. Gate-to-Source Voltage**



**Drain Source Breakdown vs. Junction Temperature**

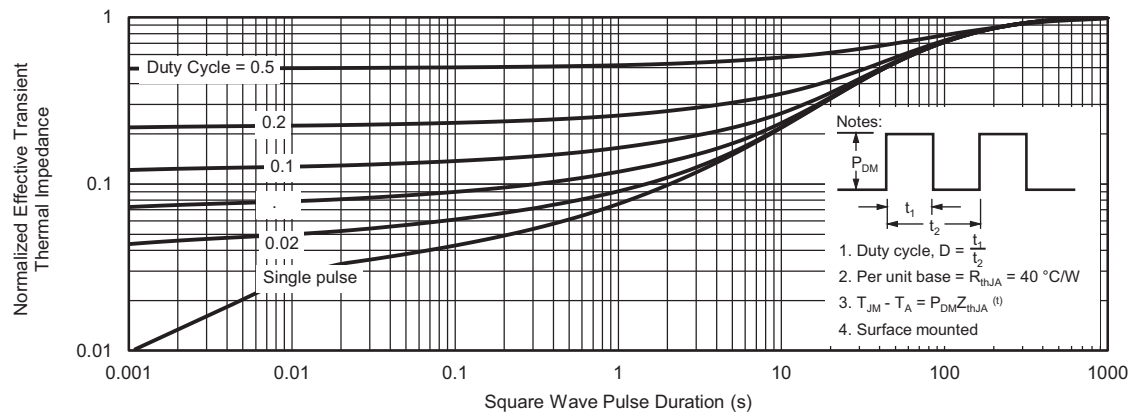


**Source Drain Diode Forward Voltage**



**Safe Operating Area**

**THERMAL RATINGS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



**Normalized Thermal Transient Impedance, Junction-to-Ambient**

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