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N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}$ (m Ω)(TYP.)	I _D (A) ^{a, e}	Q _g (TYP.)	
30	2.1at V _{GS} = 10 V	120	440 - 0	
	2.9at V _{GS} = 4.5 V	100	142 nC	

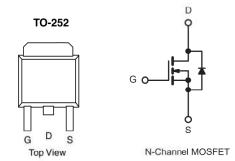
FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested
- · Compliant to RoHS Directive 2011/65/EU





- OR-ing
- Server
- DC/DC



Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	30		
Gate-Source Voltage		V _{GS}	± 20		
	T _C = 25 °C		120 ^{a, e}	A	
Continuous Drain Current (T _J = 175 °C)	T _C = 70 °C	1	98 ^e		
	T _A = 25 °C	I _D	35.8 ^{b, c}		
	T _A = 70 °C		27 ^{b, c}		
Pulsed Drain Current		I _{DM}	360		
Avalanche Current Pulse	L = 0.1 mH	I _{AS}	39		
Single Pulse Avalanche Energy	L=0.1IIII	E _{AS}	714	mJ	
Continuous Source-Drain Diode Current	T _C = 25 °C	90	120 ^{a, e}	Δ.	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	3.13 ^{b, c}	A	
	T _C = 25 °C		250 ^a		
Maximum Power Dissipation	T _C = 70 °C	В	175	w	
	T _A = 25 °C	P _D	3.75 ^{b, c}		
	T _A = 70 °C		2.63 ^{b, c}		
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Тур.	Max.	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 sec	R _{thJA}	32	40	°C/M	
Maximum Junction-to-Case	Steady State	R _{thJC}	0.5	0.6	°C/W	

- Notes:
 a. Based on T_C = 25 °C.
 b. Surface mounted on 1" x 1" FR4 board.
 c. t = 10 sec.
 d. Maximum under steady state conditions is 90 °C/W.
- e. Calculated based on maximum junction temperature.





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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		35		m\//°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	10 - 200 μΛ		- 7.5		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.5		2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1		
		V _{DS} = 24 V, V _{GS} = 0 V, T _J = 55 °C			10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			А	
Drain-Source On-State Resistance ^a	P	V _{GS} = 10 V, I _D = 30 A		2.1	2.5		
	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 20 A		2.9	3.9	mΩ	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 30 A		160		S	
Dynamic ^b							
Input Capacitance	C _{iss}			6910		pF	
Output Capacitance	Coss	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		746			
Reverse Transfer Capacitance	C _{rss}			906			
Total Gate Charge	Qg			142			
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 30 \text{ A}$		12		nC	
Gate-Drain Charge	Q _{gd}			95			
Gate Resistance	R _g	f = 1 MHz		2.8		Ω	
Turn-On Delay Time	t _{d(on)}			18		ns	
Rise Time	t _r	V_{DD} = 15 V, R_{L} = 0.625 Ω		11			
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 30 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		70			
Fall Time	t _f			10			
Turn-On Delay Time	t _{d(on)}		U. J	55			
Rise Time	t _r	$V_{DD} = 15 \text{ V}, R_{L} = 0.67 \Omega$		180			
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 30 \text{ A, V }_{GEN} = 4.5 \text{ V, R}_g = 1 \Omega$		55			
Fall Time	t _f			12			
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	Is	T _C = 25 °C			120	А	
Pulse Diode Forward Current ^a	I _{SM}				360	^	
Body Diode Voltage	V _{SD}	I _S = 22 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			52		ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 30 A, di/dt = 100 A/μs, T _J = 25 °C		70.2		nC	
Reverse Recovery Fall Time	t _a	15 - 20 M, dirdt - 100 Mps, 11 - 25 C		27			
Reverse Recovery Rise Time	t _b	7		25		ns	

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

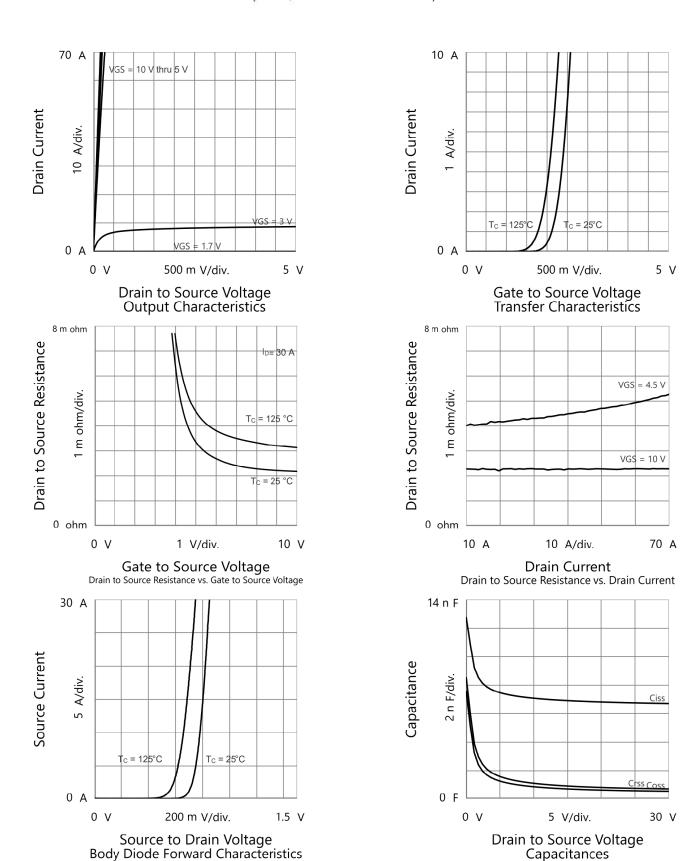
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.





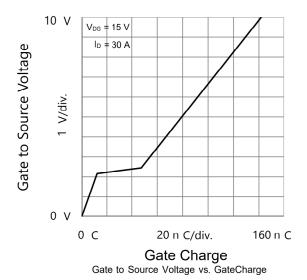
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

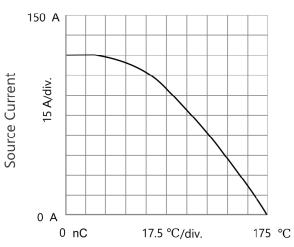






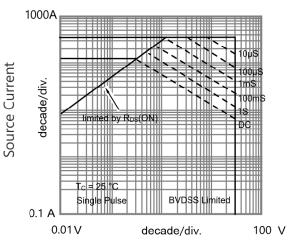
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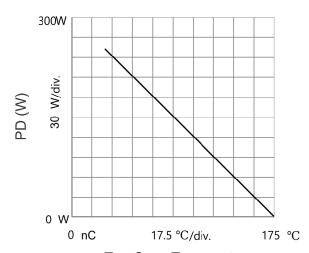


T_C - Case Temperature

Current Derating

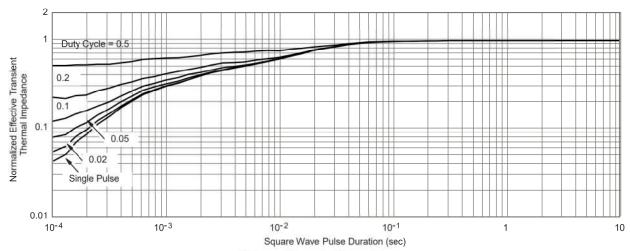


Source to Drain Voltage Safe Operating Area, Junction-to-Ambient



T_C - Case Temperature

Power Derating



Normalized Thermal Transient Impedance, Junction-to-Case



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