

## N-Channel 30 V (D-S) MOSFET

### PRODUCT SUMMARY

$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)	$Q_g$ (Typ.)
30	0.0125 at $V_{GS} = 10$ V	55 <sup>d</sup>	18.2
	0.0163 at $V_{GS} = 4.5$ V	45 <sup>d</sup>	

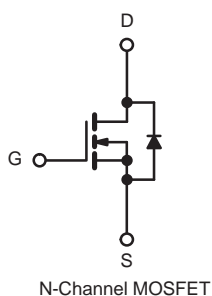
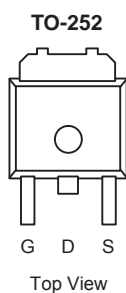
### FEATURES

- DT-Trench Power MOSFET
- 100 %  $R_g$  and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



### APPLICATIONS

- Power Supply
  - Secondary Synchronous Rectification
- DC/DC Converter



### ABSOLUTE MAXIMUM RATINGS $T_C = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ )	$I_D$	55 <sup>d</sup>	A
		45 <sup>d</sup>	
Pulsed Drain Current	$I_{DM}$	165	
Avalanche Current	$I_{AS}$	34	
Single Avalanche Energy <sup>a</sup>	$E_{AS}$	78	mJ
Maximum Power Dissipation <sup>a</sup>	$P_D$	55.5 <sup>b</sup>	W
		2.7	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 150	$^\circ\text{C}$

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) <sup>c</sup>	$R_{thJA}$	54	$^\circ\text{C/W}$
Junction-to-Case (Drain)	$R_{thJC}$	2.7	

Notes:

- Duty cycle  $\leq 1\%$ .
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR-4 material).
- Package limited.

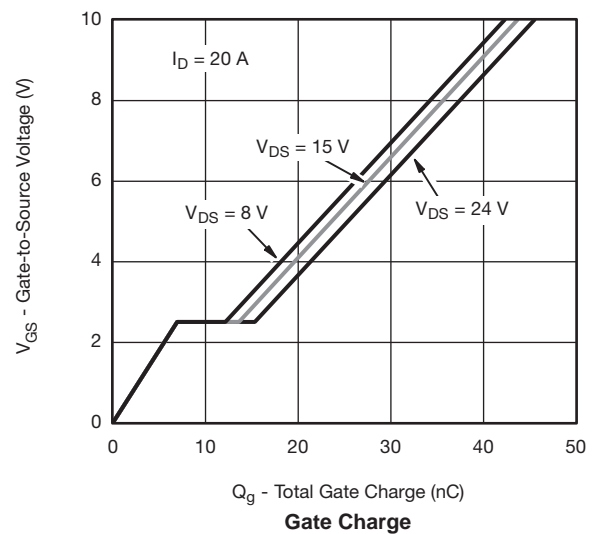
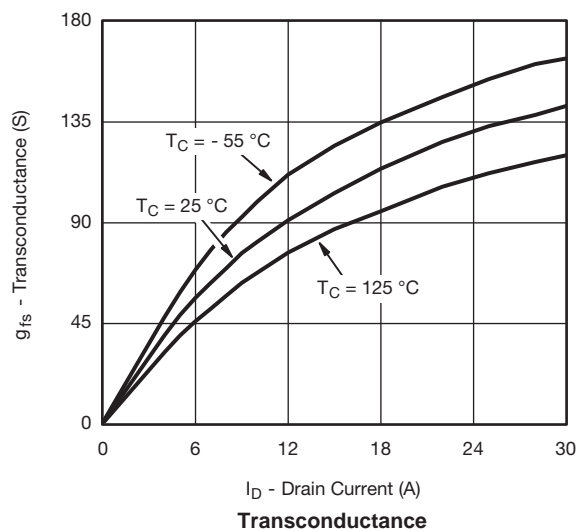
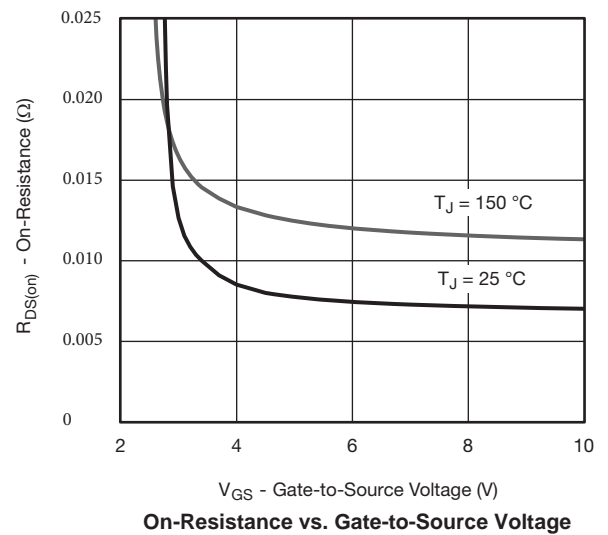
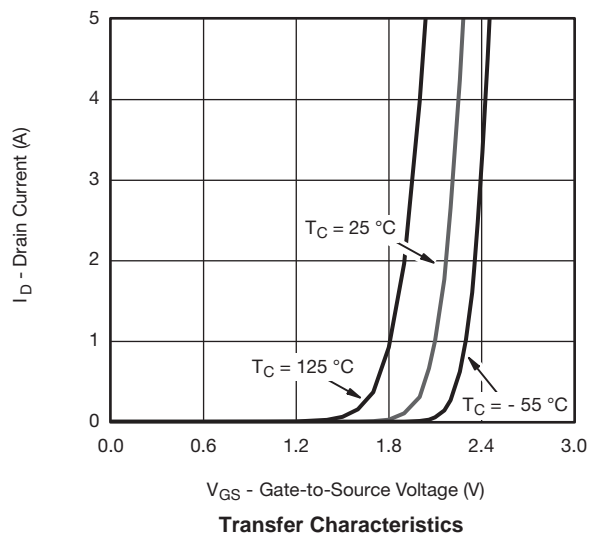
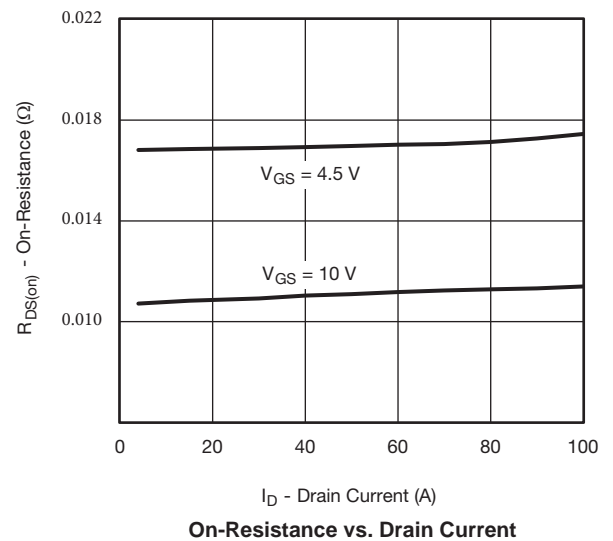
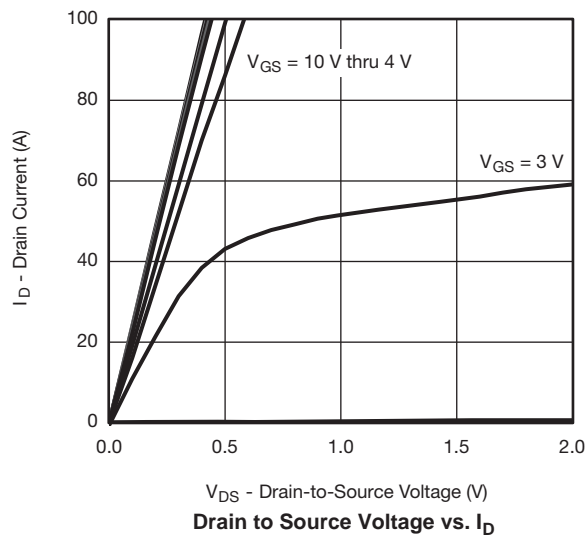
SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>DS</sub> = 0 V, I <sub>D</sub> = 250 μA	30			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1		3	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V			± 250	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			1	μA
		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	
		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 150 °C			250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 10 V, V <sub>GS</sub> = 10 V	55			A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 12 A		0.0125	0.0157	Ω
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 10 A		0.0163	0.0179	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A		100		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 15 V, f = 1 MHz		578		pF
Output Capacitance	C <sub>oss</sub>			364		
Reverse Transfer Capacitance	C <sub>rss</sub>			210		
Total Gate Charge <sup>c</sup>	Q <sub>g</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		34	56	nC
		V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 20 A		20.1	30.2	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>			6		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			5.7		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.4	2	4	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 15 V, R <sub>L</sub> = 1.5 Ω I <sub>D</sub> ≅ 10 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 1 Ω		8	16	ns
Rise Time <sup>c</sup>	t <sub>r</sub>			9	18	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			35	53	
Fall Time <sup>c</sup>	t <sub>f</sub>			9	18	
Drain-Source Body Diode Ratings and Characteristics T <sub>C</sub> = 25 °C <sup>b</sup>						
Continuous Current	I <sub>S</sub>				55	A
Pulsed Current	I <sub>SM</sub>				165	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 10 A, V <sub>GS</sub> = 0 V		0.75	1.5	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 10 A, dI/dt = 100 A/μs		34	51	ns
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>			2	3	A
Reverse Recovery Charge	Q <sub>rr</sub>			34	51	nC

Notes:

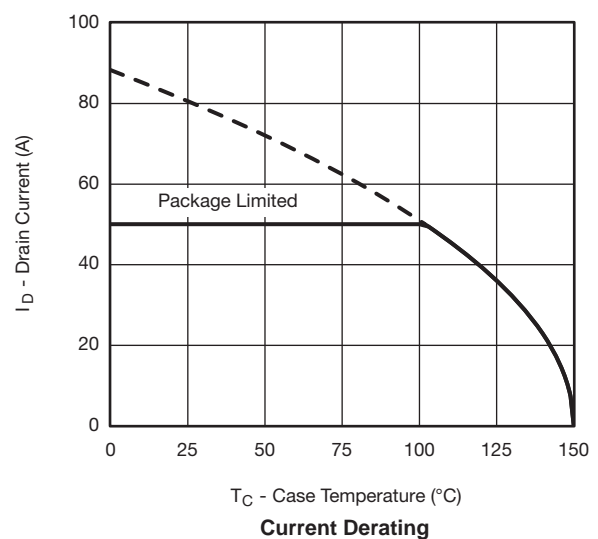
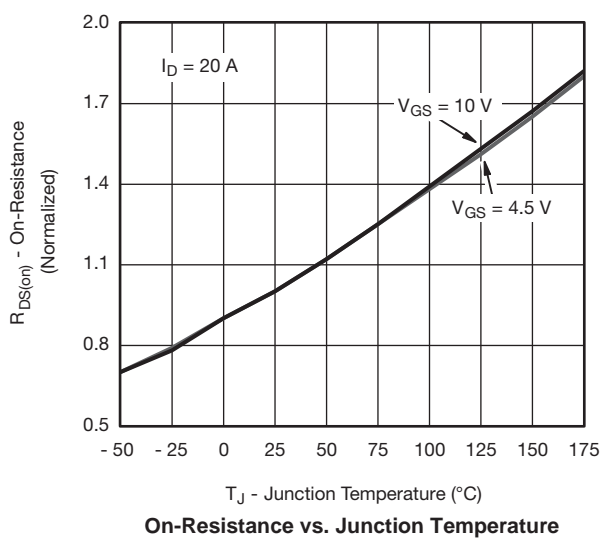
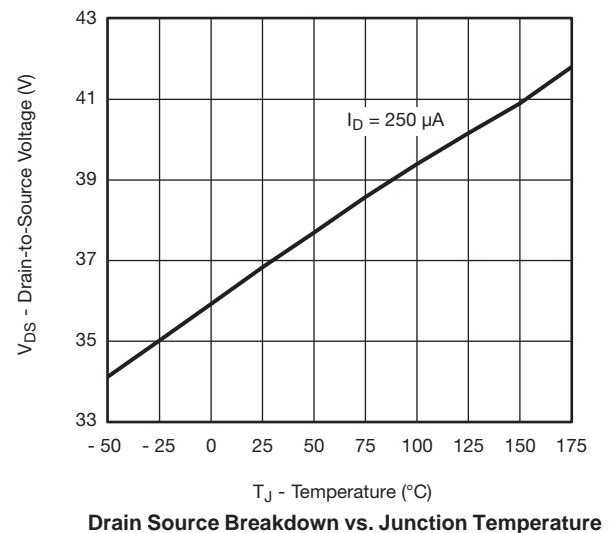
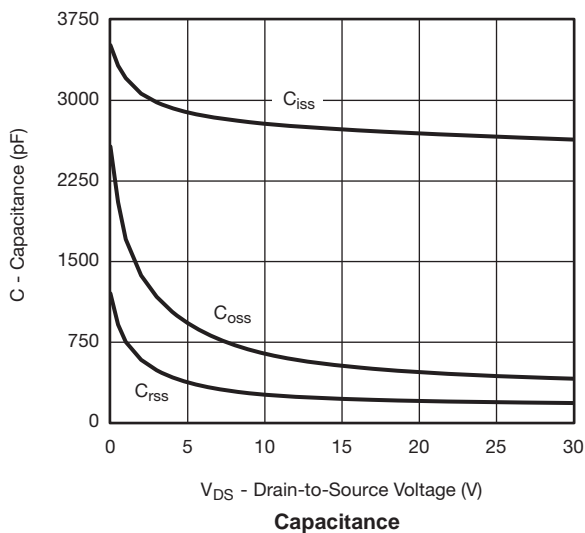
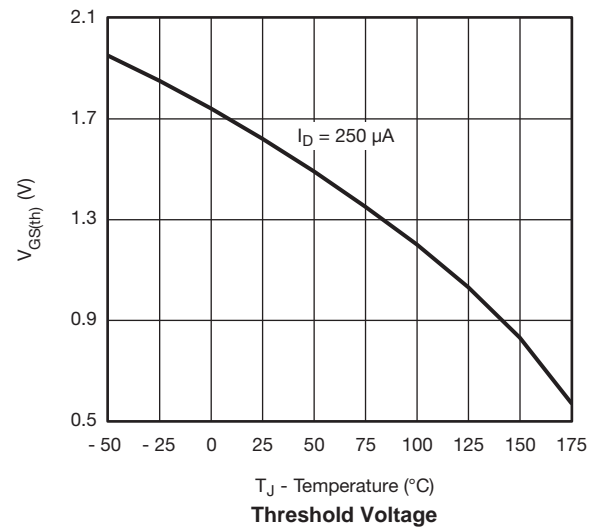
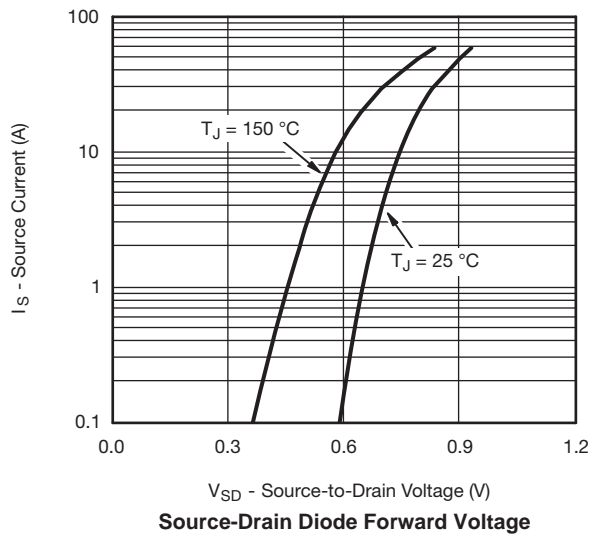
- Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

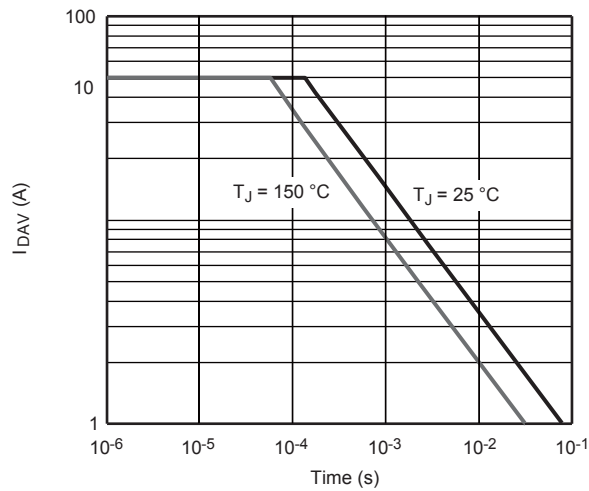
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



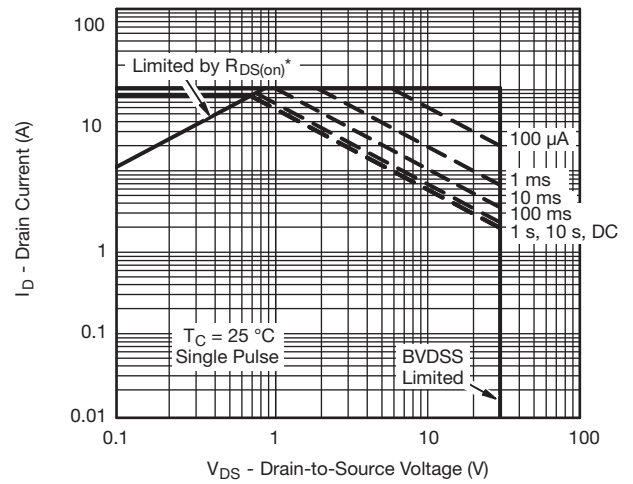
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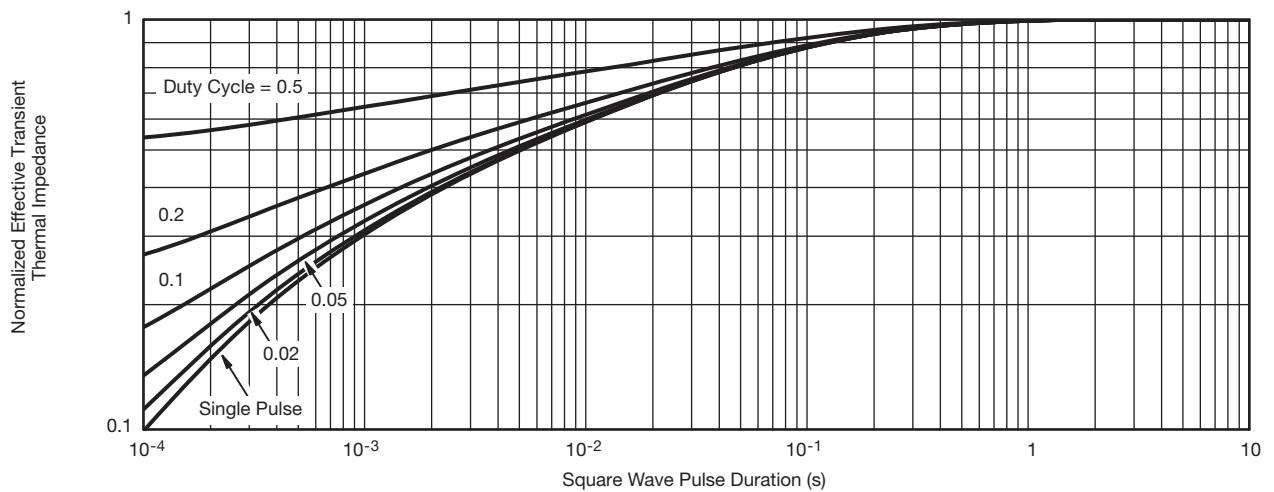


**Single Pulse Avalanche Current Capability vs. Time**



\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

**Safe Operating Area**



**Normalized Thermal Transient Impedance, Junction-to-Case**

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