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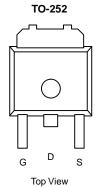
N-Channel 100 V (D-S) MOSFET

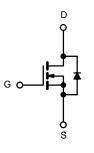
PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}$ (Ω) Max.	I _D (A)	Q _g (Typ.)	
100	0.090 at V _{GS} = 10 V	20	19.8	
	0.094 at V _{GS} = 4.5 V	15.2	19.0	

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested
- Material categorization:







N-Channel MOSFET

APPLICATIONS

- DC/DC Converters
- DC/AC Inverters
- Motor Drives

ABSOLUTE MAXIMUM RA	TINGS ($T_C = 25 ^{\circ}C$, unless of	otherwise noted)		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	100	V	
Gate-Source Voltage		V _{GS}	± 20	v
Continuous Drain Current	T _C = 25 °C	L	20	
Continuous Diain Current	T _C = 70 °C	I _D	15.2	A
Pulsed Drain Current (t = 300 μs)	I _{DM}	65		
Avalanche Current		I _{AS}	15	
Single Avalanche Energy ^a	gle Avalanche Energy ^a L = 0.1 mH		11.25	mJ
Maximum Power Dissipation ^a	T _C = 25 °C	В	41.7 ^b	W
	T _A = 25 °C ^c	P _D	2.1	VV
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Limit	Unit		
Junction-to-Ambient (PCB Mount) ^C	R _{thJA}	60	°C/W		
Junction-to-Case (Drain)	R _{thJC}	3	C/VV		

Notes:

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR-4 material).
- d. Base on T_C = 25 °C.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	100			V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		3	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 250	nA
Zero Gate Voltage Drain Current		V _{DS} = 100 V, V _{GS} = 0 V			1	
	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V, T _J = 125 °C			50	μΑ
		V _{DS} = 100 V, V _{GS} = 0 V, T _J = 150 °C			250	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α
Drain-Source On-State Resistance ^a		V _{GS} = 10 V, I _D = 6.6 A		0.090	0.099	_
	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 6 A		0.094	0.103	Ω
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 6.6 A		25		S
Dynamic ^b	-				<u> </u>	
Input Capacitance	C _{iss}			860		pF
Output Capacitance	C _{oss}	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		85		
Reverse Transfer Capacitance	C _{rss}			40		
Total Gate Charge ^c	Q_{g}			19.8	30	nC
Gate-Source Charge ^c	Q _{gs}	V _{DS} = 50 V, V _{GS} = 10 V, I _D = 6.6 A		3.6		
Gate-Drain Charge ^c	Q _{qd}			4.1		
Gate Resistance	R _q	f = 1 MHz	0.4	2	4	Ω
Turn-On Delay Time ^c	t _{d(on)}			8	16	
Rise Time ^c	t _r	$V_{DD} = 50 \text{ V}, R_{L} = 9.6 \Omega$		11	20	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 5.2 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		18	27	
Fall Time ^c	t _f			5	10	
Turn-On Delay Time ^c	t _{d(on)}			38	57	ns
Rise Time ^c	t _r	$V_{DD} = 50 \text{ V}, R_1 = 9.6 \Omega$		58	87	-
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 5.2 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		18	27	
Fall Time ^c	t _f			8	16	
Drain-Source Body Diode Ratings a	nd Characteri	stics ^b T _C = 25 °C				
Continuous Current	I _S				20	
Pulsed Current	I _{SM}				65	A
Forward Voltage ^a	V _{SD}	I _F = 5.2 A, V _{GS} = 0 V		0.8	1.5	V
Reverse Recovery Time	t _{rr}			34	51	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 5.2 A, dI/dt = 100 A/μs		3	5	Α
Reverse Recovery Charge	Q _{rr}			50	75	nC

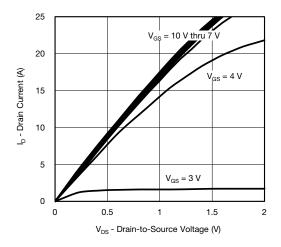
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

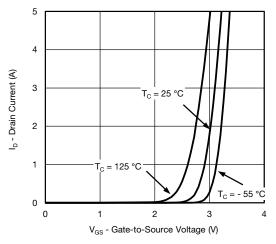
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



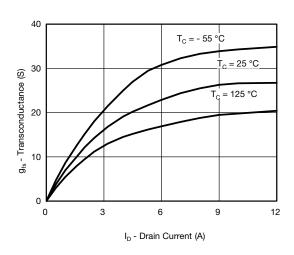
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



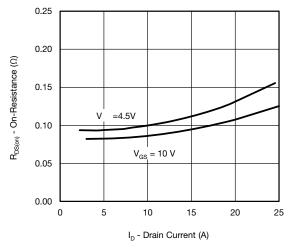
Output Characteristics



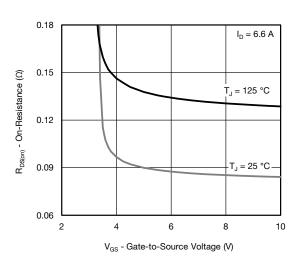
Transfer Characteristics



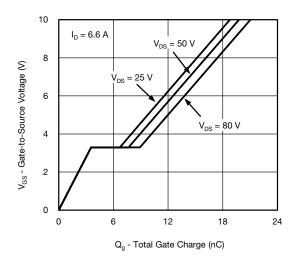
Transconductance



On-Resistance vs. Drain Current



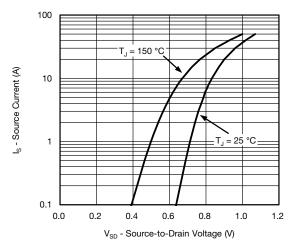
On-Resistance vs. Gate-to-Source Voltage



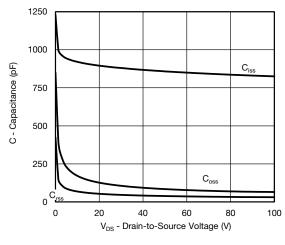
Gate Charge



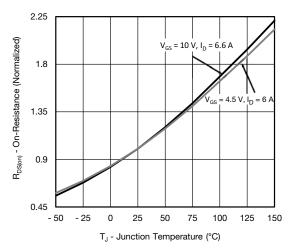
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



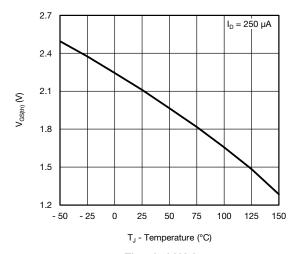
Source-Drain Diode Forward Voltage



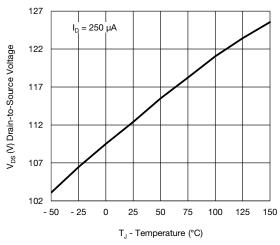
Capacitance



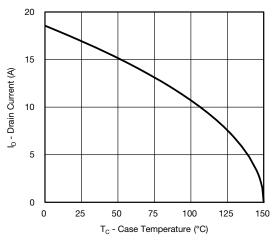
On-Resistance vs. Junction Temperature



Threshold Voltage



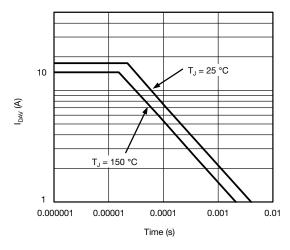
Drain Source Breakdown vs. Junction Temperature

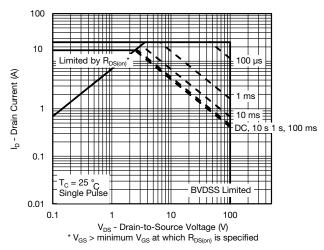


Current Derating



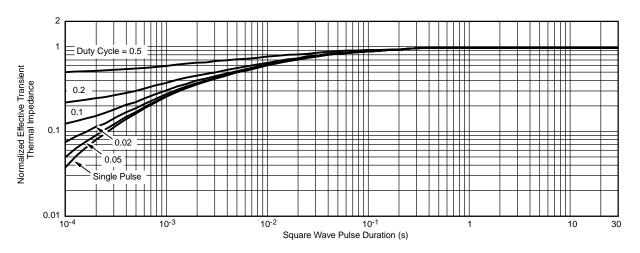
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





Single Pulse Avalanche Current Capability vs. Time





Normalized Thermal Transient Impedance, Junction-to-Case

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