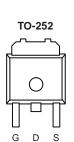
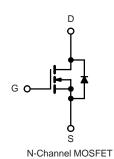


N-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^a	Q _g (Typ.)		
	0.027 at V _{GS} = 4.5 V	30	15.5 nC		
20	0.033at V _{GS} = 2.5 V	20	15.5 110		



Top View



FEATURES

- DT-Trench Power MOSFET
- 100 % R_g Tested
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC

RoHS

APPLICATIONS

- DC/DC
- Low Voltage Drive
- POL

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	20	V	
Gate-Source Voltage		V_{GS}	± 12		
Continuous Drain Current (T _J = 150 °C)	T _C = 25 °C		30 ^a		
	T _C = 70 °C		20 ^a		
	T _A = 25 °C	I _D	22.6 ^{b, c}		
	T _A = 70 °C		18.2 ^{b, c}	1 ,	
Pulsed Drain Current		I _{DM}	70	А	
Continuous Source-Drain Diode Current	T _C = 25 °C	I.	25 ^a		
	T _A = 25 °C	I _S	4.1 ^{b, c}	i	
Single Pulse Avalanche Current		I _{AS}	20		
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	20	mJ	
Maximum Power Dissipation	T _C = 25 °C		27.7		
	T _C = 70 °C	P _D	17.7	W	
	T _A = 25 °C	' Б	4.6 ^{b, c}	VV	
	T _A = 70 °C		3.0 ^{b, c}		
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 150	°C		
Soldering Recommendations (Peak Temperature)		-	260	1	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient	t ≤ 10 s	R _{thJA}	22	27	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	3.5	4.5] 5/**	

Notes:

- a. Package limited.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	$\Delta V_{DS}/T_{J}$		19		> //0C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = 250 \mu A$		- 4.0		mV/°C	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.5		2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA	
Zara Oata Wallana Bui Comi	I _{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1	—	
Zero Gate Voltage Drain Current		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 8 \text{ A}$		0.027	0.031		
Drain-Source On-State Resistance ^a		$V_{GS} = 2.5 \text{ V}, I_D = 6 \text{ A}$		0.033	0.037	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 10 A		70		S	
Dynamic ^b							
Input Capacitance	C _{iss}			1785		pF	
Output Capacitance	C _{oss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		460			
Reverse Transfer Capacitance	C _{rss}			210			
Total Gate Charge	Qg	$V_{DS} = 10 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		32		nC	
-		$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$		15.5	23.5		
Gate-Source Charge	Q_{gs}			3			
Gate-Drain Charge	Q_{gd}			3.6			
Gate Resistance	R_g	f = 1 MHz	0.2	0.75	1.5	Ω	
Turn-On Delay Time	t _{d(on)}			11	22		
Rise Time	t _r	V_{DD} = 10 V, R_L = 10 Ω		11	22	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		30	55		
Fall Time	t _f			9	18		
Turn-On Delay Time	t _{d(on)}			19	35		
Rise Time	t _r	V_{DD} = 10 V, R_L = 10 Ω		14	28		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		36	65		
Fall Time	t _f			13	26	\dashv	
Drain-Source Body Diode Characteristic	s					·	
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			25	^	
Pulse Diode Forward Current ^a	I _{SM}				70	A	
Body Diode Voltage	V _{SD}	I _S = 5 A		0.71	1.1	V	
Body Diode Reverse Recovery Time	t _{rr}	-		21	40	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			10.5	20	nC	
Reverse Recovery Fall Time	t _a	$I_F = 5 \text{ A}, \text{ dI/dt} = 100 \text{ A/µs}, T_J = 25 °C$		11			
Reverse Recovery Rise Time	t _b			10		ns	

Notes:

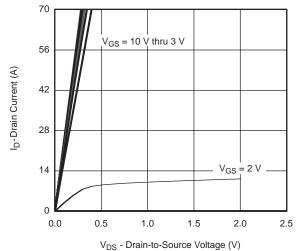
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.

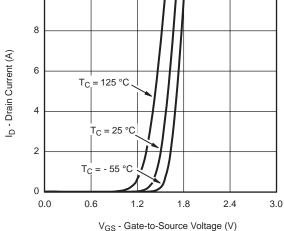
b. Guaranteed by design, not subject to production testing.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



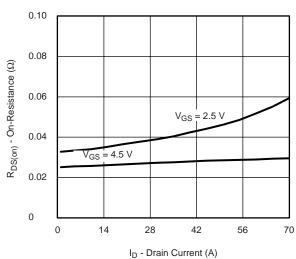
Output Characteristics



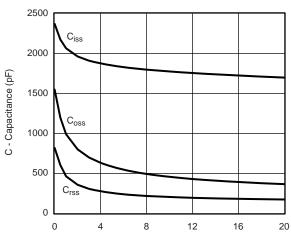
10

V_{GS} - Gate-to-Source Voltage (V) **Transfer Characteristics**

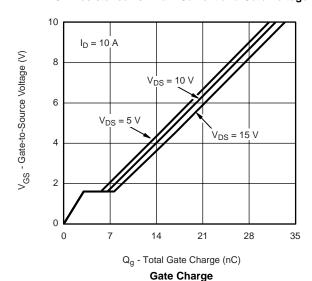


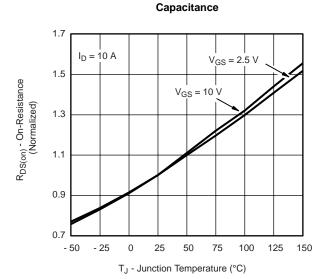


On-Resistance vs. Drain Current and Gate Voltage



V_{DS} - Drain-to-Source Voltage (V)

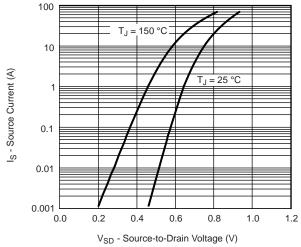




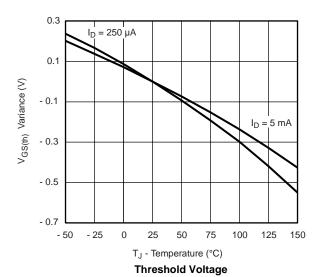
On-Resistance vs. Junction Temperature

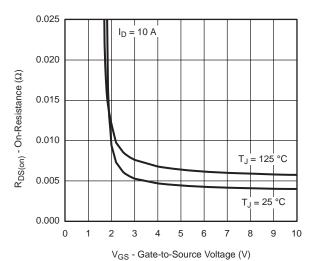


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

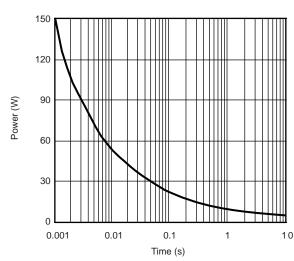


Source-Drain Diode Forward Voltage

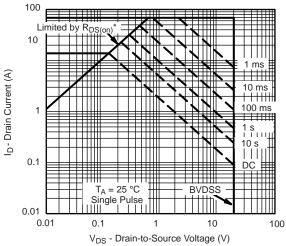




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient

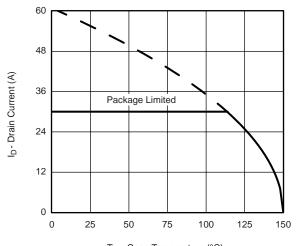


* V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

Safe Operating Area, Junction-to-Ambient

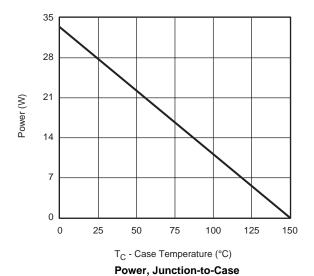


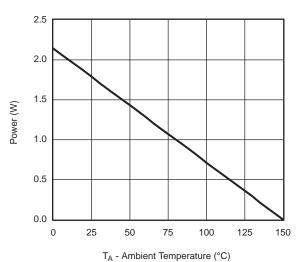
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



T_C - Case Temperature (°C)

Current Derating*



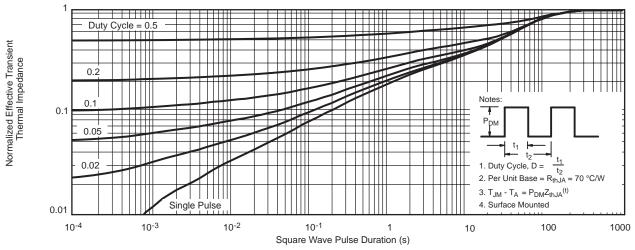


Power, Junction-to-Ambient

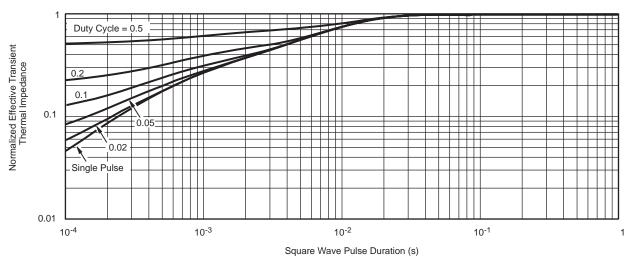
^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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