### 0) MOOFET

# N-Channel 60 V (D-S) MOSFET

I<sub>D</sub> (A)<sup>a</sup>

55

50

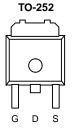
#### FEATURES

GC

- 175 °C Junction Temperature
- DT-Trench Power MOSFET

D

Material categorization:



R<sub>DS(on)</sub> (Ω)

0.0093 at  $V_{GS}$  = 10 V

0.0122 at V<sub>GS</sub> = 4.5 V

Top View

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> =	25 °C, unless othe	rwise noted)			
Parameter	Symbol	Limit	Unit V		
Gate-Source Voltage	V <sub>GS</sub>	± 20			
Continuous Drain Current (T <sub>J</sub> = 175 °C) <sup>b</sup>	T <sub>C</sub> = 25 °C	1	55		
	T <sub>C</sub> = 100 °C	I <sub>D</sub>	50 <sup>a</sup>		
Pulsed Drain Current	I <sub>DM</sub>	165	A		
Continuous Source Current (Diode Conduction)		۱ <sub>S</sub>			50 <sup>a</sup>
Avalanche Current	I <sub>AS</sub>	50			
Single Avalanche Energy (Duty Cycle $\leq$ 1 %)	L = 0.1 mH	E <sub>AS</sub>	125	mJ	
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	P	136	w	
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	3 <sup>b</sup> , 8.3 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range	·	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$t \le 10 \text{ sec}$	R <sub>thJA</sub>	15	18	°C/W
	Steady State		40	50	
Maximum Junction-to-Case		R <sub>thJC</sub>	0.85	1.1	

Notes:

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

c. t  $\leq$  10 s.





V<sub>DS</sub> (V)

60

**PRODUCT SUMMARY** 

## DTU55N06

RoHS

COMPLIANT

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N-Channel MOSFET

## DTU55N06

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<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit	
Static	Cynisor			Typ.	maxi	•••••	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 µA	60			v	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1	2	3		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$			1		
	I <sub>DSS</sub>	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 \text{ °C}$			50	μA	
		V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250	1 .	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V	50			А	
Drain-Source On-State Resistance <sup>b</sup>		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A		0.0093	0.011	Ω	
	Р	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A, T <sub>J</sub> = 125 °C		0.0103	0.0112		
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A, T <sub>J</sub> = 175 °C		0.0106	0.0127		
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 5 A		0.0122	0.0135		
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 A		60		S	
Dynamic	<u> </u>		•				
Input Capacitance	C <sub>iss</sub>			2650		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz$		470			
Reverse Transfer Capacitance	C <sub>rss</sub>			225			
Total Gate Charge <sup>c</sup>	Qg			47	70	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 30$ V, $V_{GS} = 10$ V, $I_{D} = 50$ A		10			
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			12			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			10	20	ns	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 30 \text{ V, } \text{R}_{\text{L}} = 0.6 \Omega$ $\text{I}_{\text{D}} \cong 50 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 2.5 \Omega$		15	25		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			35	50		
Fall Time <sup>c</sup>	t <sub>f</sub>			20	30		
Source-Drain Diode Ratings and Cha	aracteristics (	T <sub>C</sub> = 25 °C)					
Pulsed Current	I <sub>SM</sub>				165	А	
Diode Forward Voltage	V <sub>SD</sub>	$I_{F} = 20 \text{ A}, V_{GS} = 0 \text{ V}$		1	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs		45	100	ns	
					•		

Notes:

a. For design aid only; not subject to production testing.

b. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

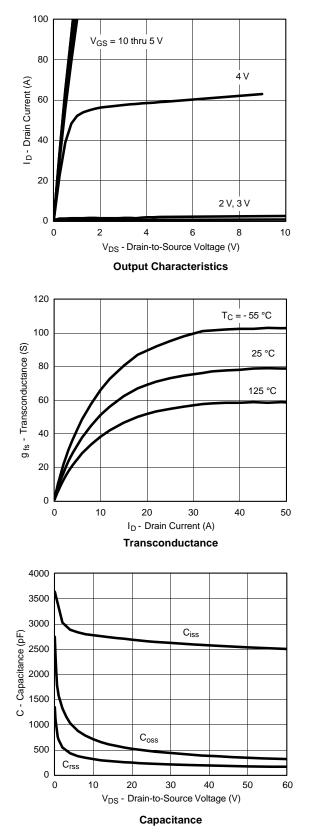
c. Independent of operating temperature.

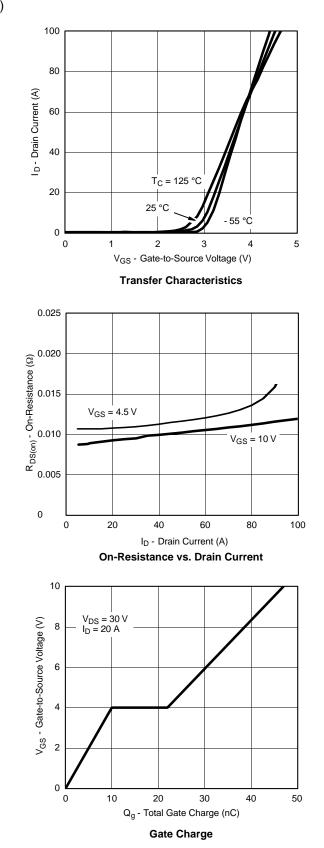
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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#### TYPICAL CHARACTERISTICS (25 °C unless noted)

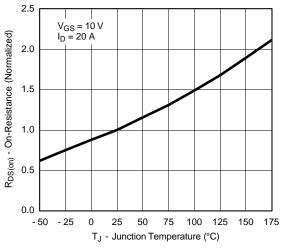




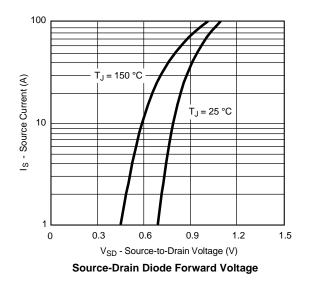


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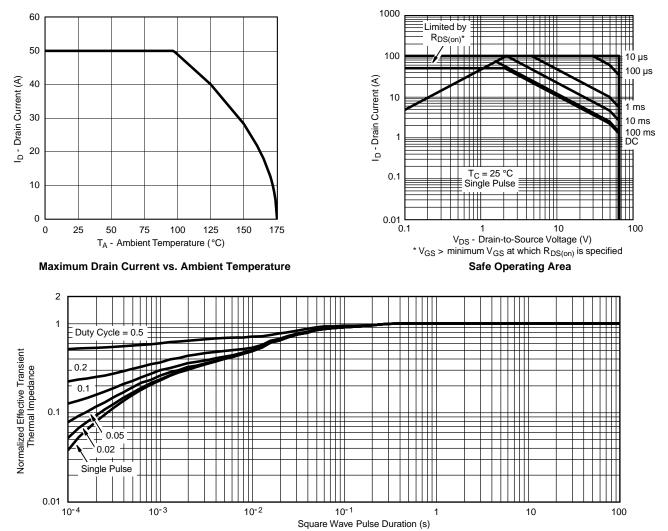
**On-Resistance vs. Junction Temperature** 





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#### THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case

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