

N-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY			
V _{(BR)DSS} (V)	$r_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	
20	0.0061 at V _{GS} = 4.5 V	60	
	0.0084 at V _{GS} = 2.5 V	30	

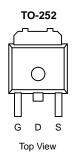
FEATURES

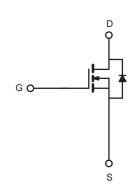
- DT-Trench Power MOSFET
- 175 °C Junction Temperature
- 100 % R_g Tested
- 100 % UIS Tested



APPLICATIONS

• OR-ing





Parameter		Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	20	V		
Gate-Source Voltage		V _{GS}	± 12	V	
Continuous Drain Current (T _J = 175 °C)	T _C = 25 °C	I-	60 ^a		
	T _C = 100 °C	I _D	40 ^a	А	
Pulsed Drain Current		I _{DM}	180	A	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	50		
Single Pulse Avalanche Energy	L = 0.1 IIII1	E _{AS}	125	mJ	
	T _C = 25 °C	D.	120 ^c	w	
Maximum Power Dissipation ^b	T _A = 25 °C ^d	P _D	3.75	VV	
Operating Junction and Storage Temperature Ra	inge	T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Limit	Unit	
Junction-to-Ambient (PCB Mount) ^d	R _{thJA}	40	°C/W	
Junction-to-Case	R _{thJC}	1.25	C/VV	

Notes:

- a. Package limited.
 b. Duty cycle ≤ 1 %.
 c. See SOA curve for voltage derating.
 d. When mounted on 1" square PCB (FR-4 material).



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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	<u> </u>				<u> </u>		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	20				
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.5		1.5	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 10	uA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ	
		V _{DS} = 20 V, V _{GS} = 0 V, T _J = 125 °C			50		
		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 \text{ °C}$			250		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	100			Α	
		$V_{GS} = 4.5 \text{ V}, I_D = 5 \text{ A}$		0.0061	0.0072		
	_	V _{GS} = 4.5 V, I _D = 10 A, T _J = 125 °C			0.008	Ω	
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 4.5 V, I _D = 10 A, T _J = 175 °C			0.009		
		V _{GS} = 2.5 V, I _D = 10 A		0.0084	0.0093		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 10 A		95		S	
Dynamic ^b	<u> </u>						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 10 V, f = 1 MHz		5950		pF	
Output Capacitance	C _{oss}			985			
Reverse Transfer Capacitance	C _{rss}			365			
Total Gate Charge ^b	Qg			33	50	nC	
Gate-Source Charge ^b	Q_{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 50 \text{ A}$		18			
Gate-Drain Charge ^b	Q _{qd}			7			
Gate Resistance	R _g		0.75	1.5	2.3	Ω	
Turn-On Delay Time ^b	t _{d(on)}			15	25	ns	
Rise Time ^b	t _r	V_{DD} = 10 V, R_L = 0.2 Ω $I_D \cong 50$ A, V_{GEN} = 4.5 V, R_g = 1.0 Ω		7	11		
Turn-Off Delay Time ^b	t _{d(off)}			35	55		
Fall Time ^b	t _f			8	12		
Source-Drain Diode Ratings and Cha	aracteristics T	= 25 °C ^c		<u> </u>			
Continuous Current	Is				60		
Pulsed Current	I _{SM}				180	Α	
Forward Voltage ^a	V _{SD}	I _F = 20 A, V _{GS} = 0 V		0.85	1.5	V	
Reverse Recovery Time	t _{rr}	1 7 65		45	90	ns	
Peak Reverse Recovery Current	I _{RM}	I _F = 20 A, di/dt = 100 A/μs		1.7	3.4	A	
Reverse Recovery Charge	Q _{rr}			0.039	0.155	μC	

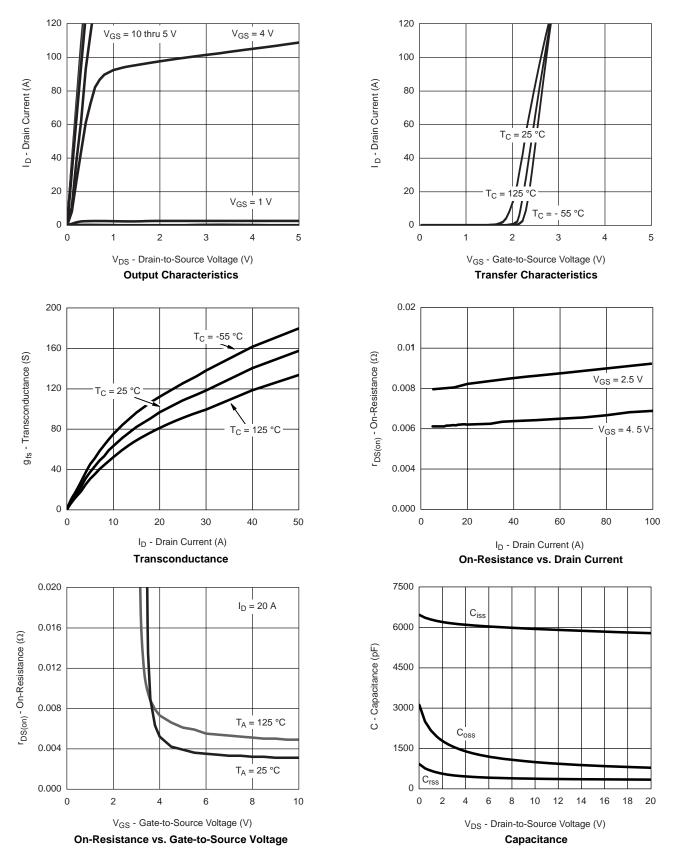
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$
- b. Independent of operating temperature.
- c. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

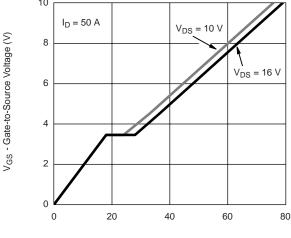


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

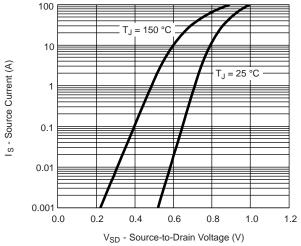


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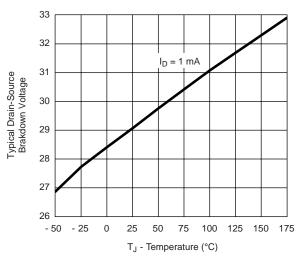
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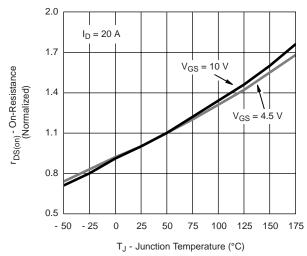




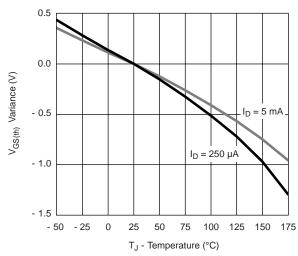
Source-Drain Diode Forward Voltage



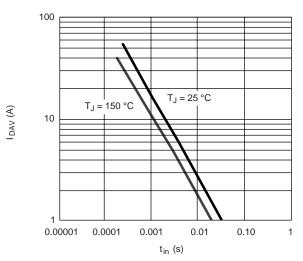
Typical Drain-Source Brakdown Voltage vs. Junction Temperature



On-Resistance vs. Junction Temperature

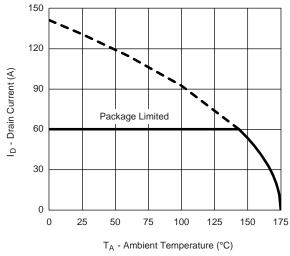


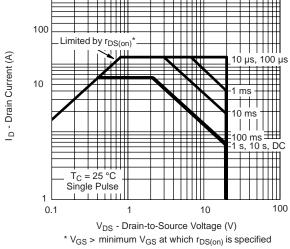
Threshold Voltage



Single Pulse Avalanche Current vs. Time

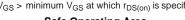
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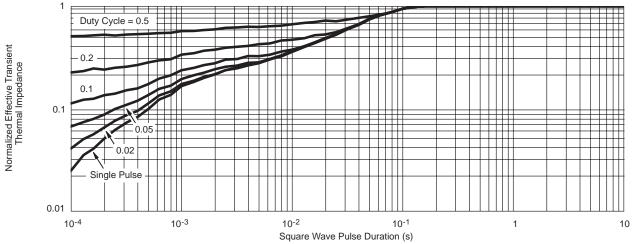


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Drain Current vs. Ambient Temperature







Normalized Thermal Transient Impedance, Junction-to-Case

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