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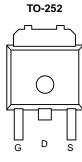
P-Channel 20 V (D-S) MOSFET

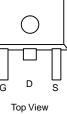
PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	
- 20	0.007 at V_{GS} = - 4.5 V	- 60	
	0.009 at V_{GS} = - 2.5 V	- 53	

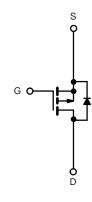
FEATURES

- DT-Trench Power MOSFET 100 % R_g Tested 100 % UIS Tested
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P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
Parameter	Symbol	Limit	Unit		
Gate-Source Voltage		V _{GS}	± 12	V	
Continuous Drain Current (T _J = 175 °C)	T _C = 25 °C		- 60 ^a		
	T _C = 125 °C	I _D	- 45	А	
Pulsed Drain Current		I _{DM}	- 220	A	
Avalanche Current		I _{AR}	- 60		
Repetitive Avalanche Energy ^b	L = 0.1 mH	E _{AR}	130	mJ	
Power Dissipation	T _C = 25 °C (TO-220AB and TO-263)	P	127 ^d	W	
	T _A = 25 °C (TO-263) ^c	P _D -	3.15	vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient	PCB Mount (TO-263) ^c	Р	35	°C/W	
	Free Air (TO-220AB)	– R _{thJA}	55		
Junction-to-Case		R _{thJC}	0.8		

Notes:

a. Package limited.

b. Duty cycle \leq 1 %.

c. When mounted on 1" square PCB (FR-4 material).

d. See SOA curve for voltage derating.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_{D} = -250 \mu A$	- 20			V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.5		- 2.5	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
		$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μΑ
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = - 16 V, V_{GS} = 0 V, T_{J} = 125 °C			- 50	
		V_{DS} = - 16 V, V_{GS} = 0 V, T_{J} = 175 °C			- 250	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -10 V$	- 60			А
Drain-Source On-State Resistance ^a		V_{GS} = - 4.5 V, I _D = - 30 A		0.007	0.0085	Ω
	Б	V_{GS} = - 4.5 V, I _D = - 30 A, T _J = 125 °C			0.010	
Diam-Source On-State Resistance	R _{DS(on)}	V_{GS} = - 4.5 V, I _D = - 30 A, T _J = 175 °C			0.013	
		$V_{GS} = -2.5 \text{ V}, \text{ I}_{D} = -20 \text{ A}$		0.009	0.0125	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 16 V, I _D = - 30 A	20			S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = - 25 V, f = 1 MHz		8400		pF
Output Capacitance	C _{oss}			1505		
Reversen Transfer Capacitance	C _{rss}			710		
Total Gate Charge ^c	Qg			160	240	nC
Gate-Source Charge ^c	Q _{gs}	V_{DS} = - 15 V, V_{GS} = - 4.5 V, I_{D} = - 30 A		32		
Gate-Drain Charge ^c	Q _{gd}			30		
Turn-On Delay Time ^c	t _{d(on)}			27		
Rise Time ^c	t _r	$V_{DD} = -15 \text{ V}, \text{ R}_{L} = 0.2 \Omega$		220		• ns
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ - 30 Å, V_{GEN} = - 4.5 V, R_g = 2.5 Ω		140		
Fall Time ^c	t _f			215		
Source-Drain Diode Ratings and Cha	racteristics ^b	(T _C = 25 °C)				
Continuous Current	۱ _S				- 60	A
Pulsed Current	I _{SM}				- 220	
Forward Voltage ^a	V _{SD}	I _F = - 30 A, V _{GS} = 0 V		- 0.8	- 1.2	V
Reverse Recovery Time	t _{rr}			55	100	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = - 30 A, dI/dt = 100 A/μs		2.5	5	А
Reverse Recovery Charge	Q _{rr}	1		0.07	0.25	μC

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

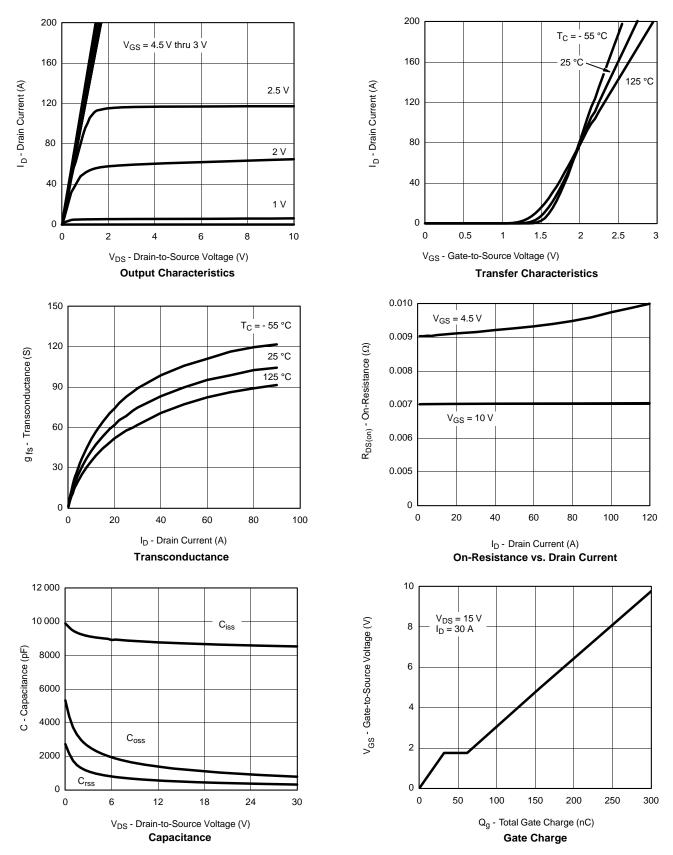
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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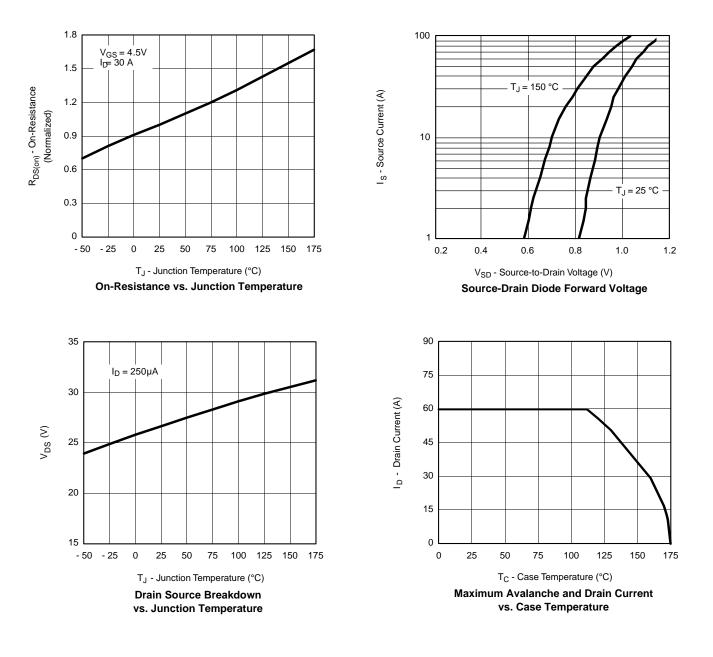
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





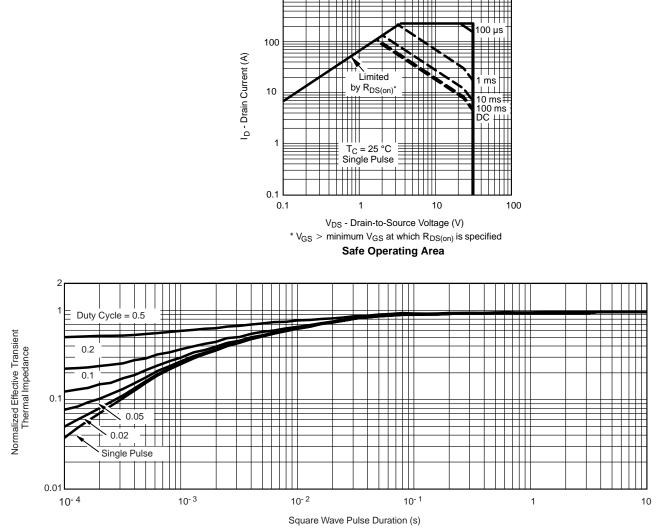
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





THERMAL RATINGS



1000

Normalized Thermal Transient Impedance, Junction-to-Case

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