

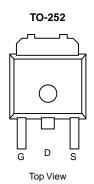
P-Channel 30 V (D-S) MOSFET

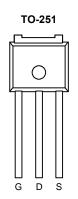
PRODUCT SUMMARY			
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	
- 30	$0.0052 \text{ at V}_{GS} = -10 \text{ V}$	-85	
- 30	0.007 at V _{GS} = - 4.5 V	-80	

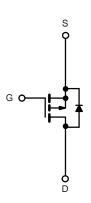
FEATURES

• Compliant to RoHS Directive 2002/95/EC









P-Channel MOSFET

ABSOLUTE MAXIMUM RAT	INGS (T _C = 25 °C, unless other	rwise noted)			
Parameter	Symbol	Limit	Unit		
Gate-Source Voltage	V_{GS}	± 20	V		
Continuous Danie Comment /T 475 000	T _C = 25 °C		- 85 ^a	_	
Continuous Drain Current (T _J = 175 °C)	T _C = 125 °C	I _D	- 68		
Pulsed Drain Current		I _{DM}	- 260	Α	
Avalanche Current	I _{AR}	- 67			
Repetitive Avalanche Energy ^b	L = 0.1 mH	E _{AR}	186	mJ	
Power Dissipation	T _C = 25 °C (TO-220AB and TO-263)	Б	187 ^d	W	
	T _A = 25 °C (TO-263) ^c	P _D	3.75	VV	
Operating Junction and Storage Temperature Range		T _J , T _{sta}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS				
Parameter		Symbol	Limit	Unit
Junction-to-Ambient	PCB Mount (TO-251) ^c	В	40	°C/W
Junction-to-Ambient	Free Air (TO-252)	R _{thJA}	62.5	
Junction-to-Case		R _{thJC}	0.8	1

Notes:

- a. Package limited.
- b. Duty cycle \leq 1 %.
- c. When mounted on 1" square PCB (FR-4 material).
- d. See SOA curve for voltage derating.

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply.

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V, } I_{D} = -250 \mu\text{A}$	- 30			V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 1		- 3	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			- 1	μΑ
	I _{DSS}	$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$			- 50	
		$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$			- 250	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 120			Α
Drain-Source On-State Resistance ^a		V _{GS} = - 10 V, I _D = - 30 A		0.0052	0.0063	Ω
	D	V _{GS} = - 10 V, I _D = - 30 A, T _J = 125 °C			0.0095	
	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 30 A, T _J = 175 °C			0.0126	
		$V_{GS} = -4.5 \text{ V}, I_D = -20 \text{ A}$		0.007	0.010	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 30 A	20			S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = -15 V, f = 1 MHz		1089		pF
Output Capacitance	C _{oss}			759		
Reversen Transfer Capacitance	C _{rss}			419		
Total Gate Charge ^c	Q_g			130	280	nC
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -30 \text{ A}$		62		
Gate-Drain Charge ^c	Q_{gd}			39		
Turn-On Delay Time ^c	t _{d(on)}			30		
Rise Time ^c	t _r	$V_{DD} = -15 \text{ V}, R_{L} = 0.2 \Omega$		268		ns
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ - 30 Å, V_{GEN} = - 10 V, R_g = 2.5 Ω		150		
Fall Time ^c	t _f			210		
Source-Drain Diode Ratings and Cha	racteristics ^b	(T _C = 25 °C)				
Continuous Current	I _S				- 85	Α
Pulsed Current	I _{SM}				- 260	А
Forward Voltage ^a	V_{SD}	I _F = -1 A, V _{GS} = 0 V		- 1.2	- 1.5	V
Reverse Recovery Time	t _{rr}			55	100	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = - 30 A, dI/dt = 100 A/μs		2.5	5	Α
Reverse Recovery Charge	Q _{rr}]		0.07	0.25	μC

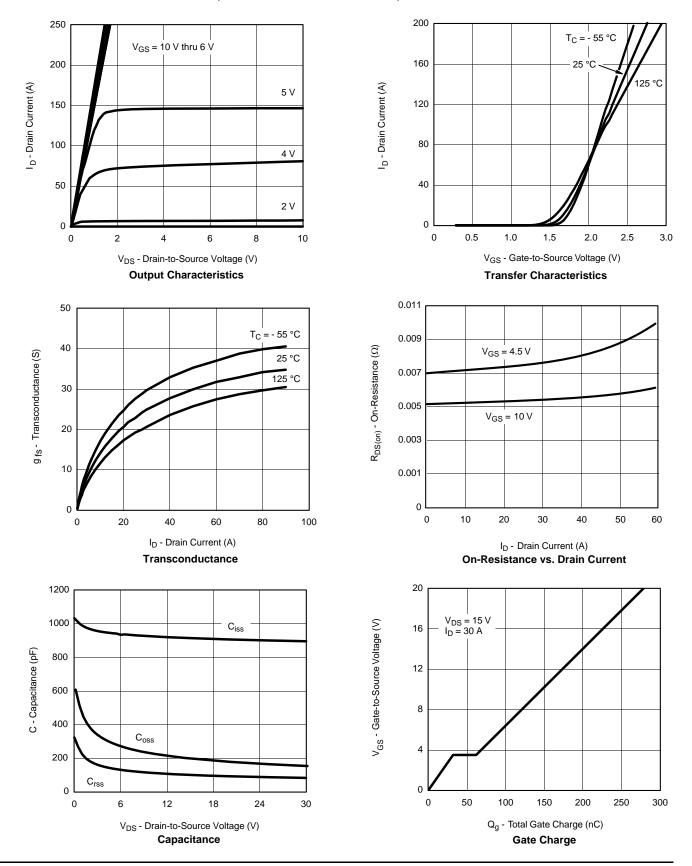
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

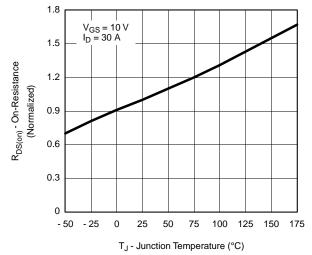
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



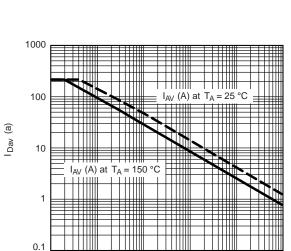
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



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On-Resistance vs. Junction Temperature



 $t_{\text{in}} \ \, (s) \\$ Avalanche Current vs. Time

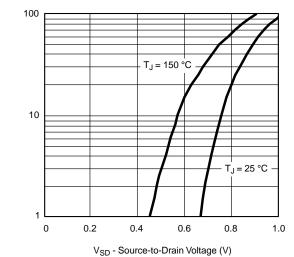
0.01

0.1

0.001

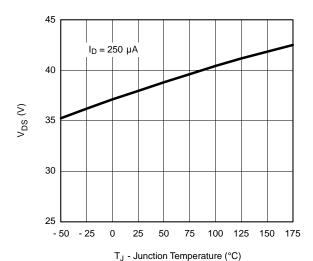
0.00001

0.0001



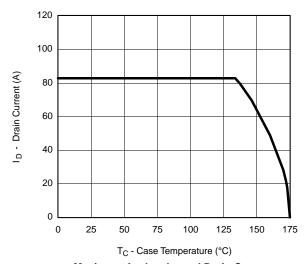
I_S - Source Current (A)

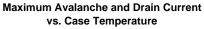
Source-Drain Diode Forward Voltage

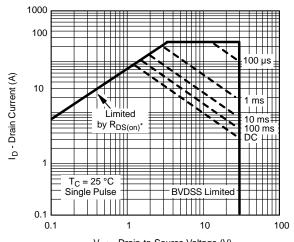


Drain Source Breakdown vs. Junction Temperature

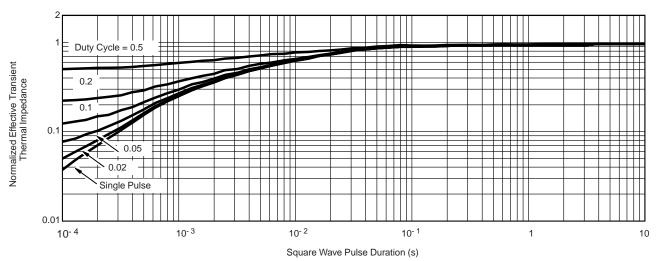
THERMAL RATINGS







$$\begin{split} &V_{DS}\text{ - Drain-to-Source Voltage (V)}\\ ^*V_{GS} > &\min\text{mum }V_{GS}\text{ at which }R_{DS(on)}\text{ is specified}\\ &\textbf{Safe Operating Area} \end{split}$$



Normalized Thermal Transient Impedance, Junction-to-Case



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