

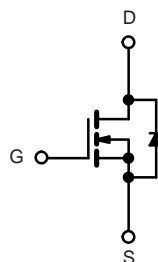
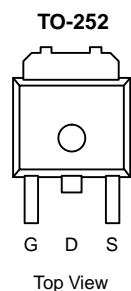
N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A) ^a
100	0.0068 at $V_{GS} = 10$ V	90
	0.0095 at $V_{GS} = 4.5$ V	80

FEATURES

- 175 °C Junction Temperature
- TrenchFET® Power MOSFET
- Material categorization:


RoHS
 COMPLIANT


N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

Parameter		Symbol	Limit	Unit
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ($T_J = 175$ °C) ^b	$T_C = 25$ °C	I_D	90	A
	$T_C = 100$ °C		80 ^a	
Pulsed Drain Current		I_{DM}	330	
Continuous Source Current (Diode Conduction)		I_S	80 ^a	
Avalanche Current		I_{AS}	80	
Single Avalanche Energy (Duty Cycle ≤ 1 %)	$L = 0.1$ mH	E_{AS}	125	mJ
Maximum Power Dissipation	$T_C = 25$ °C	P_D	136	W
	$T_A = 25$ °C		3 ^b , 8.3 ^{b, c}	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 10$ sec	R_{thJA}	15	18	°C/W
	Steady State		40	50	
Maximum Junction-to-Case		R_{thJC}	0.85	1.1	

Notes:

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

 c. $t \leq 10$ s.

SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)

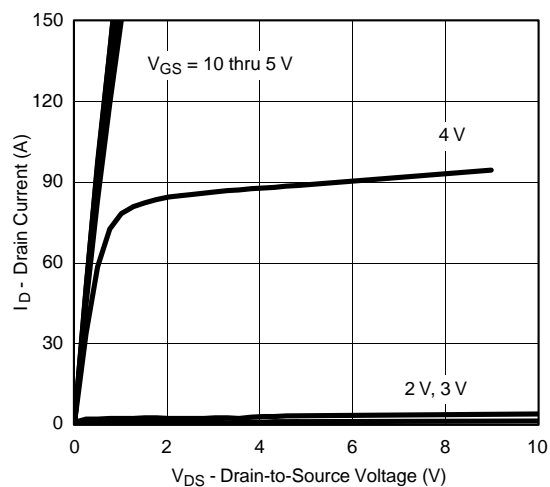
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	100			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1	2	3	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V			1	μA
		V _{DS} = 60 V, V _{GS} = 0 V, T _J = 125 °C			50	
		V _{DS} = 60 V, V _{GS} = 0 V, T _J = 175 °C			250	
On-State Drain Current ^b	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	90			A
Drain-Source On-State Resistance ^b	R _{DS(on)}	V _{GS} = 10 V, I _D = 20 A		0.0068	0.0085	Ω
		V _{GS} = 10 V, I _D = 20 A, T _J = 125 °C			0.0140	
		V _{GS} = 10 V, I _D = 20 A, T _J = 175 °C			0.0180	
		V _{GS} = 4.5 V, I _D = 15 A		0.0095	0.0120	
Forward Transconductance ^b	g _{fs}	V _{DS} = 15 V, I _D = 20 A		50		S
Dynamic						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		4150		pF
Output Capacitance	C _{oss}			420		
Reverse Transfer Capacitance	C _{rss}			35		
Total Gate Charge ^c	Q _g	V _{DS} = 30 V, V _{GS} = 10 V, I _D = 50 A		47	70	nC
Gate-Source Charge ^c	Q _{gs}			15		
Gate-Drain Charge ^c	Q _{gd}			10		
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 30 V, R _L = 0.6 Ω I _D ≅ 50 A, V _{GEN} = 10 V, R _g = 2.5 Ω		10	20	ns
Rise Time ^c	t _r			10	20	
Turn-Off Delay Time ^c	t _{d(off)}			35	50	
Fall Time ^c	t _f			6	10	
Source-Drain Diode Ratings and Characteristics (T _C = 25 °C)						
Pulsed Current	I _{SM}				320	A
Diode Forward Voltage	V _{SD}	I _F = 20 A, V _{GS} = 0 V		1	1.5	V
Reverse Recovery Time	t _{rr}	I _F = 20 A, di/dt = 100 A/μs		85	110	ns

Notes:

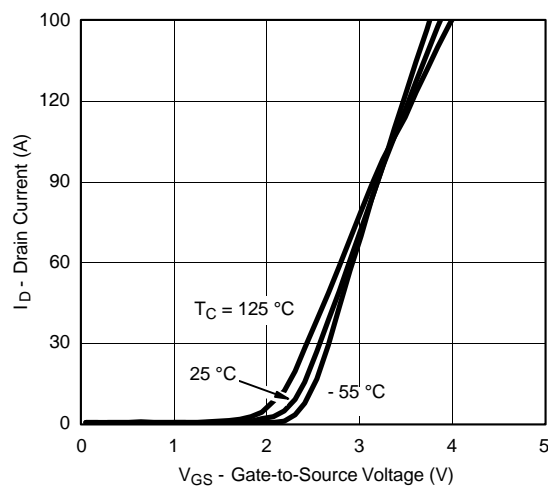
- a. For design aid only; not subject to production testing.
 b. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

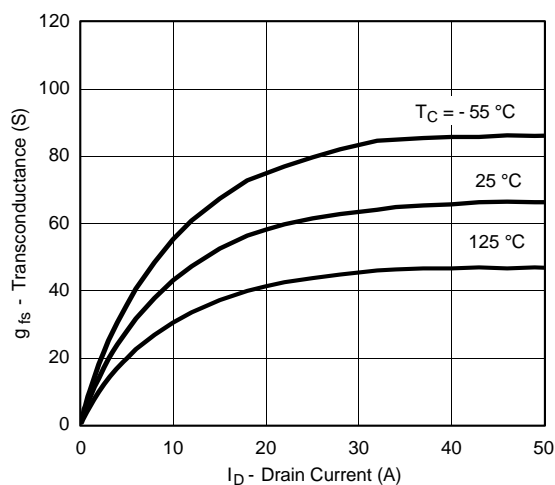
TYPICAL CHARACTERISTICS (25 °C unless noted)



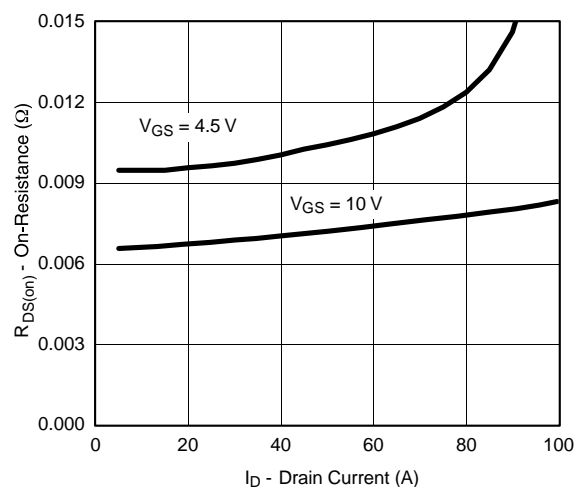
Output Characteristics



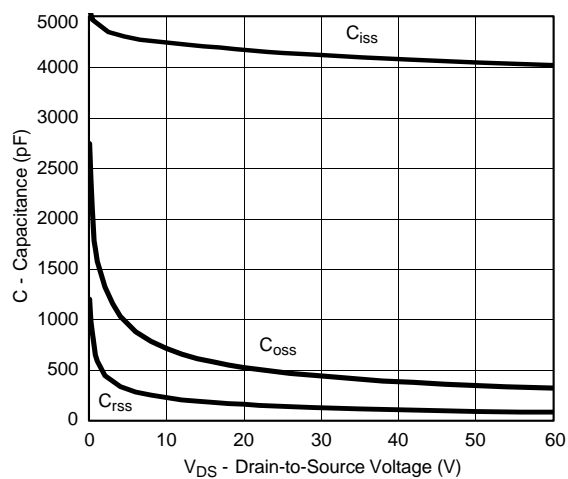
Transfer Characteristics



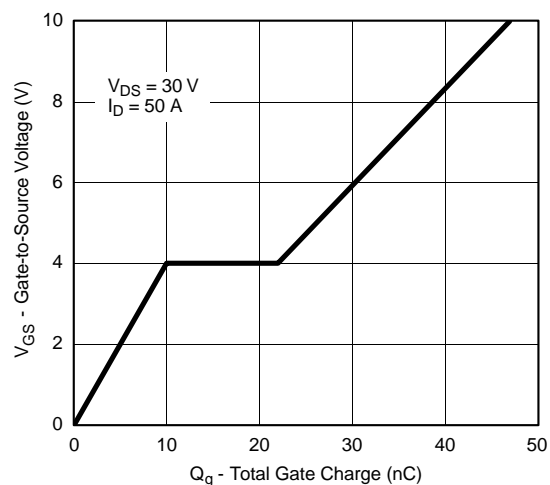
Transconductance



On-Resistance vs. Drain Current

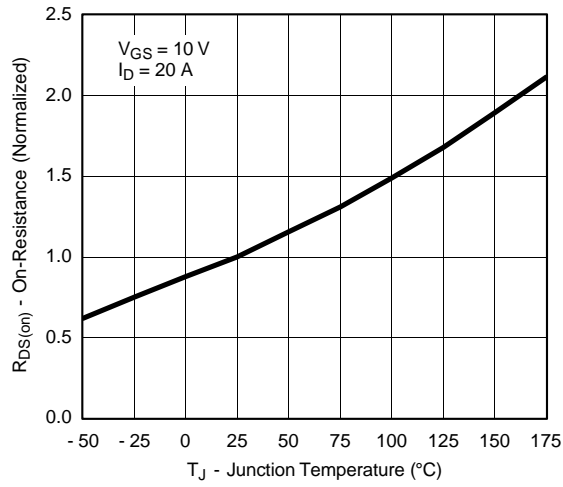


Capacitance

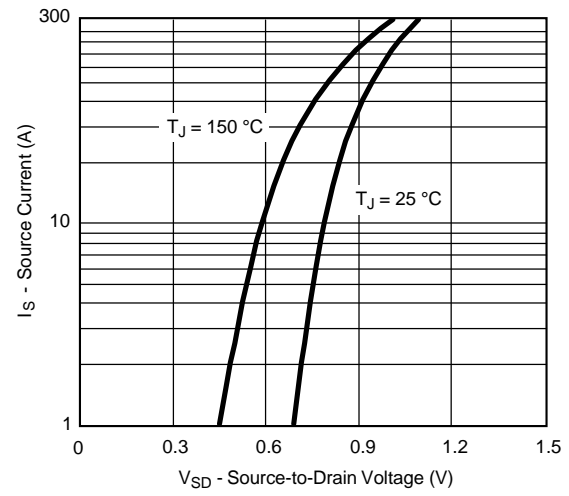


Gate Charge

TYPICAL CHARACTERISTICS (25 °C unless noted)

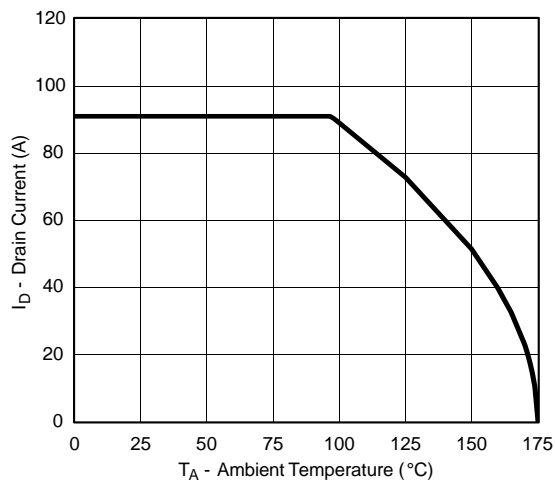


On-Resistance vs. Junction Temperature

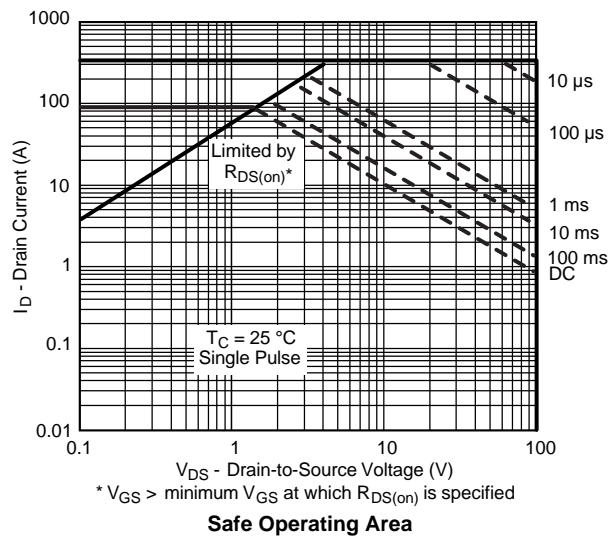


Source-Drain Diode Forward Voltage

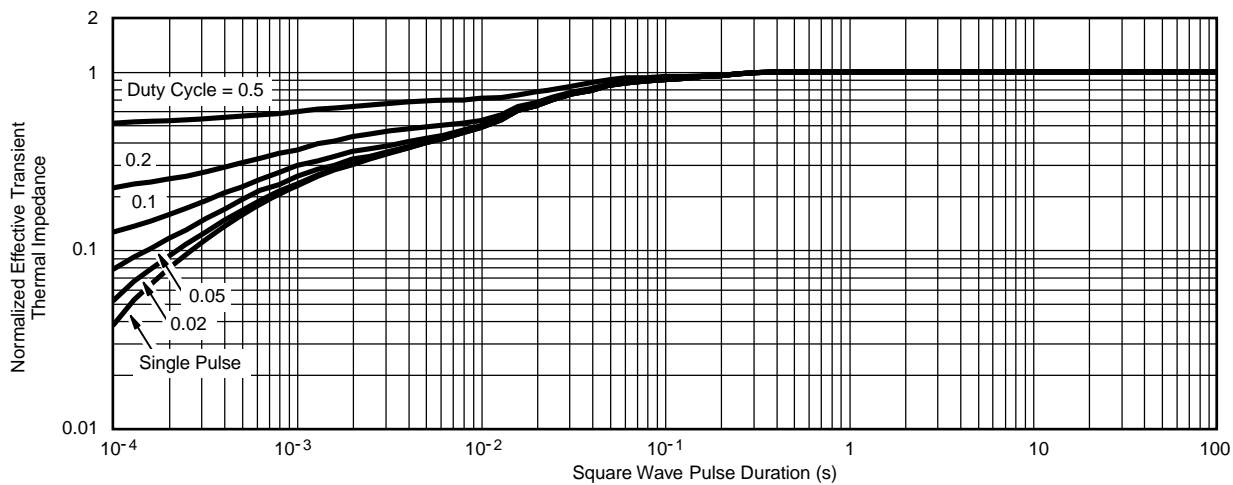
THERMAL RATINGS



Maximum Drain Current vs. Ambient Temperature



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

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