

N- and P-Channel 20 V (D-S) MOSFET



PRODUCT SUMMARY				
	V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)
N-Channel	20	0.028 at V _{GS} = 4.5 V	6.5	9.5
		0.036 at V _{GS} = 2.5 V	5.0	
P-Channel	- 20	0.072 at V _{GS} = - 4.5 V	- 3.8	8.8
		0.099 at V _{GS} = - 2.5 V	- 3.0	

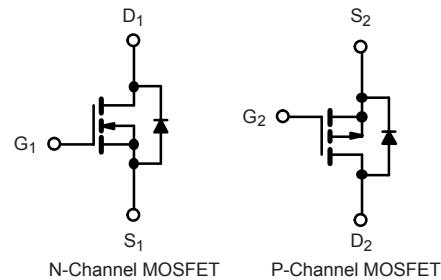
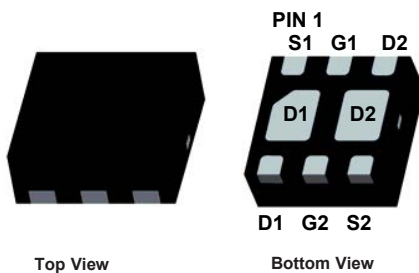
FEATURES

- DT-Trench Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- 1-2 Cell Battery Protection Circuitry
- DC/DC 'Switching' MOSFET in cellular handset and other ultra-portable applications

DFN 2x2-6L-U



ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted

Parameter	Symbol	N-Channel	P-Channel	Unit	
Drain-Source Voltage	V _{DS}	20	- 20	V	
Gate-Source Voltage	V _{GS}	± 12			
Continuous Drain Current (T _J = 150 °C)	I _D	T _C = 25 °C	6.5	- 3.8	A
		T _C = 70 °C	5.0	- 2.5	
		T _A = 25 °C	5.2 ^{b, c}	- 2.6 ^{b, c}	
		T _A = 70 °C	4.0 ^{b, c}	- 1.5 ^{b, c}	
Pulsed Drain Current	I _{DM}	25	- 15	A	
Source-Drain Current Diode Current	I _S	T _C = 25 °C	2.5		- 2.5
		T _A = 25 °C	1.6 ^{b, c}	- 1.6 ^{b, c}	
Pulsed Source-Drain Current	I _{SM}	20	- 12	A	
Single Pulse Avalanche Current	I _{AS}	6.5	-3.8		
Single Pulse Avalanche Energy	E _{AS}	2.45	1.5	mJ	
Maximum Power Dissipation	P _D	T _C = 25 °C	1.9	1.4	W
		T _C = 70 °C	0.9	0.7	
		T _A = 25 °C	1.1 ^{b, c}	0.8 ^{b, c}	
		T _A = 70 °C	0.65 ^{b, c}	0.45 ^{b, c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS

Parameter	Symbol	N-Channel		P-Channel		Unit
		Typ.	Max.	Typ.	Max.	
Maximum Junction-to-Ambient ^{b, d}	R _{thJA}	70	84	110	160	°C/W
Maximum Junction-to-Foot (Drain)	R _{thJF}	53	70	90	140	

Notes:

- Based on T_C = 25 °C.
- Surface Mounted on 1" x 1" FR4 board.
- t = 10 s.
- Maximum under Steady State conditions is 120 °C/W.

SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	N-Ch	20		V	
		$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	P-Ch	-20			
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$	N-Ch		15	mV/ $^\circ\text{C}$	
		$I_D = -250\text{ }\mu\text{A}$	P-Ch		-12		
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = 250\text{ }\mu\text{A}$	N-Ch		4		
		$I_D = -250\text{ }\mu\text{A}$	P-Ch		-4.6		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	N-Ch	0.6		1.5	V
		$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	P-Ch	-0.6		-1.5	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 12\text{ V}$	N-Ch			10	nA
			P-Ch			-10	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 16\text{ V}, V_{GS} = 0\text{ V}$	N-Ch			1	μA
		$V_{DS} = -16\text{ V}, V_{GS} = 0\text{ V}$	P-Ch			-1	
		$V_{DS} = 16\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$	N-Ch			10	
		$V_{DS} = -16\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$	P-Ch			-10	
On-State Drain Current ^b	$I_{D(on)}$	$V_{DS} = 5\text{ V}, V_{GS} = 10\text{ V}$	N-Ch	25		A	
		$V_{DS} = -5\text{ V}, V_{GS} = -10\text{ V}$	P-Ch	-15			
Drain-Source On-State Resistance ^b	$R_{DS(on)}$	$V_{GS} = 4.5\text{ V}, I_D = 3.5\text{ A}$	N-Ch		0.028	0.035	Ω
		$V_{GS} = -4.5\text{ V}, I_D = -3\text{ A}$	P-Ch		0.072	0.080	
		$V_{GS} = 2.5\text{ V}, I_D = 2.5\text{ A}$	N-Ch		0.036	0.040	
		$V_{GS} = -2.5\text{ V}, I_D = -2\text{ A}$	P-Ch		0.099	0.110	
Forward Transconductance ^b	g_{fs}	$V_{DS} = 16\text{ V}, I_D = 3.5\text{ A}$	N-Ch		18	S	
		$V_{DS} = -16\text{ V}, I_D = -3\text{ A}$	P-Ch		12		
Dynamic^a							
Input Capacitance	C_{iss}	N-Channel $V_{DS} = 16\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$ P-Channel $V_{DS} = -16\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	N-Ch		1040	pF	
Output Capacitance	C_{oss}		N-Ch		240		
			P-Ch		120		
Reverse Transfer Capacitance	C_{rss}		N-Ch		200		
Total Gate Charge	Q_g	$V_{DS} = 16\text{ V}, V_{GS} = 4.5\text{ V}, I_D = 3.5\text{ A}$	N-Ch		10	nC	
		$V_{DS} = -16\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -3\text{ A}$	P-Ch		15		
Gate-Source Charge	Q_{gs}	N-Channel $V_{DS} = 16\text{ V}, V_{GS} = 2.5\text{ V}, I_D = 2\text{ A}$	N-Ch		5.3		
			P-Ch		11.8		
			N-Ch		1.9		
			P-Ch		3.0		
Gate-Drain Charge	Q_{gd}	P-Channel $V_{DS} = -16\text{ V}, V_{GS} = -2.5\text{ V}, I_D = -2\text{ A}$	N-Ch		1.7		
			P-Ch		5.2		
Gate Resistance	R_g	$f = 1\text{ MHz}$	N-Ch		2.2	Ω	
			P-Ch		5.5		

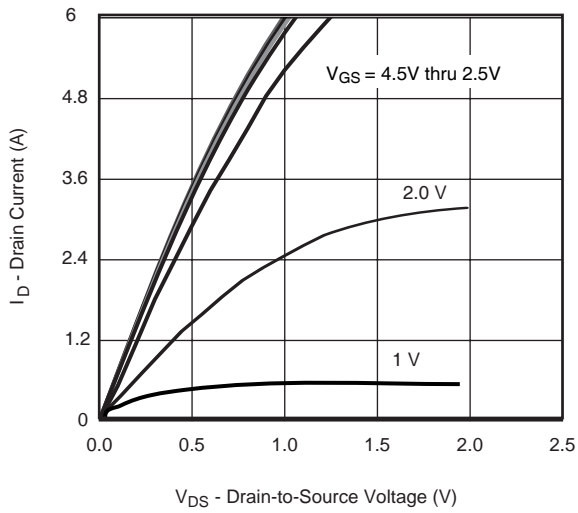
SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Dynamic^a							
Turn-On Delay Time	$t_{d(on)}$	N-Channel $V_{DD} = 16\text{ V}, R_L = 4\ \Omega$ $I_D \cong 3.5\text{ A}, V_{GEN} = 4.5\text{ V}, R_g = 1\ \Omega$ P-Channel $V_{DD} = -16\text{ V}, R_L = 4\ \Omega$ $I_D \cong -3\text{ A}, V_{GEN} = -4.5\text{ V}, R_g = 1\ \Omega$	N-Ch		7	14	ns
			P-Ch		7	14	
Rise Time	t_r		N-Ch		10	20	
			P-Ch		12	24	
Turn-Off Delay Time	$t_{d(off)}$		N-Ch		15	30	
			P-Ch		35	65	
Fall Time	t_f		N-Ch		10	18	
			P-Ch		10	18	
Turn-On Delay Time	$t_{d(on)}$	N-Ch		16	30		
		P-Ch		44	80		
Rise Time	t_r	N-Ch		17	30		
		P-Ch		33	50		
Turn-Off Delay Time	$t_{d(off)}$	N-Ch		16	30		
		P-Ch		28	60		
Fall Time	t_f	N-Ch		10	20		
		P-Ch		13	25		
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$	N-Ch			2.5	A
			P-Ch			-2.5	
Pulse Diode Forward Current ^a	I_{SM}		N-Ch			20	
			P-Ch			-12	
Body Diode Voltage	V_{SD}	$I_S = 1.6\text{ A}$ $I_S = -1.6\text{ A}$	N-Ch		0.7	1.2	V
			P-Ch		-0.7	-1.2	
Body Diode Reverse Recovery Time	t_{rr}	N-Channel $I_F = 2\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$ P-Channel $I_F = -2\text{ A}, di/dt = -100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$	N-Ch		20	30	ns
			P-Ch		26	55	
Body Diode Reverse Recovery Charge	Q_{rr}		N-Ch		14	25	nC
			P-Ch		18.5	35	
Reverse Recovery Fall Time	t_a		N-Ch		13		ns
			P-Ch		12.5		
Reverse Recovery Rise Time	t_b		N-Ch		6		
			P-Ch		13.5		

Notes:

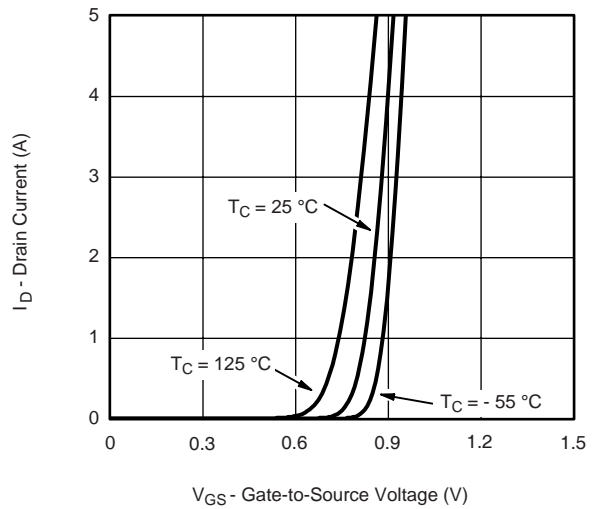
- a. Pulse test; pulse width $\leq 300\ \mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

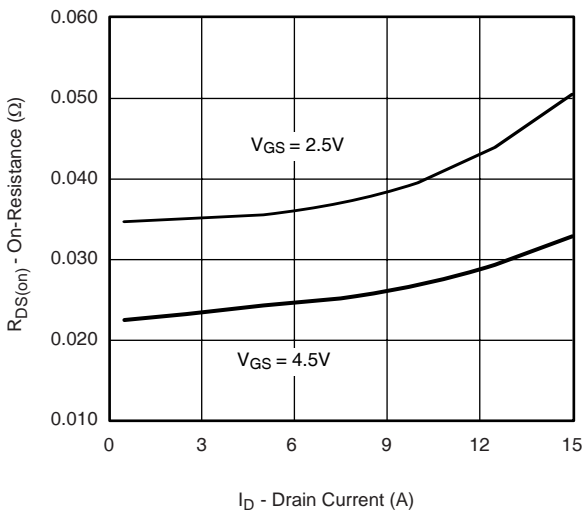
N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



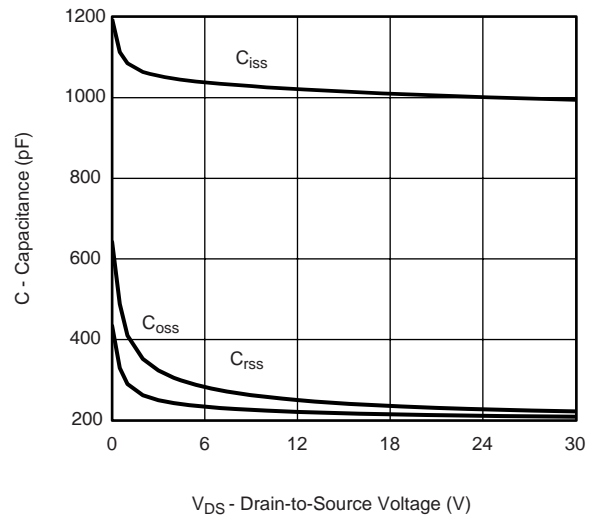
V_{DS} - Drain-to-Source Voltage (V)
Output Characteristics



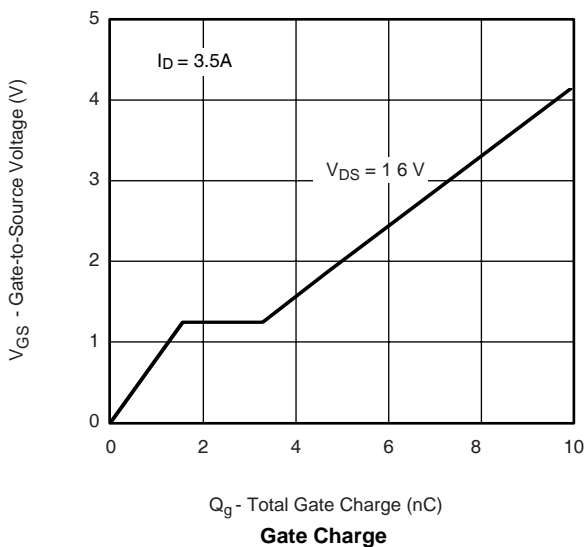
V_{GS} - Gate-to-Source Voltage (V)
Transfer Characteristics



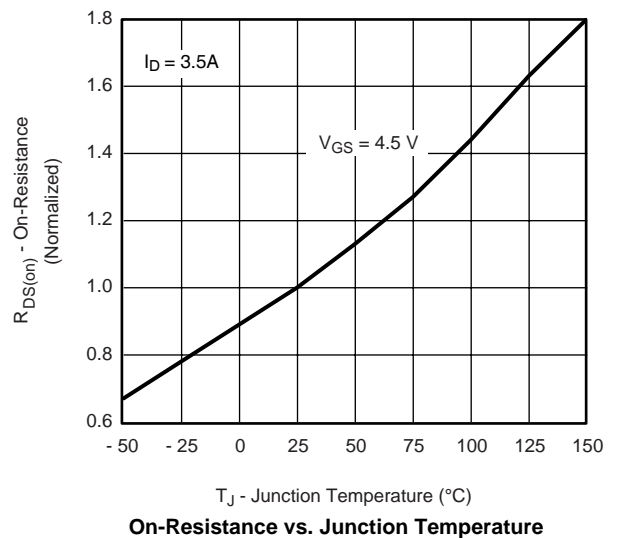
I_D - Drain Current (A)
On-Resistance vs. Drain Current



V_{DS} - Drain-to-Source Voltage (V)
Capacitance

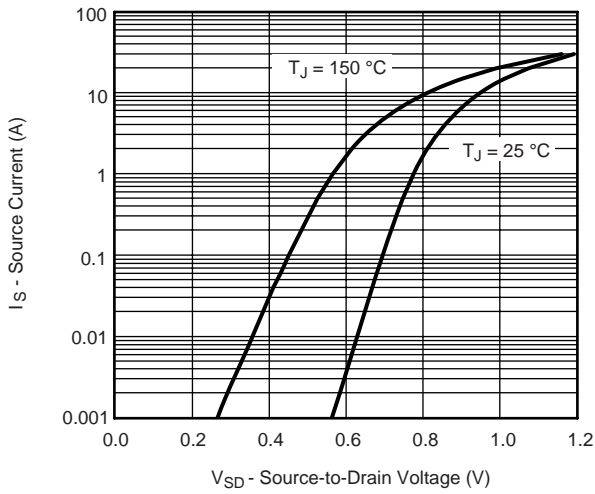


Q_G - Total Gate Charge (nC)
Gate Charge

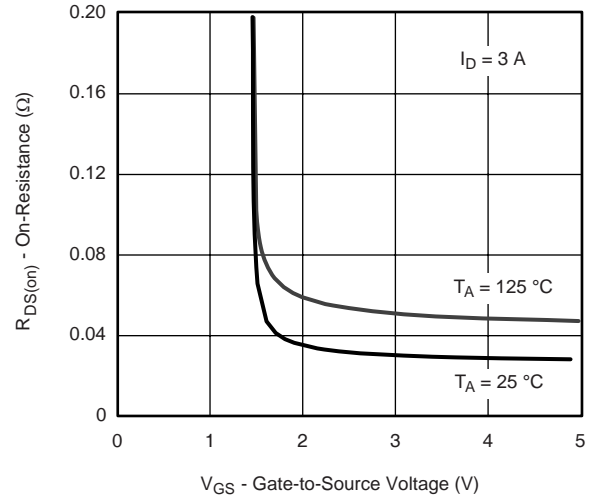


T_J - Junction Temperature ($^\circ\text{C}$)
On-Resistance vs. Junction Temperature

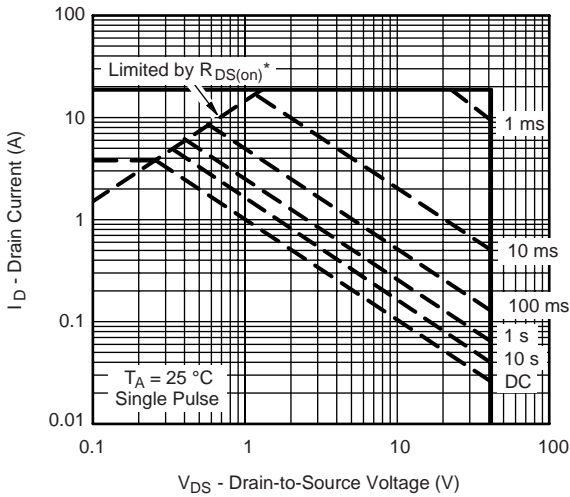
N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



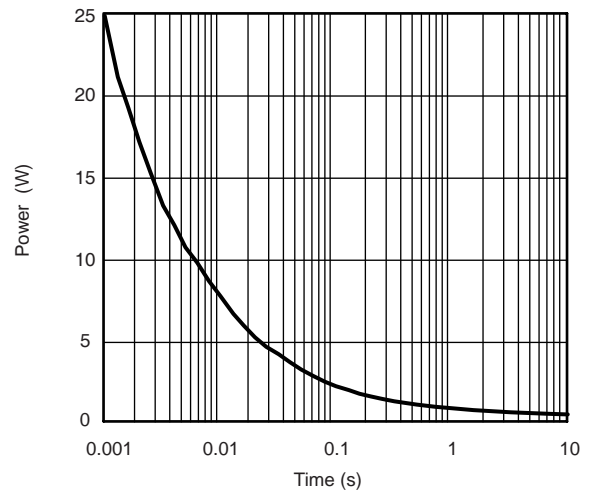
Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



Safe Operating Area, Junction-to-Ambient
* $V_{GS} >$ minimum V_{GS} at which $r_{DS(on)}$ is specified



Single Pulse Power, Junction-to-Ambient

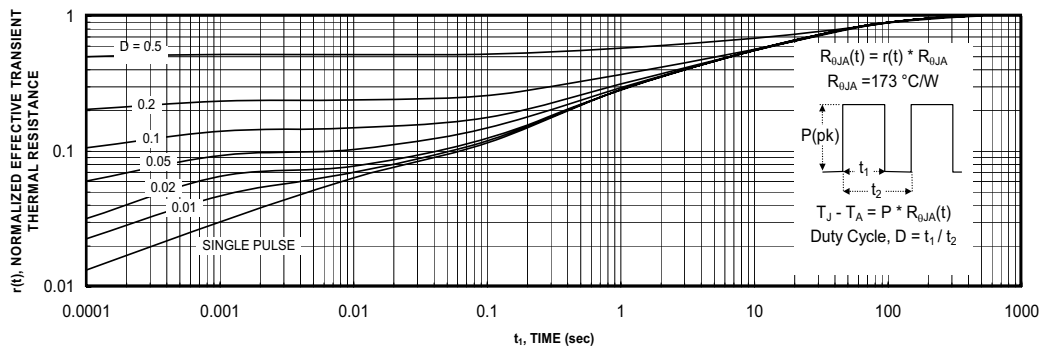
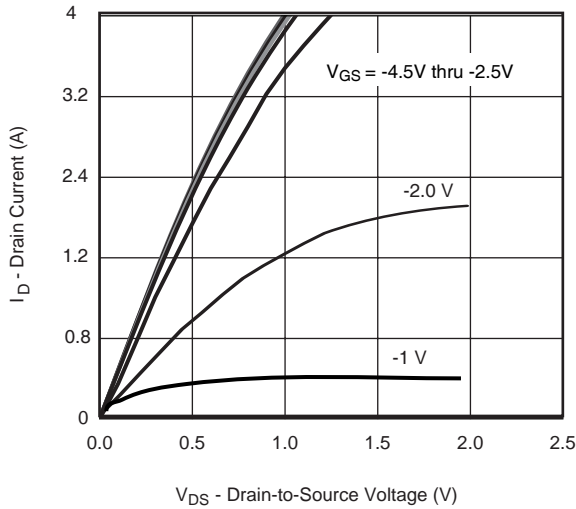


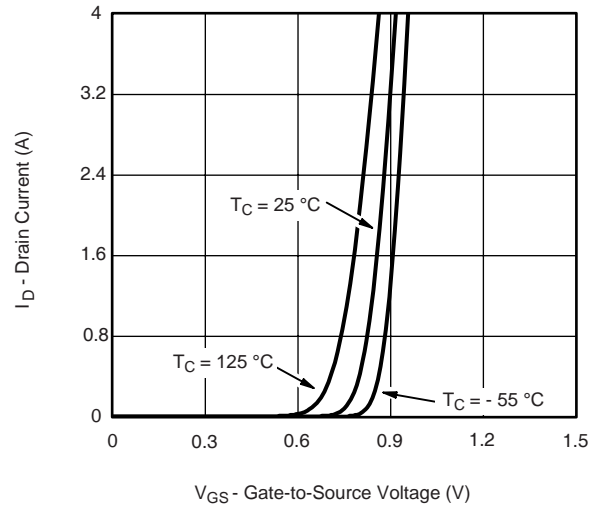
Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1b. Transient thermal response will change depending on the circuit board design.

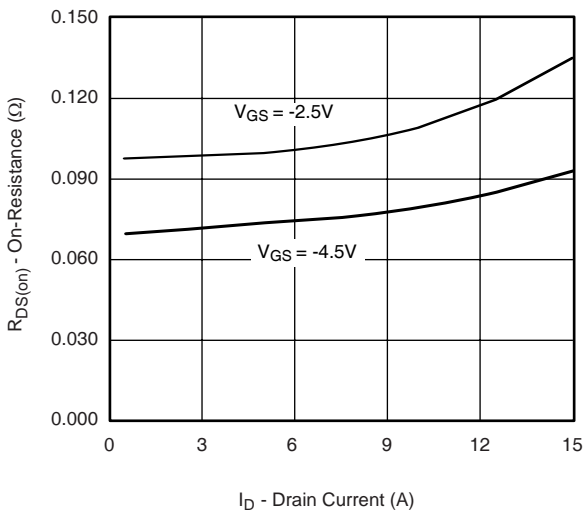
P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



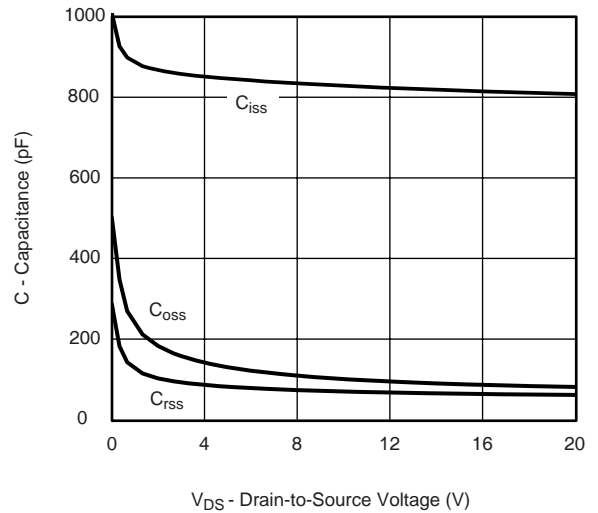
V_{DS} - Drain-to-Source Voltage (V)
Output Characteristics



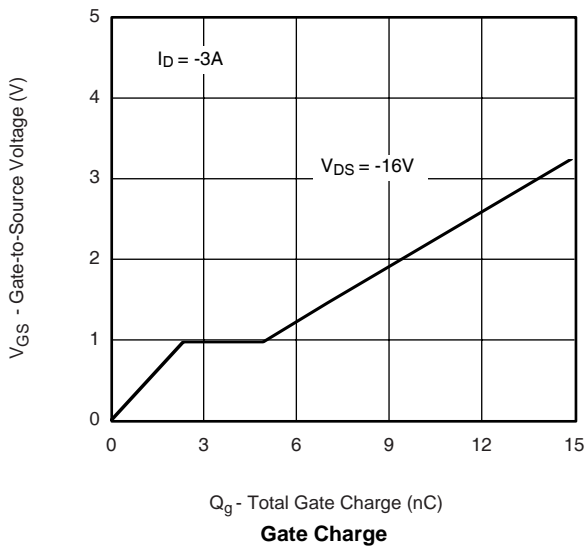
V_{GS} - Gate-to-Source Voltage (V)
Transfer Characteristics



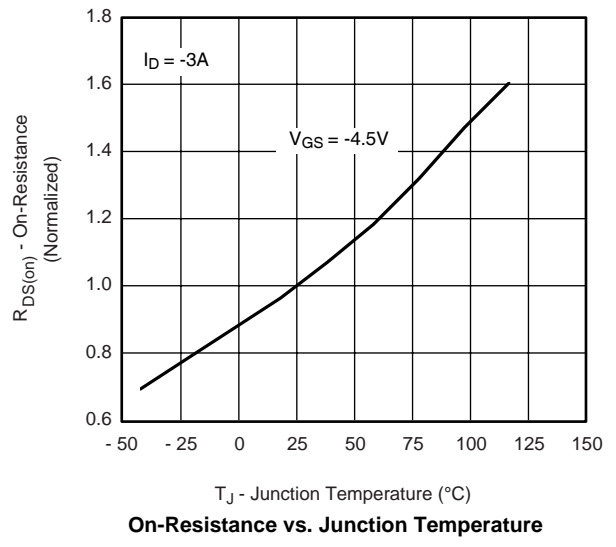
I_D - Drain Current (A)
On-Resistance vs. Drain Current



V_{DS} - Drain-to-Source Voltage (V)
Capacitance



Q_g - Total Gate Charge (nC)
Gate Charge



T_J - Junction Temperature ($^\circ\text{C}$)
On-Resistance vs. Junction Temperature

P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

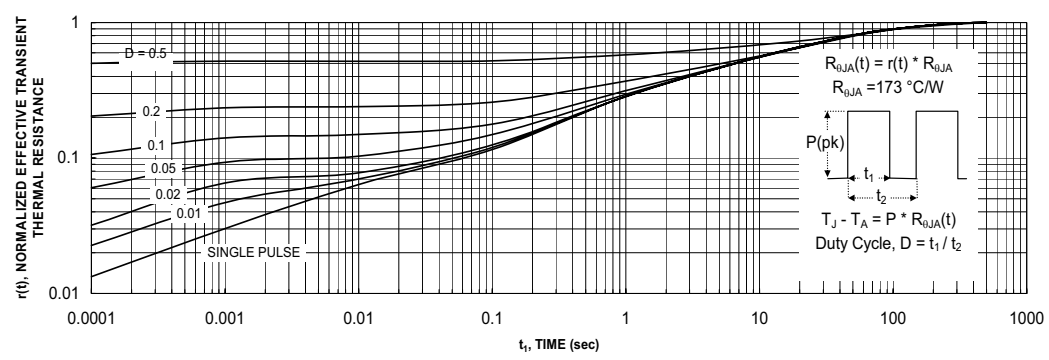
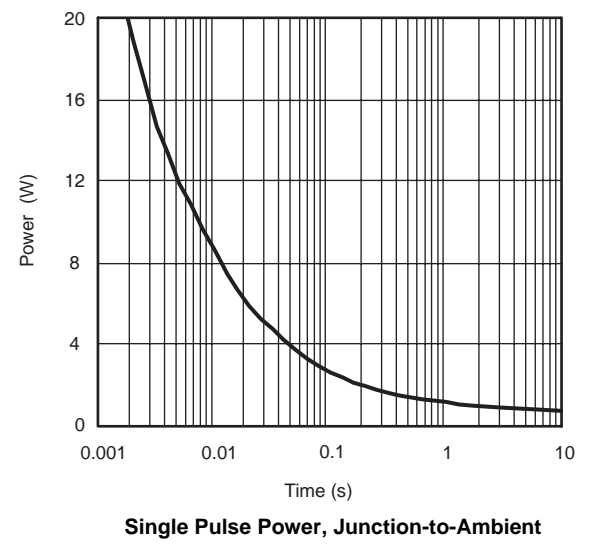
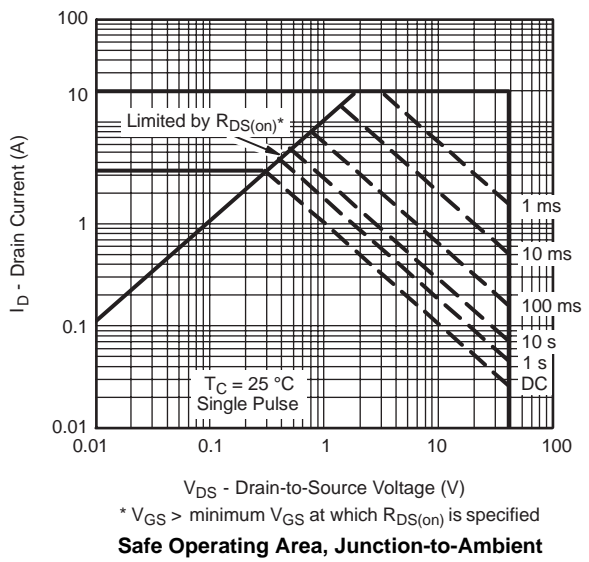
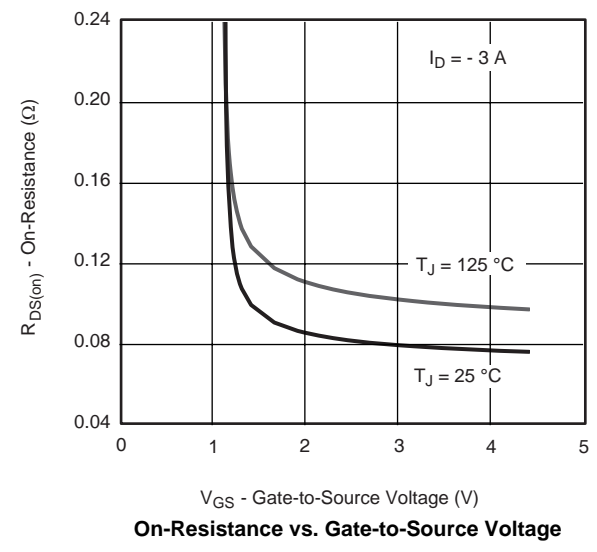
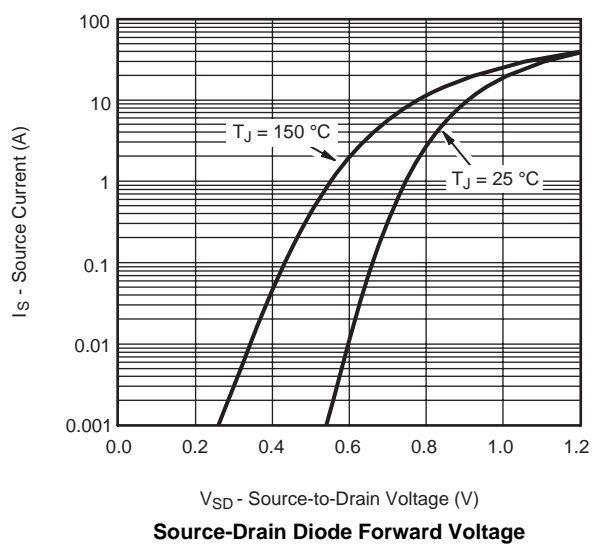
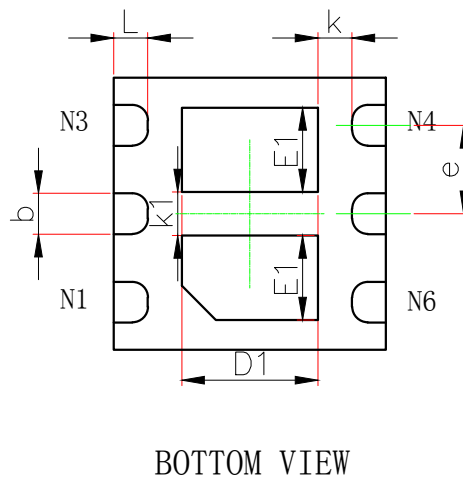
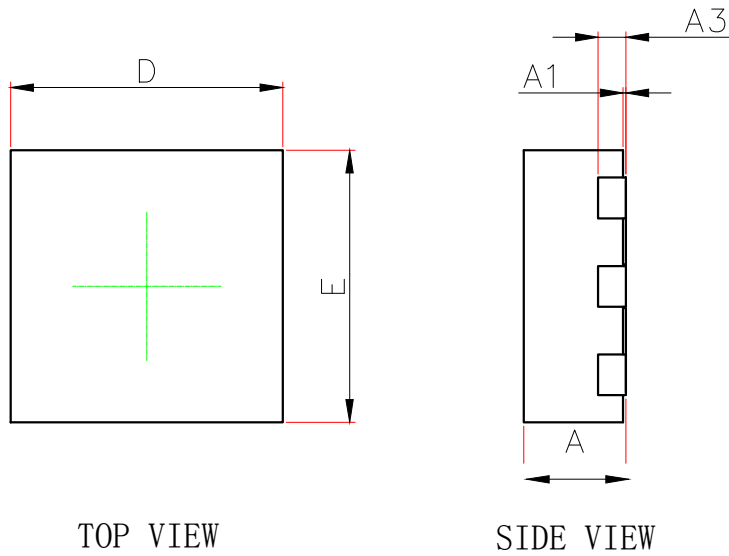


Figure 22. Transient Thermal Response Curve.
Thermal characterization performed using the conditions described in Note 1c.
Transient thermal response will change depending on the circuit board design.



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN.	MAX.	MIN.	MAX.
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A3	0.203REF.		0.008REF.	
D	1.900	2.100	0.075	0.083
E	1.900	2.100	0.075	0.083
D1	0.900	1.100	0.035	0.043
E1	0.520	0.720	0.020	0.028
b	0.250	0.350	0.010	0.014
e	0.650TYP.		0.026TYP.	
k	0.200MIN.		0.008MIN.	
k1	0.320REF.		0.013REF.	
L	0.200	0.300	0.008	0.012

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