

N-Channel 100 V (D-S) MOSFET



RoHS
COMPLIANT

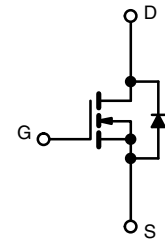
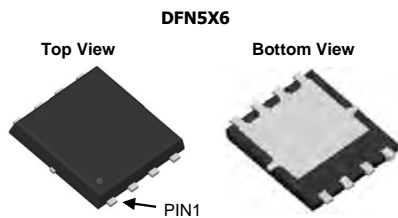
PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^{a, d}	Q _g (Typ.)
100	0.0051 at V _{GS} = 10 V	115	46nC
	0.0070 at V _{GS} = 4.5 V	90	

FEATURES

- TrenchFET IIPower MOSFET
- 100 % Rgand UIS Tested

APPLICATIONS

- Synchronous rectification
- Primary side switch
- DC/DC converters
- OR-ing
- Power supplies
- Motor drive control
- Battery and load switch



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	V _{DS}	100	V
Gate-source voltage	V _{GS}	± 20	V
Continuous drain current (T _J = 150 °C)	I _D	T _C = 25 °C	115 ^a
		T _C = 70 °C	94.2
		T _A = 25 °C	45.7 ^{b, c}
		T _A = 70 °C	21.9 ^{b, c}
Pulsed drain current (t = 100 μs)	I _{DM}	460	A
Continuous source-drain diode current	I _S	T _C = 25 °C	115 ^a
		T _A = 25 °C	9.1 ^{b, c}
Single pulse avalanche current	I _{AS}	103	mJ
Single pulse avalanche energy	E _{AS}	158	
Maximum power dissipation	P _D	T _C = 25 °C	203
		T _C = 70 °C	121
		T _A = 25 °C	6.65 ^{b, c}
		T _A = 70 °C	3.7 ^{b, c}
Operating junction and storage temperature range	T _J , T _{stg}	-55 to +150	°C
Soldering recommendations (peak temperature) ^c		260	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient ^b	R _{thJA}	13	22	°C/W
Maximum junction-to-case (drain)	R _{thJC}	0.7	1	
Maximum junction-to-case (source)	R _{thJC}	1.0	1.4	

Notes:

- Based on T_C = 25 °C.
- Surface mounted on 1" x 1" FR4 board.
- t = 10 s.
- Calculated based on maximum junction temperature.

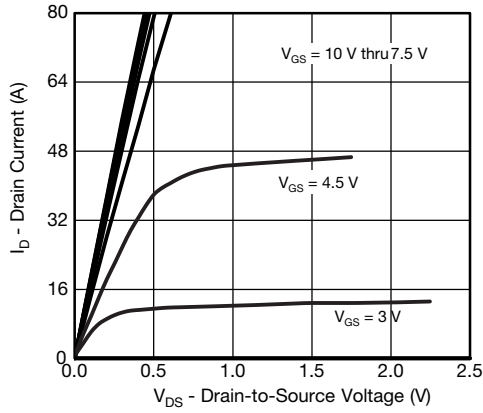
SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	100	-	-	V
V_{DS} temperature coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$	-	56	-	mV/ $^\circ\text{C}$
$V_{GS(th)}$ temperature coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = 250\text{ }\mu\text{A}$	-	-6	-	
Gate-source threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2	-	4	V
Gate-source leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$	-	-	100	nA
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 80\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	μA
		$V_{DS} = 80\text{ V}, V_{GS} = 0\text{ V}, T_J = 70\text{ }^\circ\text{C}$	-	-	10	
On-state drain current ^a	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	115	-	-	A
Drain-source on-state resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 20\text{ A}$	-	0.0051	0.0062	Ω
		$V_{GS} = 10\text{ V}, I_D = 15\text{ A}$	-	0.0070	0.0090	
Forward transconductance ^a	g_{fs}	$V_{DS} = 10\text{ V}, I_D = 20\text{ A}$	-	70	-	S
Dynamic ^b						
Input capacitance	C_{ISS}	$V_{DS} = 50\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	-	8010	-	μF
Output capacitance	C_{OSS}		-	1650	-	
Reverse transfer capacitance	C_{RSS}		-	206	-	
Total gate charge	Q_g	$V_{DS} = 50\text{ V}, V_{GS} = 10\text{ V}, I_D = 20\text{ A}$	-	46	-	nC
Gate-source charge	Q_{gs}		-	10	-	
Gate-drain charge	Q_{gd}		-	5	-	
Output charge	Q_{OSS}		$V_{DS} = 50\text{ V}, V_{GS} = 0\text{ V}$	-	23	
Gate resistance	R_g	$f = 1\text{ MHz}$	0.5	1.2	2.1	Ω
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 50\text{ V}, R_L = 2.5\text{ }\Omega, I_D \cong 20\text{ A},$ $V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$	-	11	-	ns
Rise time	t_r		-	15	-	
Turn-off delay time	$t_{d(off)}$		-	26	-	
Fall time	t_f		-	8	-	
Drain-Source Body Diode Characteristics						
Continuous source-drain diode current	I_S	$T_C = 25\text{ }^\circ\text{C}$	-	-	115	A
Pulse diode forward current ($t_p = 100\text{ }\mu\text{s}$)	I_{SM}		-	-	460	
Body diode voltage	V_{SD}	$I_S = 5\text{ A}, V_{GS} = 0\text{ V}$	-	0.7	1.2	V
Body diode reverse recovery time	t_{rr}	$I_F = 20\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$	-	45	100	ns
Body diode reverse recovery charge	Q_{rr}		-	52	103	nC
Reverse recovery fall time	t_a		-	20	-	ns
Reverse recovery rise time	t_b		-	17	-	

Notes

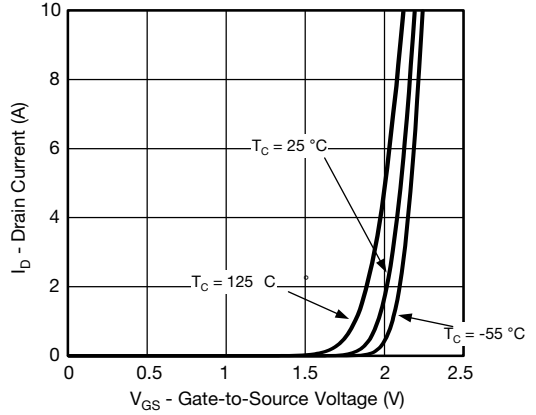
- a.P ulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

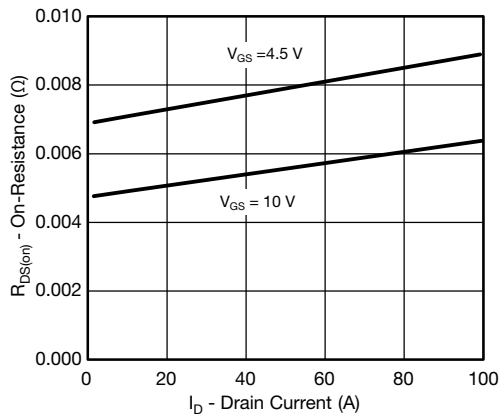
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



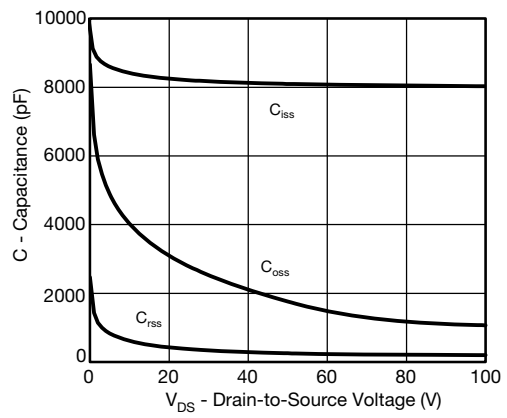
Output Characteristics



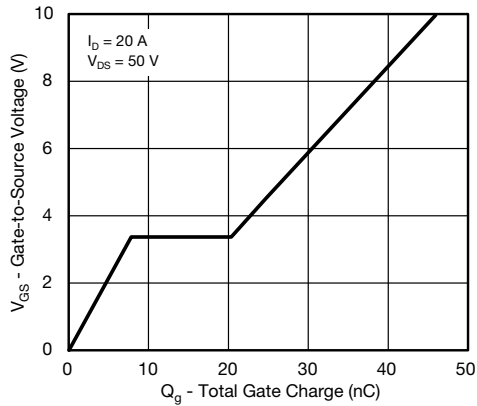
Transfer Characteristics



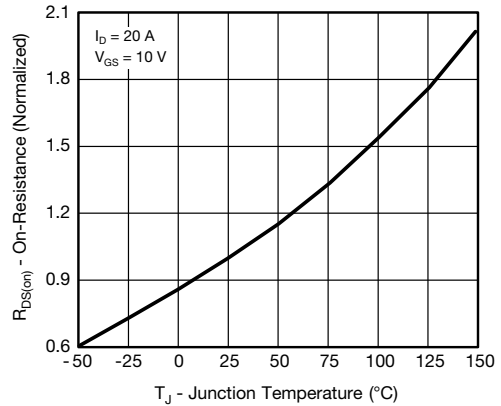
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

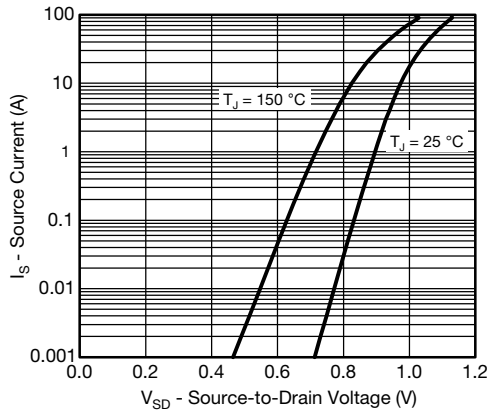


Gate Charge

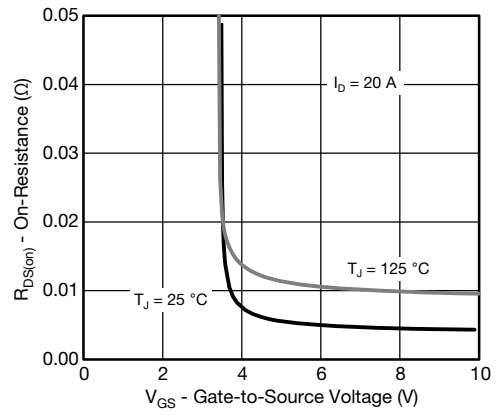


On-Resistance vs. Junction Temperature

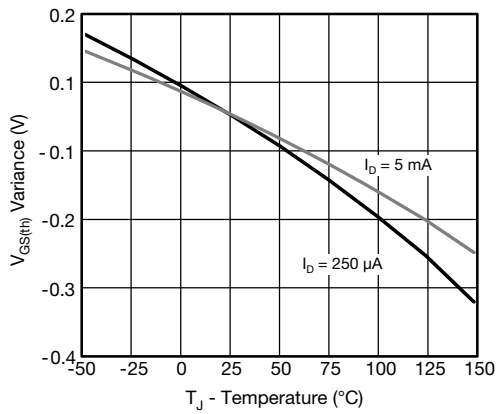
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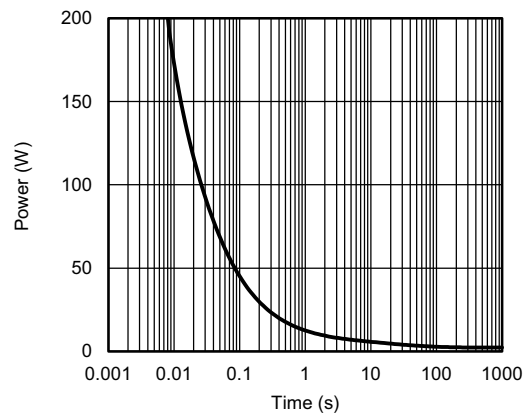
Source-Drain Diode Forward Voltage



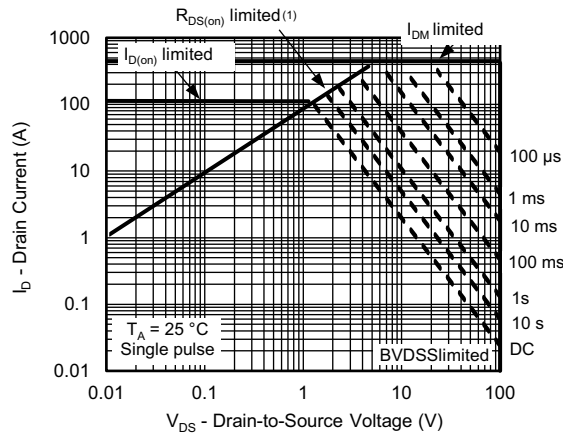
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



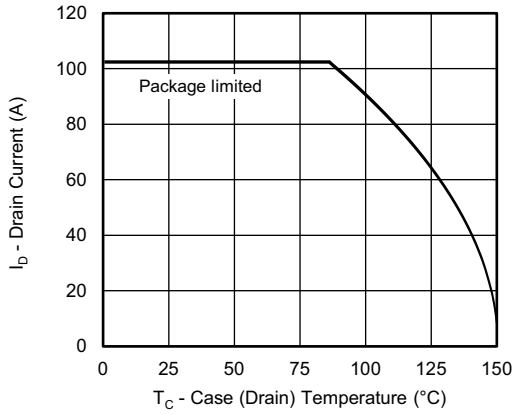
Single Pulse Power, Junction-to-Ambient



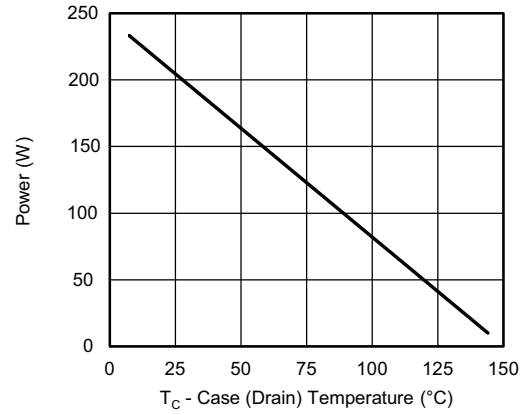
(1) $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating ^a

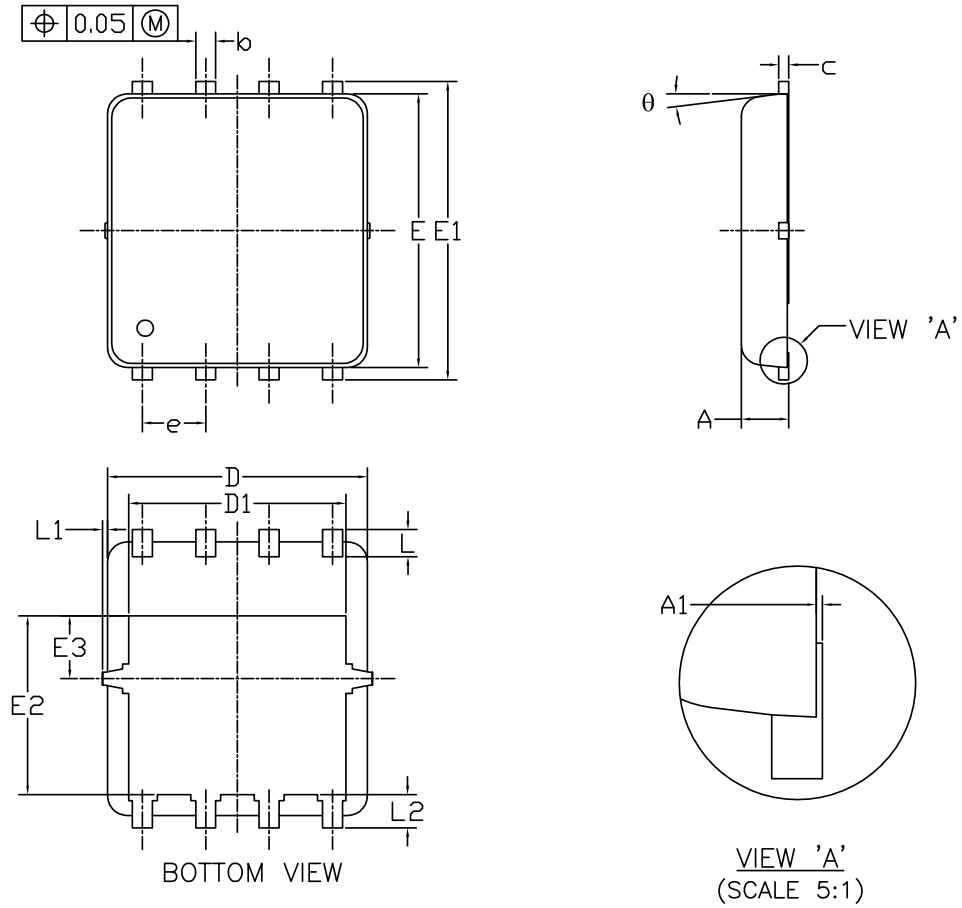


Power, Junction-to-Case

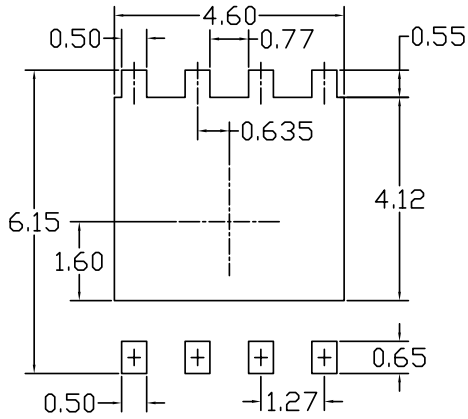
Note

- a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit

DFN5x6_8L_EP1_P PACKAGE OUTLIN



RECOMMENDED LAND PATTERN



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.85	0.95	1.00	0.033	0.037	0.039
A1	0.00	---	0.05	0.000	---	0.002
b	0.30	0.40	0.50	0.012	0.016	0.020
c	0.15	0.20	0.25	0.006	0.008	0.010
D	4.80	5.20	5.30	0.201	0.205	0.209
D1	4.25	4.35	4.45	0.167	0.171	0.175
E	5.45	5.55	5.65	0.215	0.219	0.222
E1	5.95	6.05	6.15	0.234	0.238	0.242
E2	3.525	3.625	3.725	0.139	0.143	0.147
E3	1.175	1.275	1.375	0.046	0.050	0.054
e	1.27 BSC			0.050 BSC		
L	0.45	0.55	0.65	0.018	0.022	0.026
L1	0	---	0.15	0	---	0.006
L2	0.68 REF			0.027 REF		
θ	0°	---	10°	0°	---	10°

NOTE

UNIT: mm

- PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
- CONTROLLING DIMENSION IS MILLIMETER.
CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

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