

## Dual N-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>d</sup>	Q <sub>g</sub> (Typ.)
40	0.0145 at V <sub>GS</sub> = 10 V	28	17 nC
	0.0185 at V <sub>GS</sub> = 4.5 V	20	

### FEATURES

- DT-Trench Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested

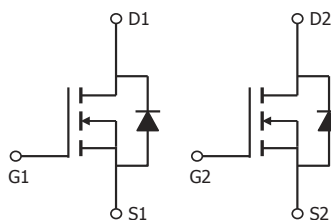
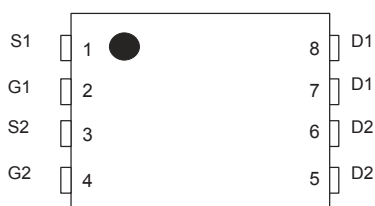


**RoHS**  
COMPLIANT

### APPLICATIONS

- Backlight inverter for LCD displays
- DC/DC converter

Top View



ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted				
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	40	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 25 °C	I <sub>D</sub>	28 <sup>a</sup>	A
	T <sub>C</sub> = 70 °C		20	
	T <sub>A</sub> = 25 °C		14 <sup>b, c</sup>	
	T <sub>A</sub> = 70 °C		8 <sup>b, c</sup>	
Pulsed Drain Current		I <sub>DM</sub>	110	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	I <sub>S</sub>	28	
	T <sub>A</sub> = 25 °C		13 <sup>b, c</sup>	
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	26	mJ
Single-Pulse Avalanche Energy		E <sub>AS</sub>	58	
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	P <sub>D</sub>	23	W
	T <sub>C</sub> = 70 °C		14.7	
	T <sub>A</sub> = 25 °C		1.7 <sup>b, c</sup>	
	T <sub>A</sub> = 70 °C		1.1 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	38	50	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	5	10	

Notes:

- Package limited.
- Surface mounted on 1" x 1" FR4 board.
- t = 10 s.
- Maximum under Steady State conditions is 85 °C/W.

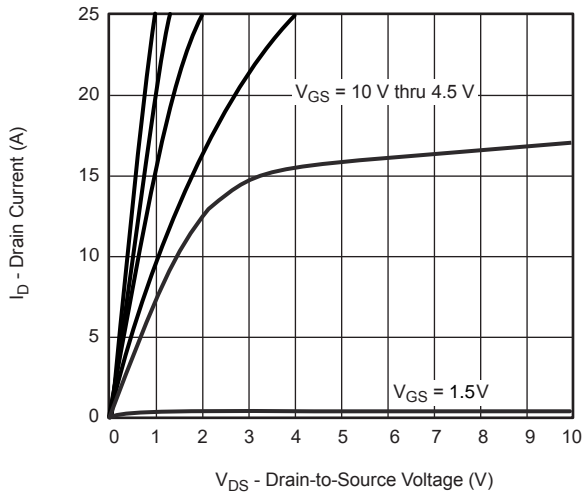
<b>SPECIFICATIONS</b> $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	40			V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$		55		mV/ $^\circ\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			-6.3		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1		3	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 40\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 32\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			10	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	36			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 10\text{ A}$		0.0145	0.017	$\Omega$
		$V_{GS} = 4.5\text{ V}, I_D = 5\text{ A}$		0.0185	0.025	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 5\text{ V}, I_D = 10\text{ A}$		40		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 20\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		1005		pF
Output Capacitance	$C_{oss}$			112		
Reverse Transfer Capacitance	$C_{rss}$			31		
Total Gate Charge	$Q_g$	$V_{DS} = 20\text{ V}, V_{GS} = 10\text{ V}, I_D = 10\text{ A}$		17		nC
Gate-Source Charge	$Q_{gs}$			4.5		
Gate-Drain Charge	$Q_{gd}$			6.8		
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		3.0		$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 20\text{ V}, R_L = 5.4\text{ }\Omega$ $I_D \cong 10\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		10		ns
Rise Time	$t_r$			28		
Turn-Off Delay Time	$t_{d(off)}$			45		
Fall Time	$t_f$			23		
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$			28	A
Pulse Diode Forward Current <sup>a</sup>	$I_{SM}$				110	
Body Diode Voltage	$V_{SD}$	$I_S = 2\text{ A}$		0.7	1.2	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 5.5\text{ A}, dI/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		25		ns
Body Diode Reverse Recovery Charge	$Q_{rr}$				55	

Notes:

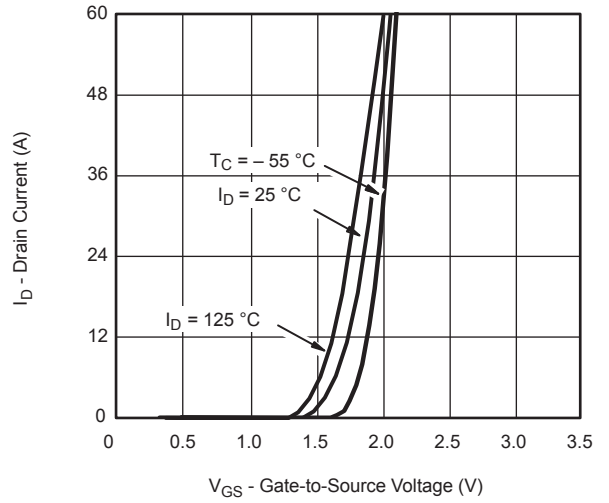
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

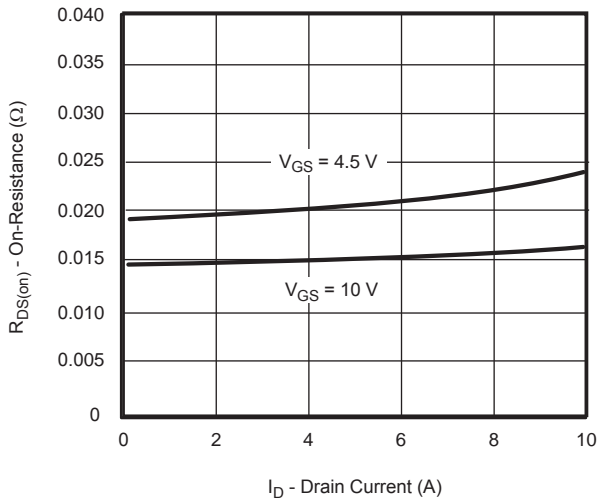
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



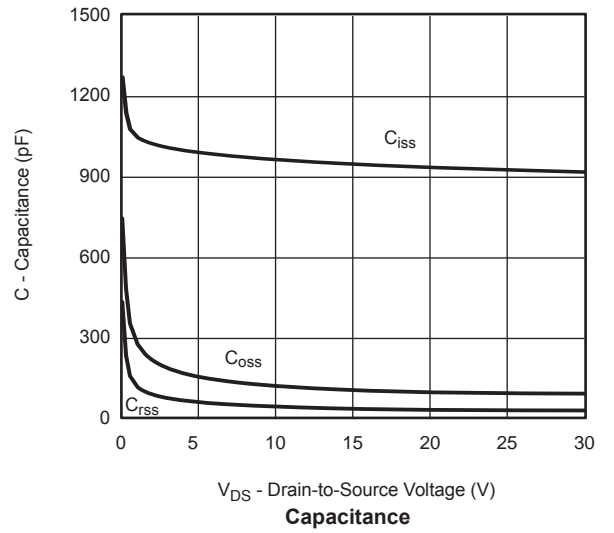
**Output Characteristics**



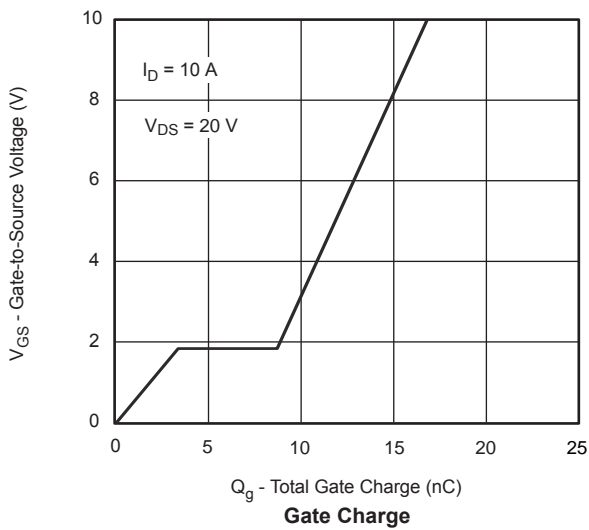
**Transfer Characteristics**



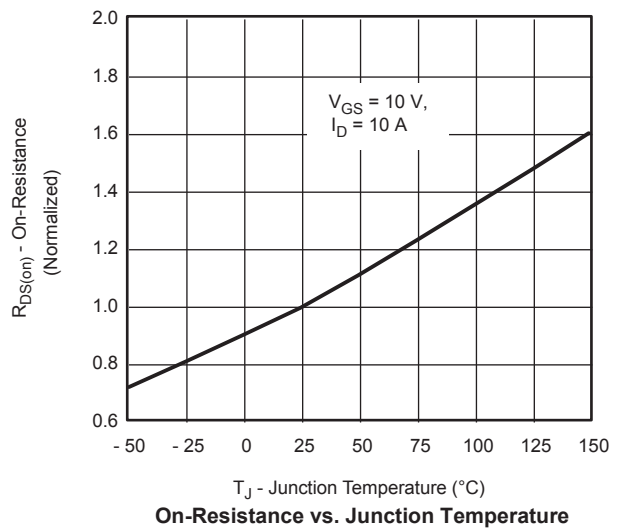
**On-Resistance vs. Drain Current**



**Capacitance**

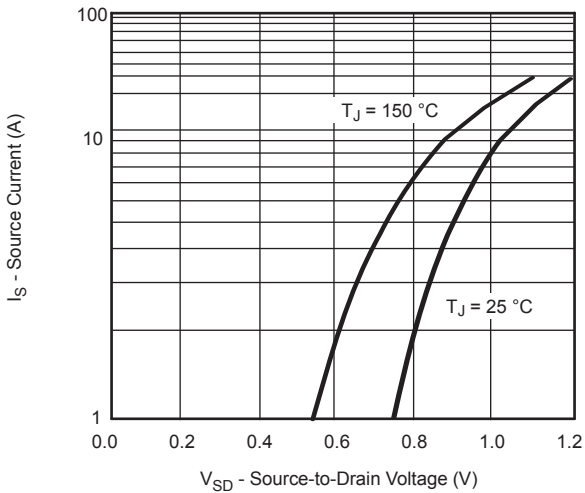


**Gate Charge**

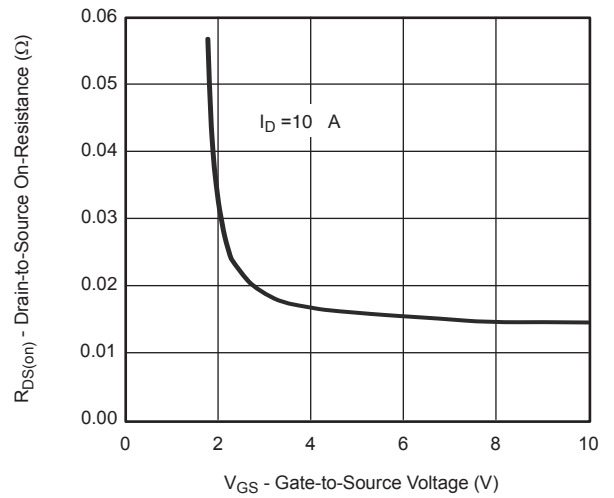


**On-Resistance vs. Junction Temperature**

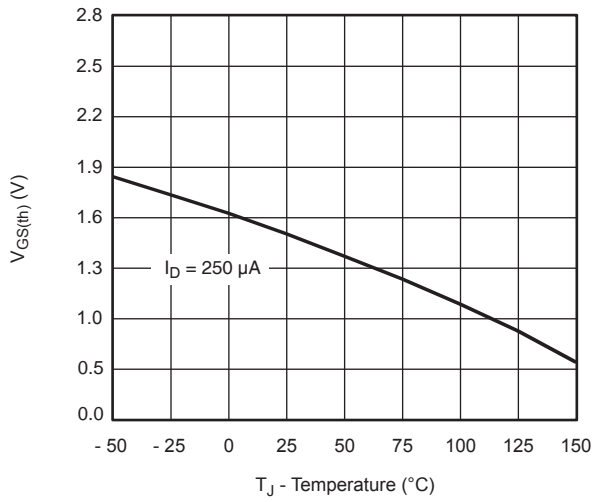
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



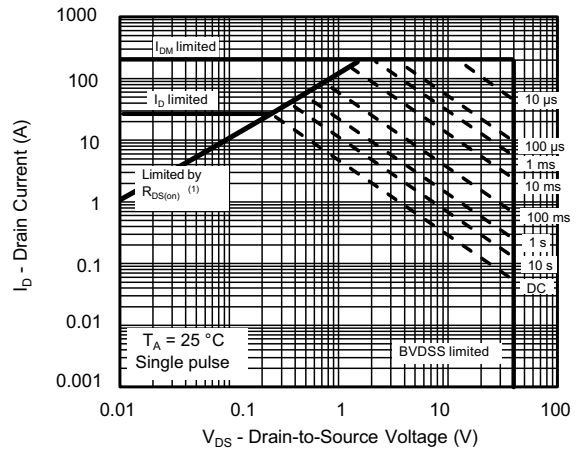
**Source-Drain Diode Forward Voltage**



**On-Resistance vs. Gate-to-Source Voltage**

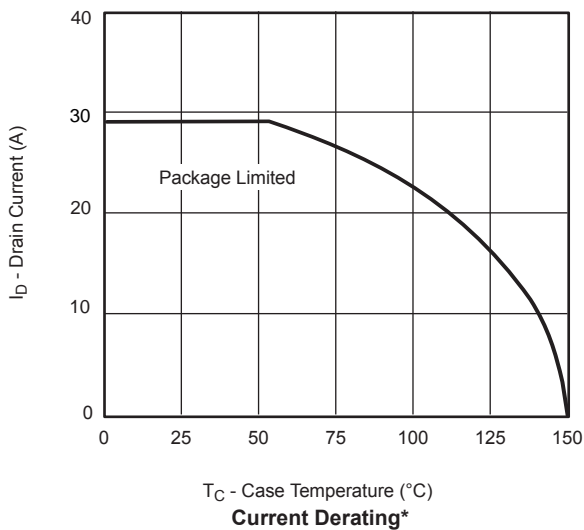


**Threshold Voltage**

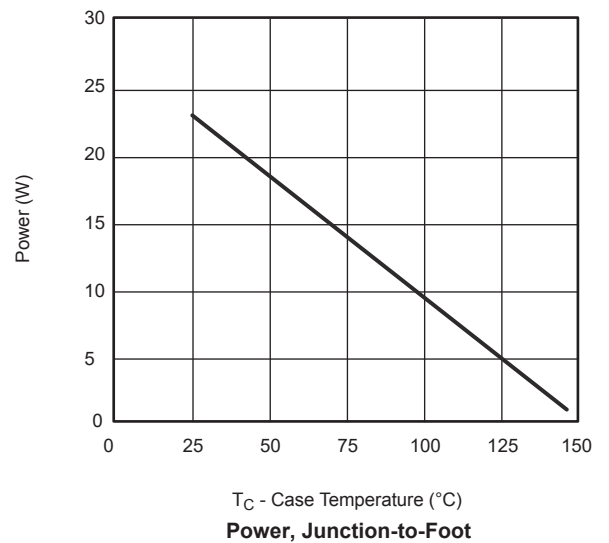


<sup>(1)</sup>  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

**Safe Operating Area, Junction-to-Ambient**

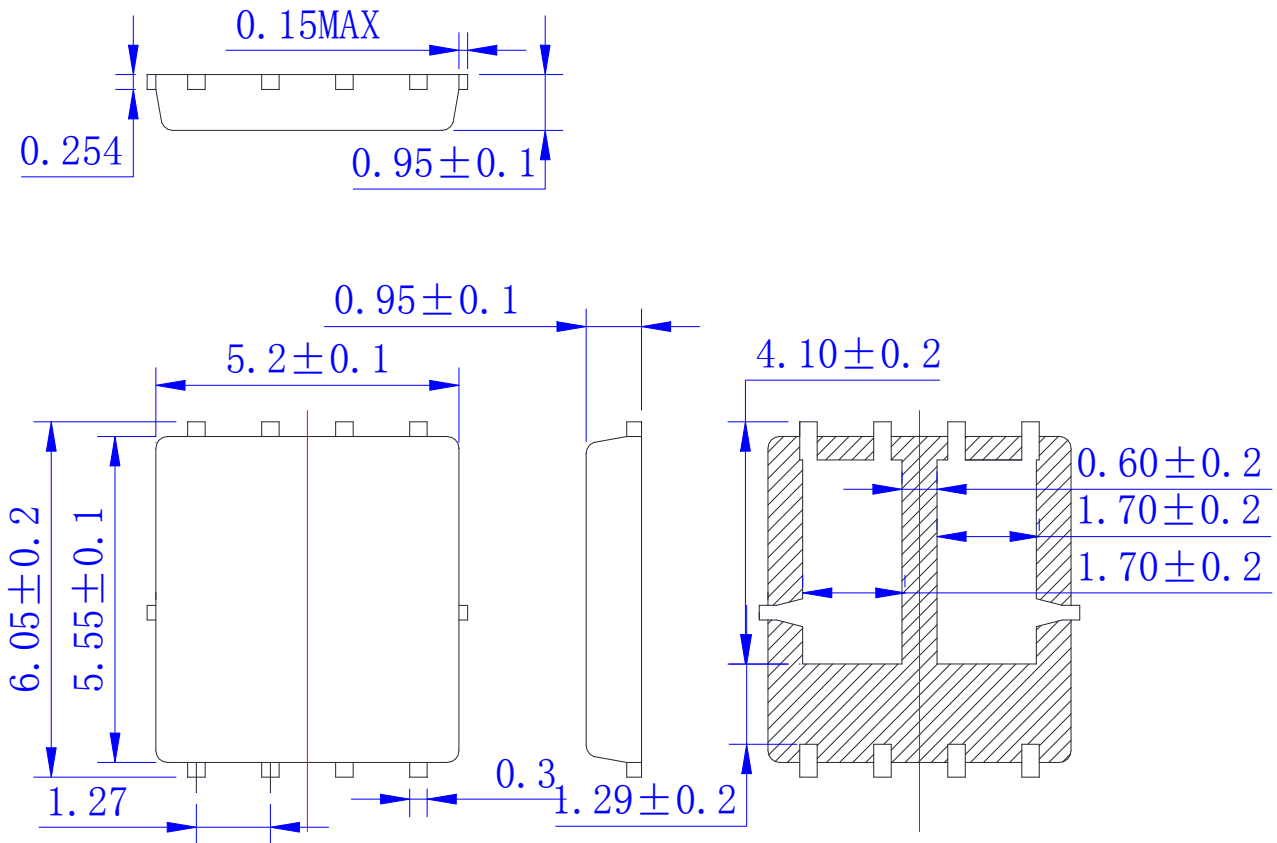


**Current Derating\***



**Power, Junction-to-Foot**

### DFN5X6 PACKAGE OUTLINE DIMENSIONS



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